

600V Super-Junction Power MOSFET

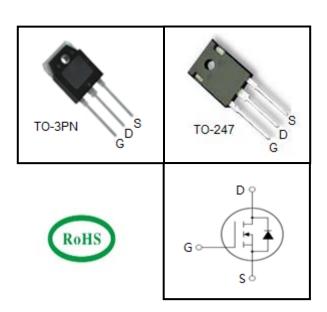
FEATURES

- Very low FOM R_{DS(on)}×Q_g
- 100% avalanche tested
- RoHS compliant
- Fast Body Diode

APPLICATIONS

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)

Device Marking and Package Information			
Device	Package	Marking	
TPV60R080CFD	TO-3PN	60R080CFD	
TPW60R080CFD	TO-247	60R080CFD	



Absolute Maximum Ratings $T_c = 25^{\circ}C$, unless otherwise noted				
Parameter		Symbol	Value	Unit
Drain-Source Voltage ($V_{GS} = 0V$)		V _{DSS}	600	V
Continuous Drain Current		Ι _D	47	A
Pulsed Drain Current	(note1)	I _{DM}	141	A
Gate-Source Voltage		V _{GSS}	±30	V
Single Pulse Avalanche Energy	(note2)	E _{AS}	1120	mJ
Avalanche Current	(note1)	I _{AR}	15	A
Repetitive Avalanche Energy	(note1)	E _{AR}	1.76	mJ
Power Dissipation ($T_C = 25^{\circ}C$)		P _D	390	w
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55~+150	°C

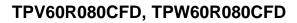
Thermal Resistance			
Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case	R _{thJC}	0.32	K/W
Thermal Resistance, Junction-to-Ambient	R _{thJA}	62	r\/ VV



Specifications $T_J = 25^{\circ}C$, ur	less othe	rwise noted				
Parameter	Symbol Test Conditions		Value			
		Min.	Тур.	Max.	Unit	
Static		•				
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0V, I_{D} = 250\mu A$	600			V
	I _{DSS}	$V_{DS} = 600V, V_{GS} = 0V, T_{J} = 25^{\circ}C$			5	μA
Zero Gate Voltage Drain Current		$V_{DS} = 600V, V_{GS} = 0V, T_{J} = 150^{\circ}C$			100	
Gate-Source Leakage	I _{GSS}	$V_{GS} = \pm 30V$			±100	nA
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \mu A$	2.5		4.5	V
Drain-Source On-Resistance (Note3)	R _{DS(on)}	V _{GS} = 10V, I _D = 20A		65	80	mΩ
Forward Transconductance (Note3)	g _{fs}	V _{DS} = 10V, I _D = 20A		40		S
Dynamic				•		
Input Capacitance	C _{iss}	$\lambda = 0 \lambda$		5100		
Output Capacitance	C _{oss}	$V_{GS} = 0V,$ $V_{DS} = 50V,$		225		pF
Reverse Transfer Capacitance	C _{rss}	f = 1.0MHz		6.2		
Total Gate Charge	Q _g	V _{DD} = 480V, I _D = 47A, V _{GS} = 10V		90		nC
Gate-Source Charge	Q _{gs}			24		
Gate-Drain Charge	Q _{gd}			30		
Turn-on Delay Time	t _{d(on)}	V_{DD} = 400V, I_D = 26A, R_G = 1.7 Ω		16		
Turn-on Rise Time	t _r			12		
Turn-off Delay Time	t _{d(off)}			83		ns
Turn-off Fall Time	t _f			5		
Drain-Source Body Diode Characteri	stics					
Continuous Body Diode Current	I _S	T _C = 25°C			47	^
Pulsed Diode Forward Current	I _{SM}				141	- A
Body Diode Voltage	V _{SD}	$T_{J} = 25^{\circ}C, I_{SD} = 47A, V_{GS} = 0V$		0.9	1.2	V
Reverse Recovery Time	t _{rr}	V _R = 400V, I _F = 26A, di _F /dt = 100A/µs		180		ns
Reverse Recovery Charge	Q _{rr}			1		μC
Peak Reverse Recovery Current	I _{rrm}	- F		10		А

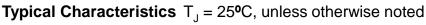
Notes

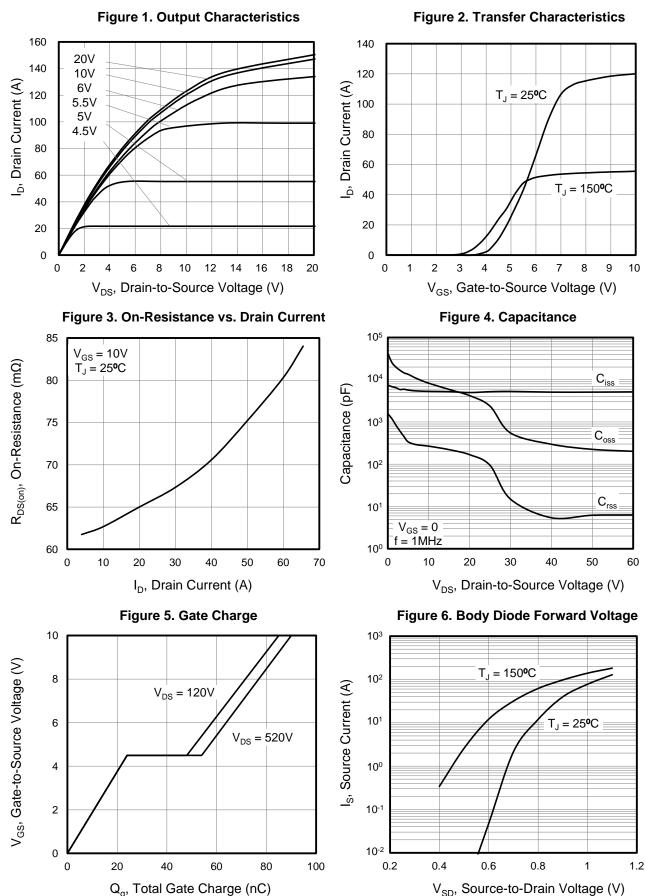
- 1. Repetitive Rating: Pulse Width limited by maximum junction temperature
- 2. I_{AS} = 15A, V_{DD} = 50V, R_{G} = 25 Ω , Starting T_{J} = 25°C
- 3. Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 1%



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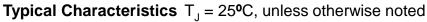


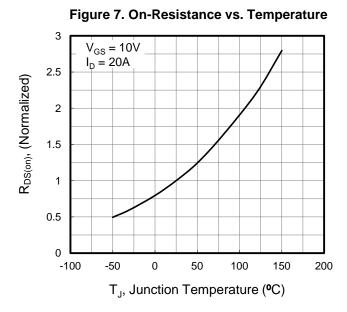


TPV60R080CFD, TPW60R080CFD



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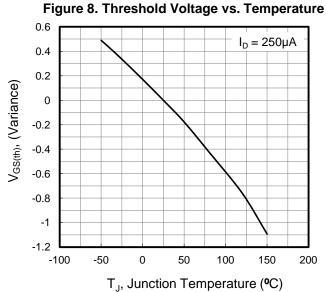
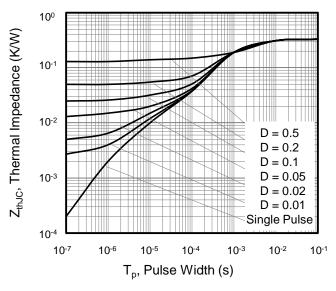
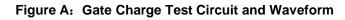


Figure 9. Transient Thermal Impedance





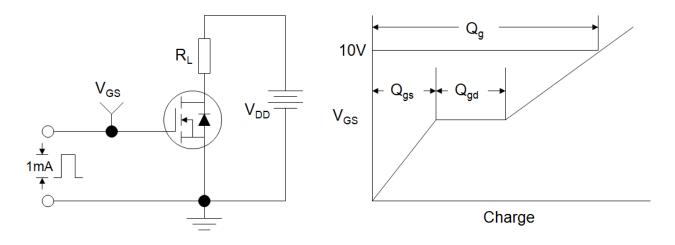


Figure B: Resistive Switching Test Circuit and Waveform

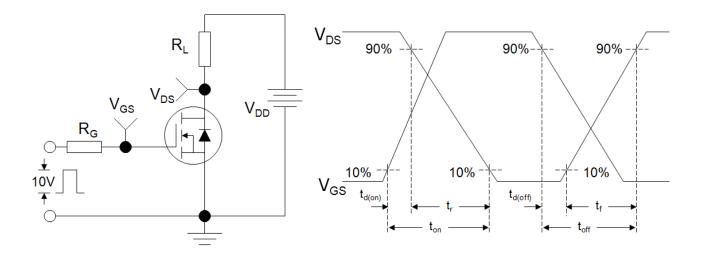
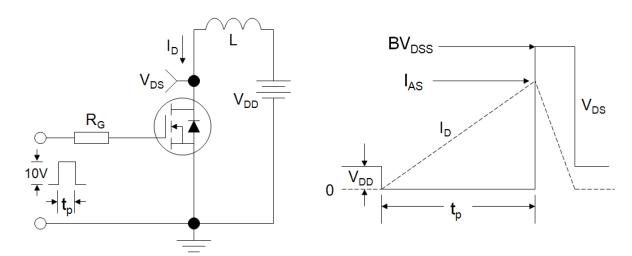


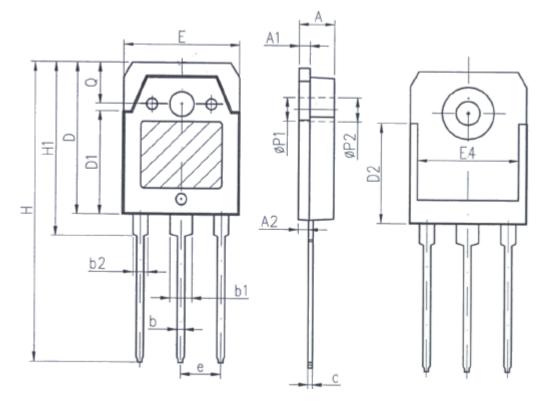
Figure C: Unclamped Inductive Switching Test Circuit and Waveform



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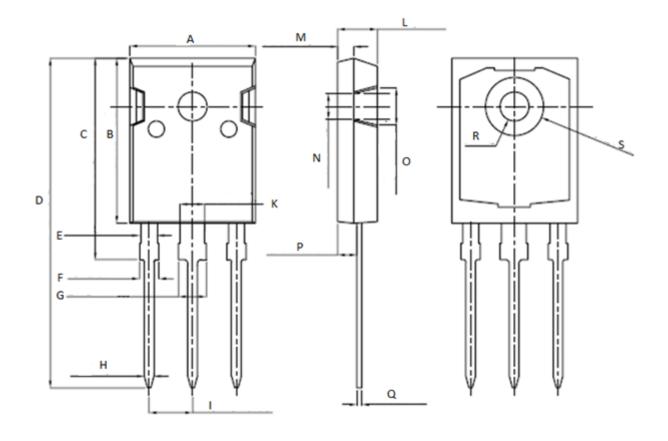
TO-3PN



Unit:mm				
Symbol	Min.	Max.		
Α	4.6	5		
A1	1.4	1.65		
A2	1.18	1. 58		
b	0.8	1.2		
b1	2.8	3.2		
b2	1.8	2.2		
c	0.5	0.75		
D	19.6	20.2		
D1	13.55	14. 25		
D2	12. 9REF			
E	15.35	15.85		
E4	12.6	-		
е	5. 45TYP			
Н	40.1	40.9		
H1	23.15	23. 65		
P1	3. 2REF			
P2	3. 5REF			



TO-247



Unit: mm			
Symbol	Min.	Max.	
Α	15.95	16. 25	
В	20.85	21.25	
С	20.95	21.35	
D	40.5	40.9	
E	1.9	2. 1	
F	2.1	2.25	
G	3. 1	3. 25	
Н	1.1	1.3	
I	5.40	5.50	

Unit: mm				
Symbol	Min.	Max.		
K	2.90	3.10		
L	4.90	5.30		
М	1.90	2.10		
Ν	4. 50	4. 70		
0	5.40	5.60		
Р	2. 29	2. 49		
Q	0. 51	0. 71		
R	φ3.5	φ3.7		
S	φ7.1	φ7.3		



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