

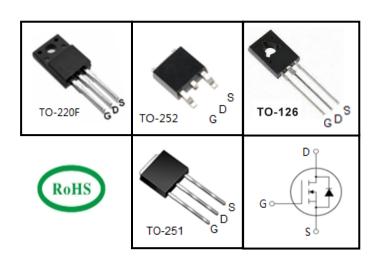
# **600V N-Channel MOSFET**

#### **FEATURES**

- Fast switching
- 100% avalanche tested
- Improved dv/dt capability

#### **APPLICATIONS**

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)



Device Marking and Package Information					
Device	TMA2N60H	TMD2N60H	TMT2N60H	TMU2N60H	
Package	TO-220F	TO-252	TO-126	TO-251	
Marking	A2N60H	D2N60H	T2N60H	U2N60H	

<b>Absolute Maximum Ratings</b> T <sub>C</sub> = 25°C, unles <sup>G</sup> otherwise noted						
Barranadar	Complete al	Value			11	
Parameter	Symbol	TO-220F	TO-252	TO-251	TO-126	Unit
Drain-Source Voltage (V <sub>GS</sub> = 0V)	V <sub>DSS</sub>	600			V	
Continuous Drain Current	I <sub>D</sub>	2			А	
Pulsed Drain Current (note1)	I <sub>DM</sub>	8			А	
Gate-Source Voltage	V <sub>GSS</sub>	±30			V	
Single Pulse Avalanche Energy (note2)	E <sub>AS</sub>	57		mJ		
Avalanche Current (note1)	I <sub>AR</sub>	2.4		А		
Repetitive Avalanche Energy (note1)	E <sub>AR</sub>	6.4		mJ		
Power Dissipation (T <sub>C</sub> = 25°C)	P <sub>D</sub>	20		25		W
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55~+150			°C	

Thermal Resistance						
Baramatan	Comple of	Value			1114	
Parameter	Symbol	TO-220F	TO-251	TO-252	TO-126	Unit
Thermal Resistance, Junction-to-Case	R <sub>thJC</sub>	6.25 5		00.00		
Thermal Resistance, Junction-to-Ambient	R <sub>thJA</sub>	62.5			°C/W	

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## TMA2N60H,TMD2N60H,TMT2N60H,TMU2N60H

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D	0		Value				
Parameter	Symbol Test Conditions		Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	600			V	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = 600V, V_{GS} = 0V, T_{J} = 25^{\circ}C$			1	μΑ	
Gate-Source Leakage	I <sub>GSS</sub>	$V_{GS} = \pm 30V$			±100	nA	
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	3.0		4.0	V	
Drain-Source On-Resistance (Note3)	R <sub>DS(on)</sub>	$V_{GS} = 10V, I_{D} = 1.0A$		3.5	4.2	Ω	
Dynamic							
Input Capacitance	C <sub>iss</sub>	V 0V		310			
Output Capacitance	C <sub>oss</sub>	$V_{GS} = 0V,$ $V_{DS} = 25V,$		39		pF	
Reverse Transfer Capacitance	C <sub>rss</sub>	f = 1.0MHz		6			
Total Gate Charge	$Q_g$			8.0			
Gate-Source Charge	$Q_{gs}$	$V_{DD} = 480V, I_{D} = 2.0A,$ $V_{GS} = 10V$		1.2		nC	
Gate-Drain Charge	$Q_{gd}$	65		5.0			
Turn-on Delay Time	t <sub>d(on)</sub>			7.8			
Turn-on Rise Time	t <sub>r</sub>	$V_{DD} = 250V, I_{D} = 2.0A,$		33			
Turn-off Delay Time	t <sub>d(off)</sub>	$R_G = 25 \Omega$		23		ns	
Turn-off Fall Time	t <sub>f</sub>			59			
Drain-Source Body Diode Character	istics			•	•		
Continuous Body Diode Current	I <sub>s</sub>	T 0500			2	,	
Pulsed Diode Forward Current	I <sub>SM</sub>	T <sub>C</sub> = 25 °C			8	Α	
Body Diode Voltage	V <sub>SD</sub>	$T_J = 25^{\circ}\text{C}, I_{SD} = 2.0\text{A}, V_{GS} = 0\text{V}$			1.4	V	
Reverse Recovery Time	t <sub>rr</sub>	$V_{GS} = 0V, I_{S} = 2.0A,$		80		ns	
Reverse Recovery Charge	Q <sub>rr</sub>	$di_{F}/dt = 100A /\mu s$		1.8		μC	

#### **Notes**

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature
- 2.  $I_{AS}$  = 2.4A,  $V_{DD}$  = 50V,  $R_{G}$  = 25  $\Omega$ , Starting  $T_{J}$  = 25  $^{\circ}C$
- 3. Pulse Test: Pulse width ≤ 300µs, Duty Cycle ≤ 1%



## **Typical Characteristics** $T_J = 25^{\circ}\text{C}$ , unless otherwise noted

Figure 1. Output Characteristics (T<sub>J</sub> = 25°C)

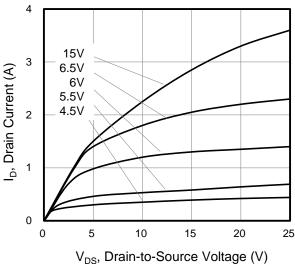
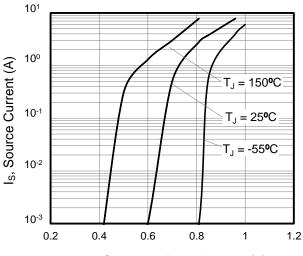


Figure 2. Body Diode Forward Voltage



V<sub>SD</sub>, Source-to-Drain Voltage (V)

Figure 3. Drain Current vs. Temperature

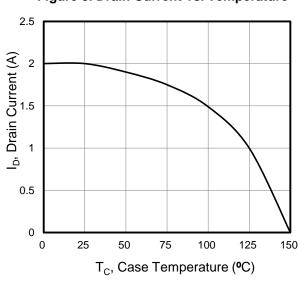


Figure 4. Power Dissipation vs. Temperature TO-251,TO-252

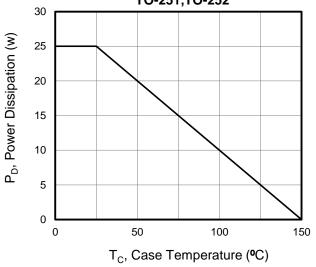


Figure 5. Transfer Characteristics

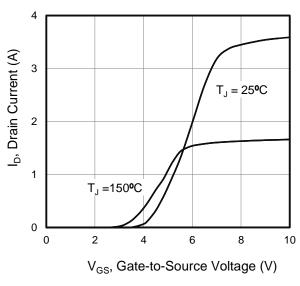
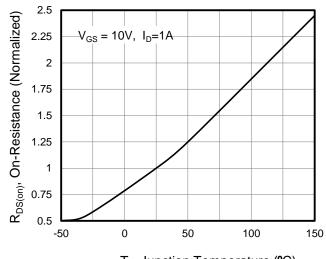


Figure 6. On-Resistance vs. Temperature



T<sub>J</sub>, Junction Temperature (°C)



## **Typical Characteristics** $T_J = 25^{\circ}\text{C}$ , unless otherwise noted

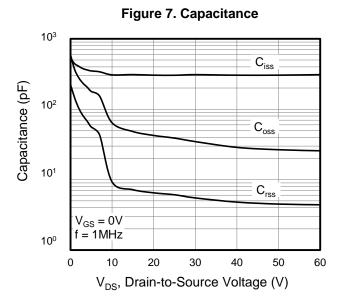


Figure 8. Gate Charge

10
(A) 8
8
6
(B) V<sub>DD</sub> = 120V
(C) V<sub>DD</sub> = 300V
(C) V<sub>DD</sub> = 480V
(C)

TO-252,TO-251,TO-126

101

(MX)

D = 0.5

D = 0.2

D = 0.1

D = 0.05

D = 0.02

D = 0.01

Single Pulse

10-3

10-7

10-6

10-5

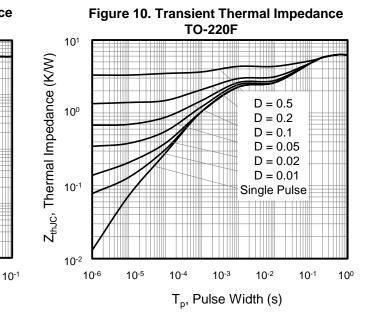
10-4

T<sub>p</sub>, Pulse Width (s)

10-3

10-2

Figure 9. Transient Thermal Impedance



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Figure A: Gate Charge Test Circuit and Waveform

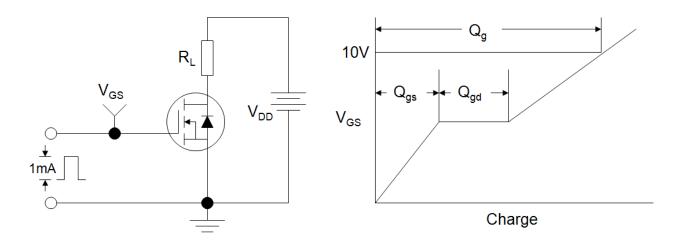


Figure B: Resistive Switching Test Circuit and Waveform

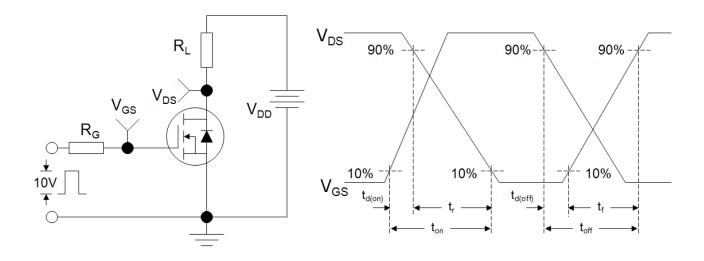
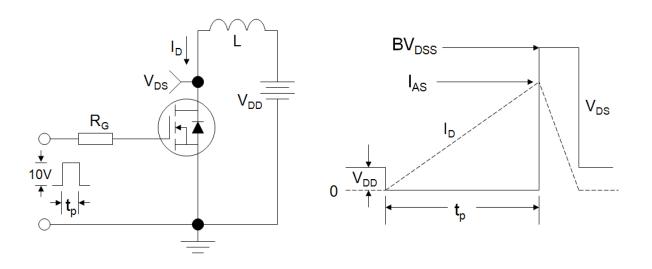
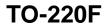
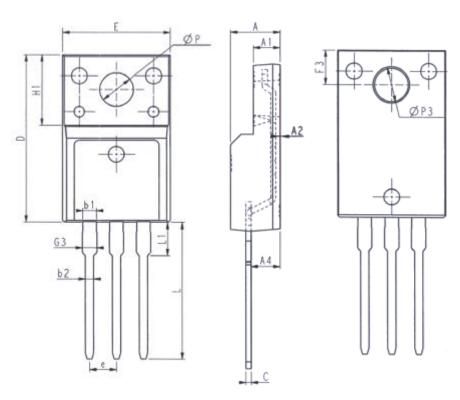


Figure C: Unclamped Inductive Switching Test Circuit and Waveform

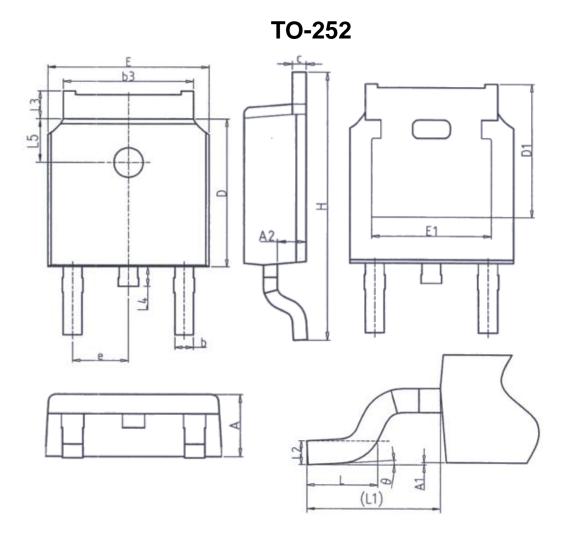


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Unit: mm			l	Jnit: mn	1
Symbol	Min.	Max.	Symbol	Min.	Max.
E	9. 96	10. 36	L	12. 68	13. 28
Α	4. 50	4. 90	L1	2. 93	3. 13
A1	2. 34	2. 74	Р	3. 03	3. 38
A2	0. 30	0. 60	Р3	3. 15	3. 65
A4	2. 56	2. 96	F3	3. 15	3. 45
С	0. 40	0. 65	G3	1. 25	1. 55
D	15. 57	16. 17	b1	1. 18	1. 43
H1	6. 70REF		b2	0. 70	0. 95
е	2. 54BSC				

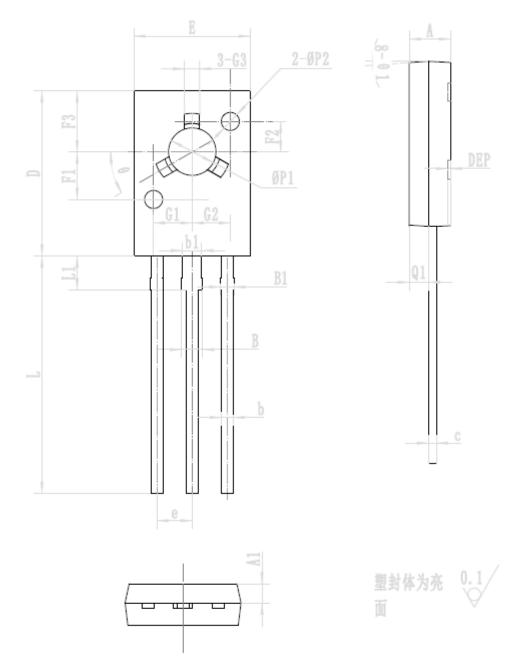


Unit: mm					
Symbol	Min.	Max.			
Α	2. 20	2. 40			
A1	0.00	0. 20			
A2	0. 97	1. 17			
b	0. 68	0. 90			
b3	5. 20	5. 50			
С	0. 43	0. 63			
D	5. 98	6. 22			
D1	5. 30REF				
E	6. 40	6. 80			
E1	4. 63	_			

Unit: mm					
Symbol	Min. Max.				
е	2. 286BSC				
Н	9. 40	10.50			
L	1. 38	1. 75			
L1	2. 90REF				
L2	0. 51	IBSC			
L3	0.88	1. 28			
L4	_	1.00			
L5	1. 65	1. 95			
θ	0°	8°			

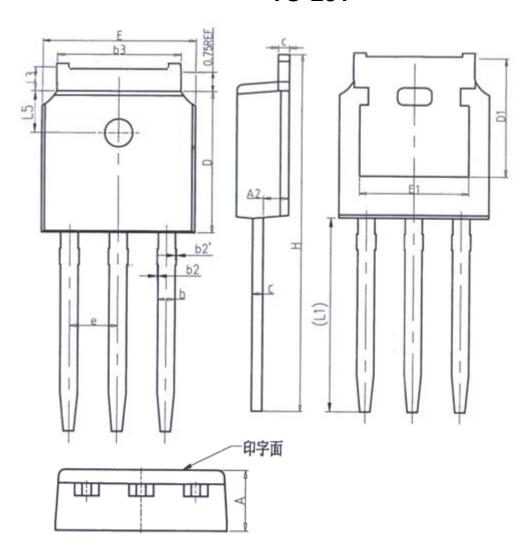


# **TO-126**



	MM				
SYMBOL	MIN	NOM	MAX		
*A	2.60	2.70	2.80		
A1	1.22	1.27	1.32		
*b	0.71	0.76	0.81		
b1	1.22	1.27	1.32		
*B	1.27	1.37	1.45		
*B1	0.71	0.76	88.0		
*c	0.38	0.40	0.45		
*D	10.70	10.80	10.90		
*E	7.50	7.60	7.70		
F1	2.90	3.00	3.10		
F2	1.90	2.00	2.10		
F3	3.90	4.00	4.10		
G1	2.40	2.50	2.60		
G2	2.40	2.50	2.60		
G3	0.90	1.00	1.10		
*e	2.24	2.29	2.34		
*L	15.30	15.50	15.70		
L1	2.10	2.20	2.30		
Q1	1.17	1.27	1.37		
θ	27°	30°	33°		
θ1	1°	4°	7°		
<b>*</b> ФР1	3.00	3.10	320		
ΦP2	1.15	1.20	1.25		
DEP	0.10	0.15	0.20		
带*为检验尺寸					





Unit: mm					
Symbol	Min.	Max.			
Α	2. 20	2. 40			
A2	0. 97	1. 17			
b	0. 68	0.90			
b2	0.00	0.10			
b2′	0.00	0.10			
b3	5. 20	5. 50			
С	0. 43	0. 63			
D	5. 98	6. 22			

Unit: mm					
Symbol	Min.	Max.			
D1	5. 30REF				
E	6. 40	6. 80			
E1	4. 63	_			
е	2. 286BSC				
Н	16. 22	16. 82			
L1	9. 15	9. 65			
L3	0.88	1. 28			
L5	1. 65	1. 95			



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