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## DESCRIPTION

The RS2596 is Monolithic IC that design for a step－down DC／DC Converter，and own the ability of driving a 3A load without additional transistor component．

The output version included $3.3 \mathrm{~V}, 5 \mathrm{~V}, 12 \mathrm{~V}$ and an adjustable type．It operates at a switching frequency of 150 KHz thus allowing smaller sized filter components than what would be needed with lower frequency switching regulators．Other features include a guaranteed $4 \%$ tolerance on output voltage under specified input voltage and output load conditions，and $15 \%$ on the oscillator frequency．Regarding protected function，thermal shutdown is to prevent over temperature operating from damage，and current limit is against over current operating of the output switch．

## FEATURES

－ $3.3 \mathrm{~V}, 5 \mathrm{~V}, 12 \mathrm{~V}$ and adjustable
－Adjustable version output voltage range： $1.23-37 \mathrm{~V}$
－ $4 \%$ max over line and load conditions
－ $150 \mathrm{KHz} 15 \%$ fixed switching frequency
－TTL shutdown capability
－Operating voltage can be up to 40 V
－Output load current：3A
－TO220－5，TO263－5 packages
－Low power standby mode
－Thermal－shunt down and current－limit protection
－Built－in switching a transistor on chip，requires only 4 external components

## APPLICATIONS

－Simple High－efficiency step－down regulator
－Positive to negative converter
－On－card switching regulators

## BLOCK DIAGRAM



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## APPLICATION CIRCUIT



## ORDER INFORMATION

| Device | Device Code |
| :---: | :--- |
|  | $\mathbf{X X}$ is nominal output voltage（for example，AD＝ADJ，33＝3．3V，50＝5．0V，12＝12V）． |
|  | $\mathbf{Y}$ is package designator： |
| U：TO－263－5 |  |
| RS2596－XX Y Z | E：TO－220－5 |
|  | $Z$ is Lead Free designator： |
|  | P：Commercial Standard，Lead（Pb）Free and Phosphorous（P）Free Package |
|  | G：Green（Halogen Free with Commercial Standard） |

## PIN CONFIGURATION

TO－263－5


TO－220－5


## PIN DESCRIPTION

| Pin Name | Description | Pin No． |  |
| :---: | :--- | :---: | :---: |
|  |  | TO－263－5 | TO－220－5 |
| VIN | Regulator Input Pin | 1 | 1 |
| VOUT | Regulator Output Pin | 2 | 2 |
| GND | Ground Pin | 3,6 | 3,6 |
| FB | Output Voltage Feed Back Control Pin | 4 | 4 |
| SD | ON／OFF Shutdown Pin | 5 | 5 |

## ABSOLUTE MAXIMUM RATINGS（NOTE1）

| Parameter | Symbol | Range | Units |
| :--- | :---: | :---: | :---: |
| Supply voltage | $\mathrm{V}_{\mathrm{CC}}$ | 40 | V |
| On／Off pin input voltage | $\mathrm{V}_{\mathrm{SD}}$ | $-0.3 \sim+25$ | V |
| Feedback pin voltage | $\mathrm{V}_{\mathrm{FB}}$ | $-0.3 \sim+25$ | V |
| Output voltage to ground | $\mathrm{V}_{\text {OUT }}$ | -1 | V |
| Power dissipation | $\mathrm{P}_{\mathrm{D}}$ | Internally Limited | W |
| Storage temperature | Tstg | $-40 \sim+150$ | ${ }^{\circ} \mathrm{C}$ |
| Operating junction temperature range | $\mathrm{T}_{\mathrm{J}}$ | $-40 \sim+125$ | ${ }^{\circ} \mathrm{C}$ |
| Operating voltage | $\mathrm{V}_{\mathrm{OP}}$ | $+4.5 \sim+40$ | V |

## ELECTRICAL CHARACTERISTICS（CONTINUED）

Specifications with boldface type apply over for full operating temperature range，the other type are for $\mathrm{T}_{\mathrm{J}}=25^{\circ} \mathrm{C}$（Note 2）

| Part No． | Parameter | Symbol | Conditions | Min． | Typ． （Note3） | Max． （Note4） | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| RS2596－3．3V | Output voltage | $\mathrm{V}_{\text {OUt }}$ | $5 \mathrm{~V} \leq \mathrm{V}_{\text {IN }} \leq 40 \mathrm{~V}, 0.2 \mathrm{~A} \leq \mathrm{L}_{\text {LOAD }} \leq 3 \mathrm{~A}$ | $\begin{aligned} & 3.168 \\ & 3.135 \\ & \hline \end{aligned}$ | 3.3 | $\begin{aligned} & 3.342 \\ & 3.465 \\ & \hline \end{aligned}$ | V |
|  | Efficiency | $\eta$ | $\mathrm{V}_{\text {IN }}=12 \mathrm{~V}, \mathrm{I}_{\text {LOAD }}=3 \mathrm{~A}$ | － | 72 | － | \％ |
| RS2596－5．0V | Output voltage | $V_{\text {OUt }}$ | $7 \mathrm{~V} \leq \mathrm{V}_{\text {IN }} \leq 40 \mathrm{~V}, 0.2 \mathrm{~A} \leq \mathrm{I}_{\text {LOAD }} \leq 3 \mathrm{~A}$ | $\begin{aligned} & 4.800 \\ & 4.750 \\ & \hline \end{aligned}$ | 5.0 | $\begin{aligned} & \hline 5.200 \\ & 5.250 \end{aligned}$ | V |
|  | Efficiency | $\eta$ | $\mathrm{V}_{\text {IN }}=12 \mathrm{~V}, \mathrm{I}_{\text {LOAD }}=3 \mathrm{~A}$ | － | 79 | － | \％ |
| RS2596－12V | Output voltage | $V_{\text {OUt }}$ | $15 \mathrm{~V} \leq \mathrm{V}_{\text {IN }} \leq 40 \mathrm{~V}, 0.2 \mathrm{~A} \leq \mathrm{I}_{\text {LOAD }} \leq 3 \mathrm{~A}$ | $\begin{aligned} & \hline 11.52 \\ & 11.40 \\ & \hline \end{aligned}$ | 12.0 | $\begin{aligned} & \hline 12.48 \\ & 12.60 \\ & \hline \end{aligned}$ | V |
|  | Efficiency | $\eta$ | $\mathrm{V}_{\text {IN }}=25 \mathrm{~V}, \mathrm{I}_{\text {LOAD }}=3 \mathrm{~A}$ | － | 90 | － | \％ |
| RS2596－ADJ | Reference voltage | $\mathrm{V}_{\text {FB }}$ | $4.5 \mathrm{~V} \leq \mathrm{V}_{\text {IN }} \leq 40 \mathrm{~V}, 0.2 \mathrm{~A} \leq \mathrm{I}_{\text {LOAD }} \leq 3 \mathrm{~A}$ <br> $\mathrm{V}_{\text {out }}$ programmed for 3 V | 1.193 | 1.230 | $\begin{aligned} & \hline 1.267 \\ & 1.280 \\ & \hline \end{aligned}$ | V |
|  | Efficiency | $\eta$ | $\mathrm{V}_{\text {IN }}=12 \mathrm{~V}, \mathrm{I}_{\text {LOAD }}=3 \mathrm{~A}$ | － | 72 | － | \％ |

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## ALL OUTPUT VOLTAGE VERSIONS ELECTRICAL CHARACTERISTICS

Specifications with boldface type apply over for full operating temperature range，the other type are for $\mathrm{T}_{\mathrm{J}}=25^{\circ} \mathrm{C}$ （Unless otherwise specified， $\mathrm{V}_{\mathbb{I N}}=12 \mathrm{~V}$ for the $3.3 \mathrm{~V}, 5 \mathrm{~V}$ ，and adjustable version and $\mathrm{V}_{\mathbb{I N}}=24 \mathrm{~V}$ for the 12 V version， $\mathrm{I}_{\text {LOAD }}=500 \mathrm{~mA}$ ）

| Parameter | Symbol | Test Condition |  | Min． | Typ． | Max． | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device Parameters |  |  |  |  |  |  |  |
| Feedback bias current | $\mathrm{I}_{\mathrm{b}}$ | Adjustable version only， $\mathrm{V}_{\mathrm{FB}}=1.3 \mathrm{~V}$ |  | － | 40 | $\begin{gathered} \hline 50 \\ 100 \end{gathered}$ | nA |
| Oscillator frequency | $f_{0}$ | （Note 5） |  | $\begin{aligned} & 127 \\ & 110 \\ & \hline \end{aligned}$ | 150 | $\begin{array}{r} 173 \\ 173 \\ \hline \end{array}$ | KHz |
| Saturation voltage | $\mathrm{V}_{\text {SAT }}$ | $\mathrm{l}_{\text {Out }}=3 \mathrm{~A}($（Note 6，7） |  | － | 1.16 | $\begin{aligned} & 1.4 \\ & 1.5 \end{aligned}$ | V |
| Max．duty cycle（ON） Min．duty cycle（OFF） | DC | （Note 7） |  | － | $\begin{gathered} 100 \\ 0 \end{gathered}$ | － | \％ |
| Current limit | $\mathrm{I}_{\mathrm{CL}}$ | Peak current（Note 6，7） |  | 3.6 | － | $\begin{aligned} & \hline 5.5 \\ & 6.9 \\ & \hline \end{aligned}$ | A |
| Output leakage current | L | Output＝0V（Note 6，8） |  | － | － | 50 | $\mu \mathrm{A}$ |
| Quiescent current | $l_{0}$ | （Note 8） |  | － | 5 | 30 | mA |
| Standby quiescent current | $\mathrm{I}_{\text {StBy }}$ | ON／OFF pin＝5V（Note 9） |  | － | 85 | $\begin{aligned} & 200 \\ & 300 \\ & \hline \end{aligned}$ | $\mu \mathrm{A}$ |
| Thermal resistance | $\theta_{\text {Jc }}$ | $\begin{aligned} & \text { TO220-5 } \\ & \hline \text { TO263-5 } \\ & \hline \end{aligned}$ | Junction to case | － | 2.5 | － | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
|  | $\begin{array}{\|c\|} \hline \theta_{\mathrm{JA}} \\ \text { (Note 10) } \\ \hline \end{array}$ | TO220－5 | Junction to ambient | － | 28 | － | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
|  |  | TO263－5 |  | － | 30 | － |  |
| ON／OFF Control |  |  |  |  |  |  |  |
| ON／OFF pin logic input threshold voltage | $\mathrm{V}_{\text {IH }}$ | Low（Regulator ON） |  | － | 1.4 | 0.6 | V |
|  | VIL | High（Reg | ator OFF） | 2.0 |  | － |  |
| ON／OFF pin input current | $\mathrm{I}_{\mathrm{H}}$ | $\mathrm{V}_{\text {LOGIC }}=2.5 \mathrm{~V}$ | （Regulator OFF） | － | 6 | 15 | $\mu \mathrm{A}$ |
|  | ILL | $\mathrm{V}_{\text {LOGIC }}=0.5$ | （Regulator ON） | － | 0.02 | 5 |  |

Notes：
1．Absolute Maximum Ratings indicate limits beyond which damage to the device may occur．Operating Ratings indicate conditions for which the device is intended to be functional，but do not guarantee specific performance limits．For guaranteed specifications and test conditions，see the Electrical Characteristics．
2．External components such as the catch diode，inductor，input and output capacitors，and voltage programming resistors can affect switching regulator system performance．
3．Typical numbers are at $25^{\circ} \mathrm{C}$ and represent the most likely norm．
4．All limits guaranteed at room temperature（standard type face）and at temperature extremes（bold type face）．All room temperature limits are $100 \%$ production tested．All limits at temperature extremes are guaranteed via correlation using standard Statistical Quality Control（SQC）methods．All limits are used to calculate Average Outgoing Quality Level（AOQL）．
5．The switching frequency is reduced when the second stage current limit is activated．
6．No diode，inductor or capacitor connected to output pin．
7．Feedback pin removed from output and connected to OV to force the output transistor switch ON．
8．Feedback pin removed from output and connected to 12 V for the $3.3 \mathrm{~V}, 5 \mathrm{~V}$ ，ADJ．version，and 15 V for the 12 V version，to force the output transistor switch OFF．
9． $\mathrm{V}_{\mathbb{N}}=40 \mathrm{~V}$ ．
10．Junction to ambient thermal resistance．（With copper area of approximately $3 \mathrm{in}^{2}$ ）

## CHARACTERISTICS CURVE



## PACKAGE INFORMATION

## TO－263－5



Notes：
1．Refer to JEDEC TO－263 BA．
2．All dimensions are in millimeter．

RS2596
TO－220－5


Note：All dimensions are in millimeter．

## IMPORTANT NOTICE

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