

# **SDB5100D**

**Schottky Barrier Rectifier** 

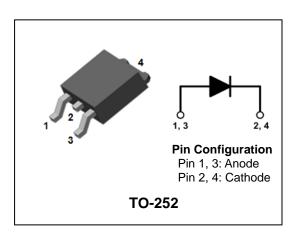
#### HIGH VOLTAGE SCHOTTKY RECTIFIER

#### **Features**

- · Low forward voltage drop
- Low power loss and High efficiency
- · Low leakage current
- · High surge capability
- Halogen-free component and RoHS compliant device

#### **Applications**

- High efficiency SMPS
- · Output rectification
- · High frequency switching
- Freewheeling
- DC-DC converter systems



#### **Product Characteristics**

I <sub>F(AV)</sub>	5A
$V_{RRM}$	100V
V <sub>FM</sub> at 125℃	0.68V
I <sub>FSM</sub>	120A

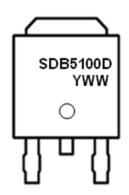
### Description

The SDB5100D is ideally suited for a full wave output rectifier in low switching power supplies, inverters and as free wheeling diodes.

#### **Ordering Information**

Device	Marking Code	Package	Packaging
SDB5100D	SDB5100D	TO-252	Tape & Reel

## **Marking Information**



SDB5100D = Specific Device Code YWW = Year & Week Code Marking

- -. Y = Year Code
- -. WW = Week Code

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# **Absolute Maximum Ratings (Limiting Values)**

Characteristic	Symbol	Value	Unit
Maximum repetitive reverse voltage Maximum working peak reverse voltage Maximum DC blocking voltage	V <sub>RRM</sub> V <sub>RWM</sub> V <sub>R</sub>	100	V
Maximum average forward rectified current	I <sub>F(AV)</sub>	5	Α
Peak forward surge current 8.3ms single half sine-wave superimposed on rated load per diode	I <sub>FSM</sub>	120	Α
Storage temperature range	T <sub>stg</sub>	-45℃ to +150℃	$^{\circ}$
Maximum operating junction temperature	TJ	150	${\mathbb C}$

### **Thermal Characteristics**

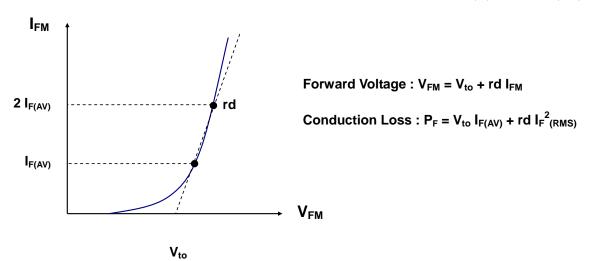
Characteristic	Symbol	Value	Unit
Maximum thermal resistance junction to case	$R_{\text{th(j-c)}}$	4.0	°C/W

### **Electrical Characteristics**

Characteristic	Symbol	Test Condition		Min.	Тур.	Max.	Unit
Peak forward voltage drop	V <sub>FM</sub> <sup>(1)</sup>	I <sub>FM</sub> = 5A	T <sub>j</sub> =25℃	-	-	0.85	V
			T <sub>j</sub> =125℃	-	-	0.68	V
Reverse leakage current	I <sub>RM</sub> <sup>(1)</sup>	$V_R = V_{RRM}$	T <sub>j</sub> =25℃	-	-	10	uA
			T <sub>j</sub> =125℃	-	-	10	mA
Junction capacitance	C <sub>j</sub>	$V_R = 4V_{DC}$ , f=1MHz		-	100	-	pF

**Note :** (1) Pulse test :  $t_P \le 380 \ \mu\text{s}$ , Duty cycle  $\le 2\%$ 

To evaluate the conduction losses use the following equation (Fig 4.):  $P_F = 0.62 \times I_{F(AV)} + 0.042 I_{F(RMS)}^2$ 



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## **Rating and Characteristic Curves**

Fig. 1) Typical Forward Characteristics

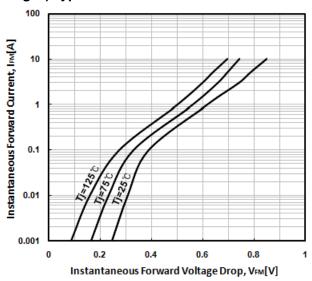


Fig. 3) Maximum Forward Derative Curve

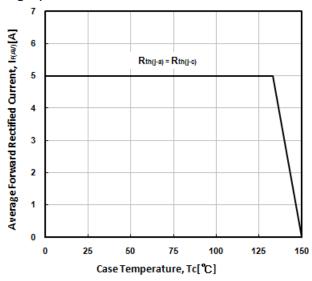


Fig. 5) Maximum Non-Repetitive Peak Forward Surge Current

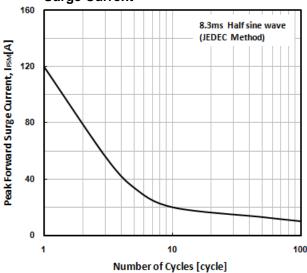


Fig. 2) Typical Reverse Characteristics

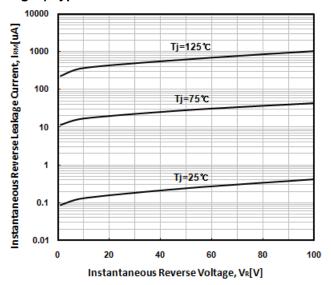


Fig. 4) Forward Power Dissipation

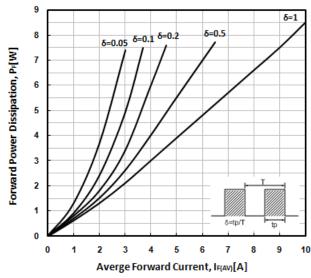
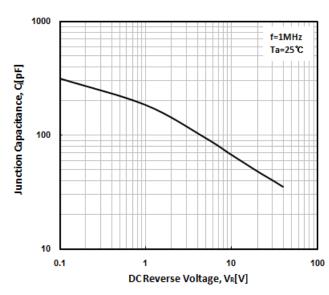
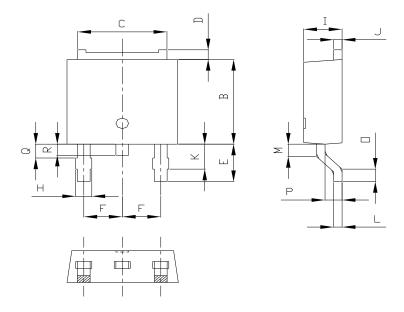


Fig. 6) Typical Junction Capacitance



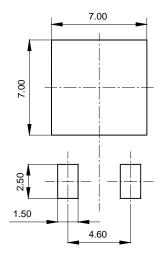
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# **Package Outline Dimension**



	1	NOTE		
SYMBOL	MINIMUM	NOMINAL	MAXIMUM	INOIE
Α	6.40	6.61	6.80	
В	5.90	6.10	6.31	
C	5.04	5.34	5.64	
D	0.50	0.70	0.90	
E	2.50	2.70	2.91	
F	2.10	2.30	2.50	
Н		0.96 MAX		
I	2.20	2.30	2.40	
J	0.40	0.50	0.60	
K	1.60	1.80	2.00	
L	0.40	0.50	0.60	
М	0.81	0.91	1.01	
0	0.80	0.90	1.00	
Р	0.90	1.00	1.10	
Q		0.95 MAX	•	
R	161	181	1.00	

# **\* Recommended Land Pattern [unit: mm]**



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