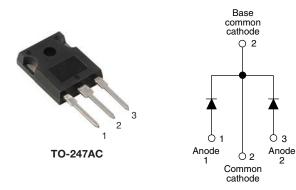


**Vishay Semiconductors** 

## Schottky Rectifier, 2 x 30 A

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PRODUCT SUMMARY								
Package	TO-247AC							
I <sub>F(AV)</sub>	2 x 30 A							
V <sub>R</sub>	150 V							
V <sub>F</sub> at I <sub>F</sub>	0.67 V							
I <sub>RM</sub> max.	25 mA at 125 °C							
T <sub>J</sub> max.	175 °C							
Diode variation	Common cathode							
E <sub>AS</sub>	0.5 mJ							

#### **FEATURES**

- 175 °C T<sub>J</sub> operation
- · Low forward voltage drop
- High frequency operation
- purity, • High high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance



- RoHS COMPLIANT HALOGEN FREE
- · Guard ring for enhanced ruggedness and long term reliability
- Designed and qualified according to JEDEC-JESD47
- · Material categorization: For definitions of compliance please see www.vishay.com/doc?99912

#### DESCRIPTION

The VS-60CPQ150... center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS									
SYMBOL	CHARACTERISTICS	VALUES	UNITS						
I <sub>F(AV)</sub>	Rectangular waveform	60	А						
V <sub>RRM</sub>		150	V						
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	2300	А						
V <sub>F</sub>	$30 \text{ A}_{pk}, \text{ T}_{\text{J}} = 125 ^{\circ}\text{C} \text{ (per leg)}$	0.67	V						
TJ	Range	- 55 to 175	°C						

VOLTAGE RATINGS								
PARAMETER	SYMBOL	VS-60CPQ150PbF	VS-60CPQ150-N3	UNITS				
Maximum DC reverse voltage	V <sub>R</sub> 150		150	V				
Maximum working peak reverse voltage	V <sub>RWM</sub>	150	v					

ABSOLUTE MAXIMUM RATINGS									
PARAMETER		SYMBOL	TEST COND	TEST CONDITIONS					
Maximum average forward current	per leg	<b>I</b> =	50 % duty cycle at $T_{C}$ = 151 °C, rectangular waveform		30				
See fig. 5	per device	IF(AV)		, rectangular wavelonn	60	А			
Maximum peak one cycle non surge current per leg	aximum peak one cycle non-repetitive		5 $\mu s$ sine or 3 $\mu s$ rect. pulse	Following any rated load condition and with rated	2300	~			
See fig. 7		I <sub>FSM</sub>	10 ms sine or 6 ms rect. pulse	V <sub>RRM</sub> applied	510				
Non-repetitive avalanche energy per leg E <sub>AS</sub>		E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 1 A, L = 1 mH		0.5	mJ			
Repetitive avalanche current p	oer leg	I <sub>AR</sub>	Current decaying linearly to zer Frequency limited by $T_J$ maxim		1	А			

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ELECTRICAL SPECIFICATIONS									
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS						
		30 A	T.I = 25 °C	0.80	0.83	V			
Maximum forward voltage drop per leg See fig. 1	V <sub>FM</sub> <sup>(1)</sup>	60 A	1j=25 C	0.93	0.99				
	VFM ("	30 A	T.I = 125 °C	0.64	0.67				
		60 A	1j = 125 C	0.74	0.77				
Maximum reverse leakage current per leg		T <sub>J</sub> = 25 °C	$V_{\rm B}$ = Rated $V_{\rm B}$	10	100	μA			
See fig. 2	I <sub>RM</sub>	T <sub>J</sub> = 125 °C	VR - naleu VR	12	25	mA			
Typical junction capacitance per leg	CT	$V_R$ = 5 $V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C			820	pF			
Typical series inductance per leg	L <sub>S</sub>	Measured lead to lead 5 mm from package body			7.5	nH			
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		-	10 000	V/µs			

#### Note

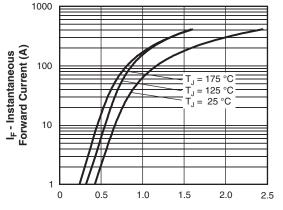
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 $^{(1)}\,$  Pulse width < 300  $\mu s,\,duty\,cycle$  < 2  $\,\%$ 

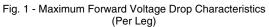
THERMAL - MECHANICAL SPECIFICATIONS									
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS				
Maximum junction and storage temperature range		T <sub>J</sub> , T <sub>Stg</sub>		- 55 to 175	°C				
Maximum thermal resistance, junction to case per leg		Р	DC operation See fig. 4	0.8					
Maximum thermal resistance, junction to case per package		R <sub>thJC</sub> DC operation		0.4	°C/W				
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, smooth and greased	0.25					
Approvimate weight				6	g				
Approximate weight				0.21	oz.				
Mounting torque	minimum			6 (5)	kgf ⋅ cm				
Mounting torque –	maximum			12 (10)	(lbf ⋅ in)				
Marking device			Case style TO-247AC (JEDEC)	60CP	Q150				

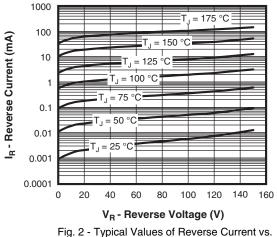


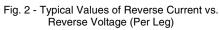
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V<sub>FM</sub> - Forward Voltage Drop (V)







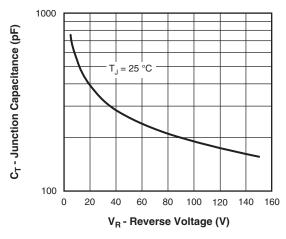
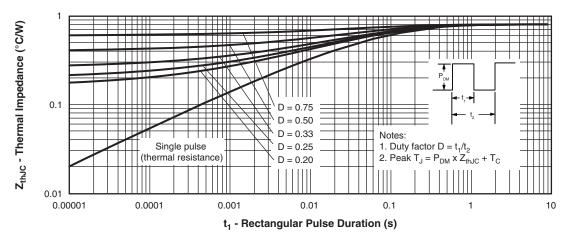
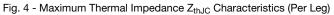
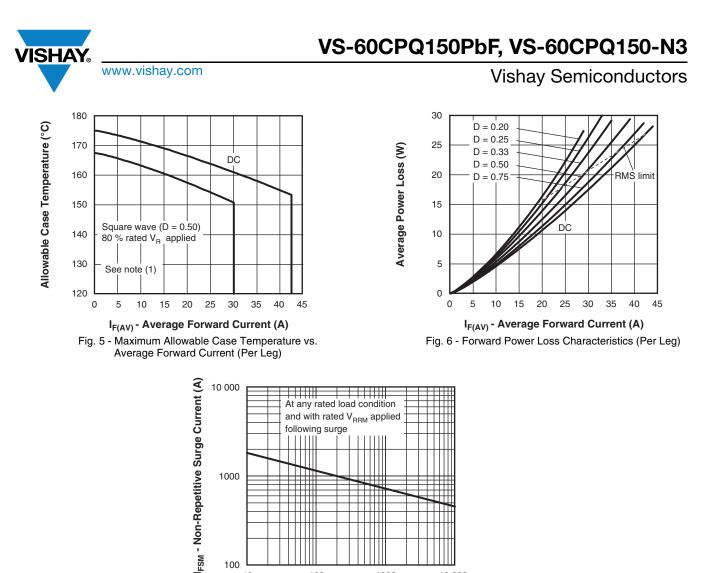


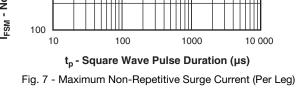
Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)





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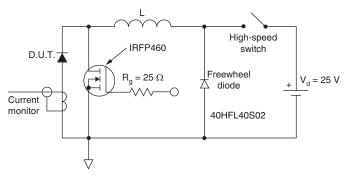


Fig. 8 - Unclamped Inductive Test Circuit

#### Note

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## Vishay Semiconductors

### **ORDERING INFORMATION TABLE**

Device code	VS-	60	С	Р	Q	150	PbF
		(2)	(3)	(4)	(5)	6	(7)
	1 - 2 - 3 -	Vish Curi Circ C =	nay Sem rent ratir uit confi Commo	iconduc ng (60 = guration on catho	tors pro 60 A)	$\bigcirc$	
	4 - 5 -	P =	kage: TO-247 ottky "Q				
	6 - 7 -						
				ad (Pb)·			-

• -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)								
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION					
VS-60CPQ150PbF	25	500	Antistatic plastic tube					
VS-60CPQ150-N3	25	500	Antistatic plastic tube					

LINKS TO RELATED DOCUMENTS							
Dimensions		www.vishay.com/doc?95542					
Part marking information	TO-247AC PbF	www.vishay.com/doc?95226					
	TO-247AC -N3	www.vishay.com/doc?95007					

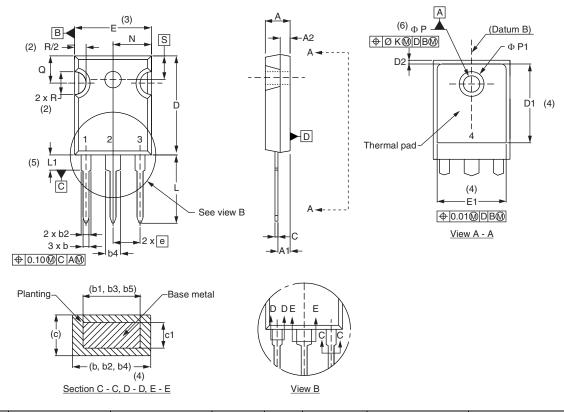
## **Outline Dimensions**



Vishay Semiconductors

**TO-247** 

### **DIMENSIONS** in millimeters and inches



SYMBOL	MILLIM	IETERS	INC	HES	NOTES		SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES		STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
A	4.65	5.31	0.183	0.209			D2	0.51	1.35	0.020	0.053	
A1	2.21	2.59	0.087	0.102			Е	15.29	15.87	0.602	0.625	3
A2	1.17	1.37	0.046	0.054			E1	13.46	-	0.53	-	
b	0.99	1.40	0.039	0.055			е	5.46	BSC	0.215	BSC	
b1	0.99	1.35	0.039	0.053			ØК	0.2	254	0.0	)10	
b2	1.65	2.39	0.065	0.094			L	14.20	16.10	0.559	0.634	
b3	1.65	2.33	0.065	0.092			L1	3.71	4.29	0.146	0.169	
b4	2.59	3.43	0.102	0.135			N	7.62	BSC	0	.3	
b5	2.59	3.38	0.102	0.133			ØР	3.56	3.66	0.14	0.144	
С	0.38	0.89	0.015	0.035			Ø P1	-	7.39	-	0.291	
c1	0.38	0.84	0.015	0.033			Q	5.31	5.69	0.209	0.224	
D	19.71	20.70	0.776	0.815	3		R	4.52	5.49	0.178	0.216	
D1	13.08	-	0.515	-	4		S	5.51	BSC	0.217	' BSC	

#### Notes

<sup>(1)</sup> Dimensioning and tolerancing per ASME Y14.5M-1994

(2) Contour of slot optional

(3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body

<sup>(4)</sup> Thermal pad contour optional with dimensions D1 and E1

<sup>(5)</sup> Lead finish uncontrolled in L1

<sup>(6)</sup> Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")

<sup>(7)</sup> Outline conforms to JEDEC<sup>®</sup> outline TO-247 with exception of dimension c and Q

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