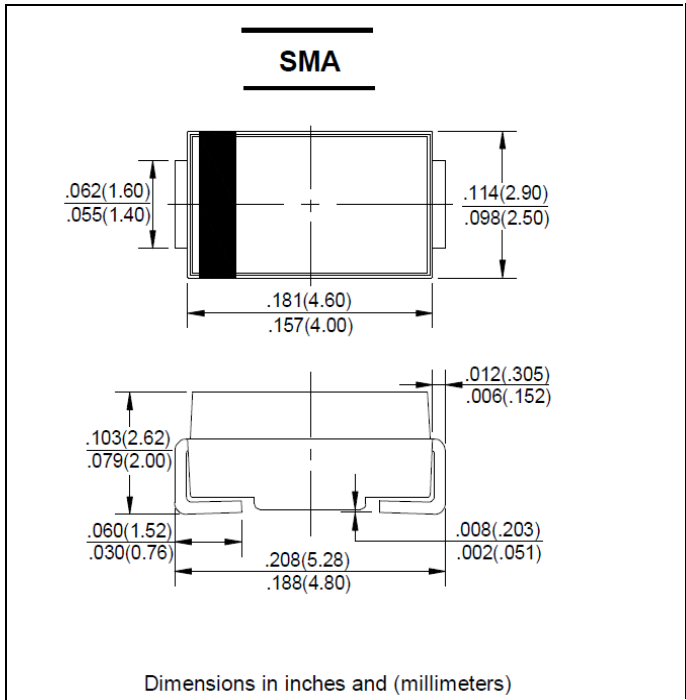


FEATURES

- For surface mounted applications
- Metal silicon junction, majority carrier conduction
- Low power loss, high efficiency
- Built-in strain relief, ideal for automated placement
- High forward surge current capability
- High temperature soldering guaranteed:
250°C/10 seconds at terminals
- The plastic material carries U/L recognition 94V-0

MECHANICAL DATA

- Case: JEDEC DO -214AC. molded plastic
- Terminals: Axial leads. Solderable per MIL - STD - 750 Method 2026
- Polarity: Color band denotes cathode
- Weight: 0.003 ounce. 0.093 grams
- Mounting position: Any


MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Ratings at 25 °C ambient temperature unless otherwise specified.

Single phase, half wave, 60HZ, resistive or inductive load. For capacitive load, derate current by 20%

Characteristic	Symbol	SS22	SS23	SS24	SS25	SS26	SS28	SS29	SS210	Unit	
Peak Repetitive Reverse Voltage	V_{RRM}	20	30	40	50	60	80	90	100	V	
Working Peak Reverse Voltage	V_{RWM}										
DC Blocking Voltage	V_R										
RMS Reverse Voltage	$V_{R(RMS)}$	14	21	28	35	42	56	64	71	V	
Average Rectified Output Current @ $T_L = 75^\circ C$	I_O	2.0								A	
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	I_{FSM}	50								A	
Forward Voltage @ $I_F = 1.0A$	V_{FM}	0.55		0.70			0.85			V	
Peak Reverse Current @ $T_A = 25^\circ C$ At Rated DC Blocking Voltage @ $T_A = 100^\circ C$	I_{RM}					0.5					mA
						20					
Typical Thermal Resistance (Note 1)	$R_{\theta JL}$ $R_{\theta JA}$					28					$^\circ C/W$
						88					
Operating Temperature Range	T_J	-65 to +125								$^\circ C$	
Storage Temperature Range	T_{STG}	-65 to +150								$^\circ C$	

- NOTE:**
1. Measured at 1.0MHZ and applied reverse voltage of 4.0V DC
 2. P.C.B. mounted with 0.2x0.2 (5.0x5.0mm) copper pad areas

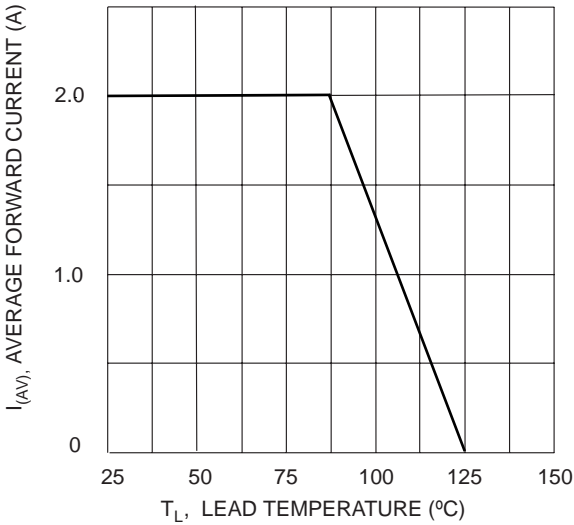


Fig. 1 Forward Current Derating Curve

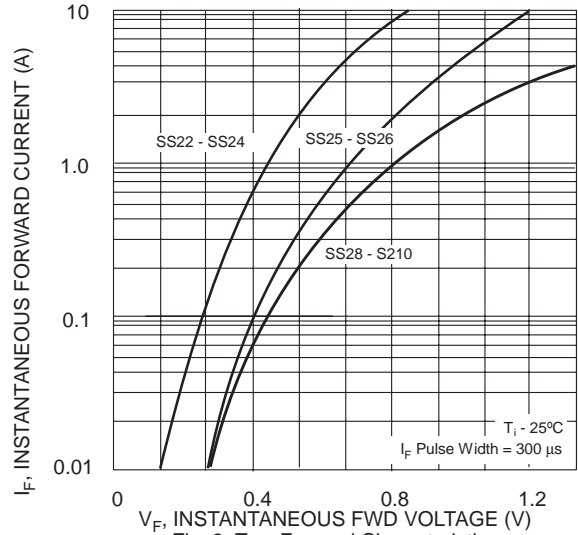


Fig. 2 Typ. Forward Characteristics

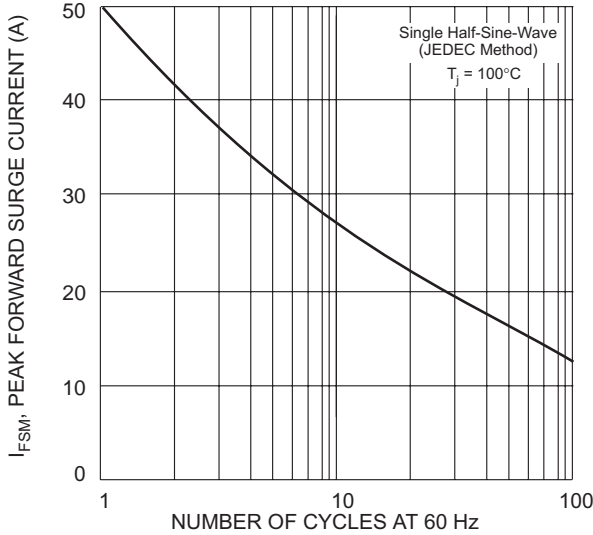


Fig. 3 Max Non-Repetitive Peak Fwd Surge Current

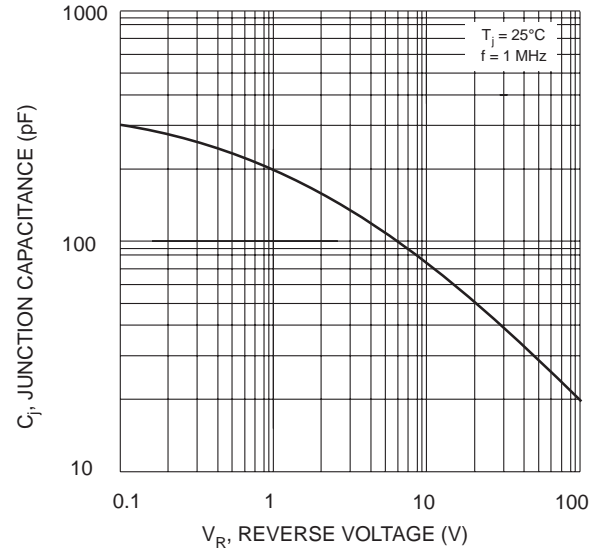


Fig. 4 Typical Junction Capacitance

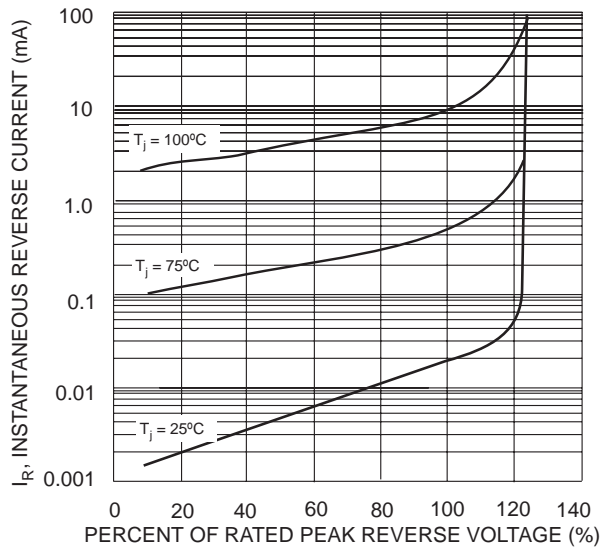


Fig. 5 Typical Reverse Characteristics