

# 700V Super-junction Power MOSFET

### **Description**

#### 700V Super-junction Power MOSFET

Super-junction power MOSFET is a revolutionary technology for high voltage power MOSFETs, designed according to the SJ principle. The Multi-EPI SJ MOSFET provide an extremely low switching, communication and conduction losses device with highest robustness make especially resonant switching applications more reliable, more efficient, lighter and cooler, designed by Wuxi Unigroup Microelectronics Company.

#### **Features**

- Very low FOM RDS(on)×Qg
- 100% avalanche tested
- Easy to use/drive
- RoHS compliant

## **Applications**

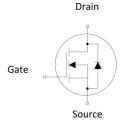
- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)
- Charger

TO-263





TO-252





## **Device Marking and Package Information**

Device	Package	Marking
TPB70R950M	TO-263	
TPD70R950M	TO-252	70R950M

## **Key Performance Parameters**

Parameter	Value	Unit
V <sub>DS</sub> @ T <sub>j,max</sub>	750	V
R <sub>DS(on),max</sub>	0.95	Ω
$Q_{g,typ}$	9.6	nC
I <sub>D</sub>	4.5	A
I <sub>D,pulse</sub>	13.5	A
E <sub>OSS</sub> @ 400V	1.05	μЈ
Body Diode di <sub>F</sub> /dt	500	A/µs



<b>Absolute Maximum Ratings</b> T <sub>C</sub> = 25°C, unless otherwise noted					
Parameter		Symbol	Value	Unit	
	T <sub>C</sub> = 25°C			4.5	А
Continuous Drain Current	T <sub>C</sub> = 100°C		I <sub>D</sub>	2.7	
Pulsed Drain Current	•	(note1)	I <sub>D,pulse</sub>	13.5	А
Gate-Source Voltage			$V_{GSS}$	±30	V
Single Pulse Avalanche Energ	у	(note2)	E <sub>AS</sub>	50	mJ
Repetitive Avalanche Energy (note2)		E <sub>AR</sub>	0.15	mJ	
Avalanche Current			I <sub>AR</sub>	1.0	Α
MOSFET dv/dt Ruggedness, V <sub>DS</sub> = 0480V		dv/dt	50	V/ns	
Power Dissipation For TO-263,TO-252		$P_{D}$	37	W	
Continuous Diode Forward Current		I <sub>S</sub>	3.8	A	
Diode Pulsed Current (note1)		I <sub>S,pulse</sub>	13.5		
Reverse Diode dv/dt (note3)		(note3)	dv/dt	15	V/ns
Maximum Diode Commutation Speed (note3)		(note3)	di <sub>f</sub> /dt	500	A/µs
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	-55~+150	°C	

Thermal Resistance For TO-263,TO-252			
Parameter Symbol Value Unit			
Thermal Resistance, Junction-to-Case	R <sub>thJC</sub>	3.4	°C/W
Thermal Resistance, Junction-to-Ambient	$R_{thJA}$	62	C/VV

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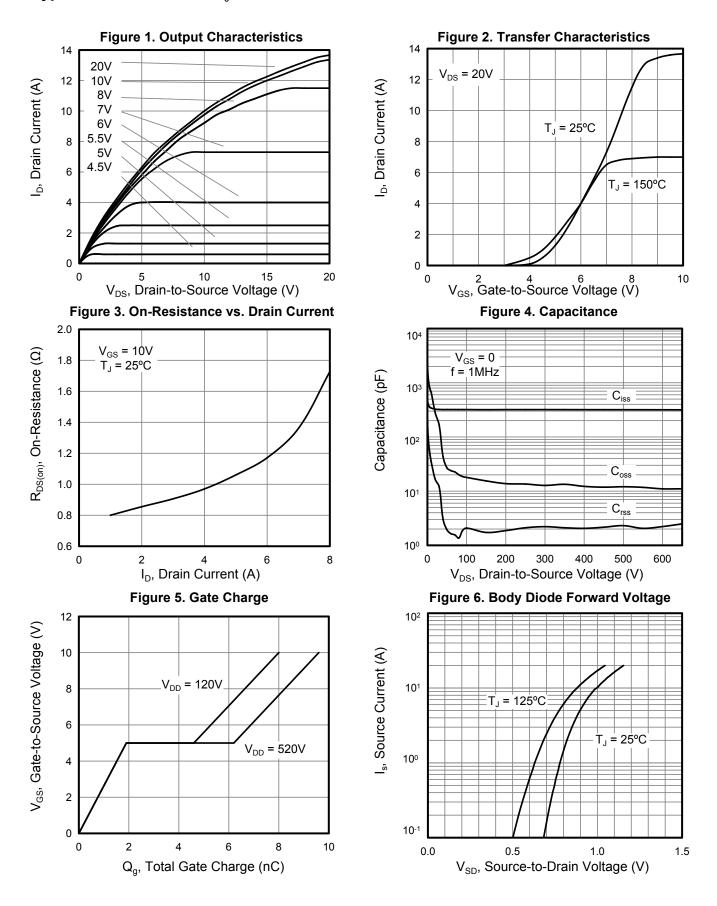


			Value				
Parameter	er Symbol Test Conditions		Min. Typ. N		Max.	Unit x.	
Static Characteristics				•			
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$V_{GS} = 0V, I_D = 250\mu A$	700			V	
Zoro Cato Voltago Proin Current		$V_{DS} = 700V$ , $V_{GS} = 0V$ , $T_{J} = 25$ °C	, V <sub>GS</sub> = 0V, T <sub>J</sub> = 25°C		1		
Zero Gate Voltage Drain Current	DSS	V <sub>DS</sub> = 700V, V <sub>GS</sub> = 0V, T <sub>J</sub> = 150°C			100	μA 00	
Gate-Source Leakage Current	I <sub>GSS</sub>	$V_{GS} = \pm 20V$			±1	μA	
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2.5		4.0	V	
Drain-Source On-State-Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 2A		0.87	0.95	Ω	
Gate Resistance	$R_{G}$	f = 1.0MHz open drain		5		Ω	
Dynamic Characteristics							
Input Capacitance	C <sub>iss</sub>	\/ - 0\/		320		pF	
Output Capacitance	C <sub>oss</sub>	$V_{GS} = 0V,$ $V_{DS} = 100V,$		18			
Reverse Transfer Capacitance	C <sub>rss</sub>	f = 1.0MHz		2.1			
Total Gate Charge	$Q_g$			9.6			
Gate-Source Charge	$Q_{gs}$	$V_{DD} = 520V, I_{D} = 4.5A,$ $V_{GS} = 10V$		1.9		nC	
Gate-Drain Charge	$Q_{gd}$	63		4.3			
Turn-on Delay Time	t <sub>d(on)</sub>			54			
Turn-on Rise Time	t <sub>r</sub>	V <sub>DD</sub> = 400V, I <sub>D</sub> = 4.5A,		62			
Turn-off Delay Time	$t_{d(off)}$	$R_G = 25\Omega$		86		ns	
Turn-off Fall Time	t <sub>f</sub>			51			
Drain-Source Body Diode Characte	ristics						
Body Diode Forward Voltage	V <sub>SD</sub>	$T_J = 25^{\circ}\text{C}, I_{SD} = 2\text{A}, V_{GS} = 0\text{V}$		0.9	1.2	V	
Reverse Recovery Time	t <sub>rr</sub>			271		ns	
Reverse Recovery Charge	Q <sub>rr</sub>	V <sub>R</sub> = 400V, I <sub>F</sub> = 4.5A, di <sub>F</sub> /dt = 100A/μs	-	3.1		μC	
Peak Reverse Recovery Current	I <sub>rrm</sub>			23		Α	

#### **Notes**

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature
- 2.  $I_{AS}$  = 1.0A,  $V_{DD}$  = 50V,  $R_G$  = 25 $\Omega$ , Starting  $T_J$  = 25 $^{\circ}$ C
- 3. Identical low side and high side switch with identical  $R_{\rm G}$

# **Typical Characteristics** $T_J = 25^{\circ}\text{C}$ , unless otherwise noted



# **Typical Characteristics** $T_J = 25$ °C, unless otherwise noted

Figure 7. On-Resistance vs. Junction Temperature

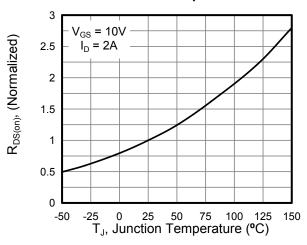


Figure 9. Transient Thermal Impedance For

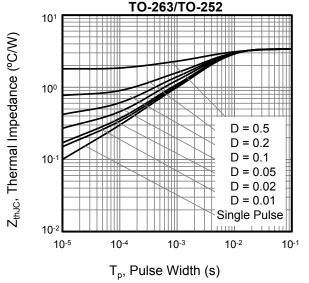


Figure 11. Typ. Coss Stored Energy

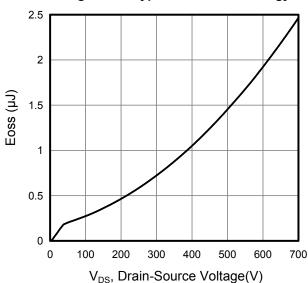


Figure 8. Breakdown voltage vs. Junction Temperature

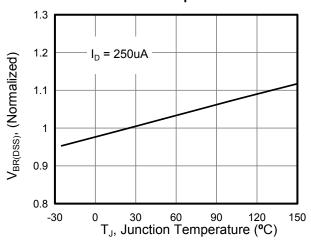
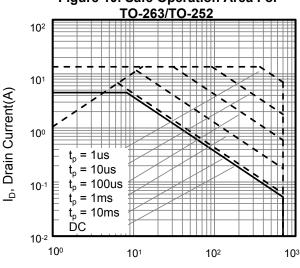


Figure 10. Safe Operation Area For



V<sub>DS</sub>, Drain-Source Voltage(V)



Figure A: Gate Charge Test Circuit and Waveform

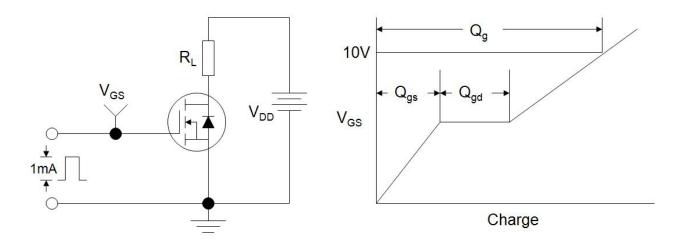


Figure B: Resistive Switching Test Circuit and Waveform

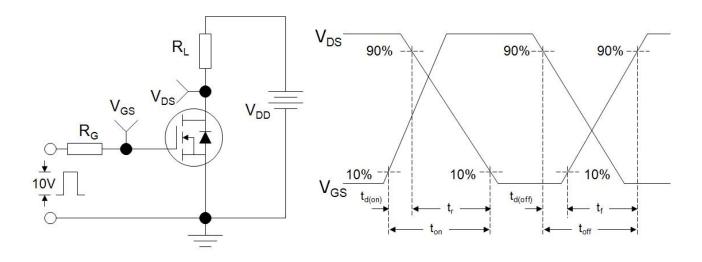
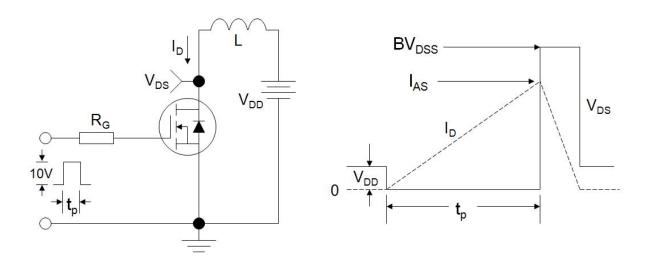
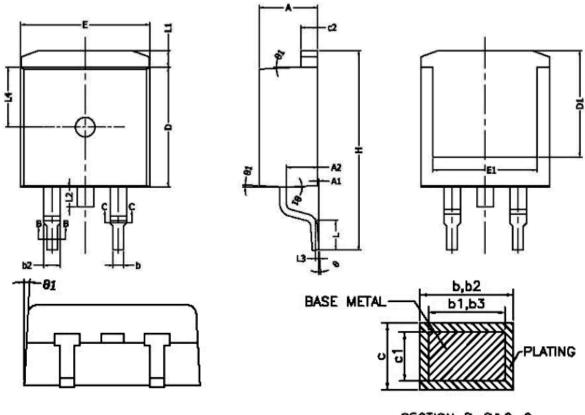


Figure C: Unclamped Inductive Switching Test Circuit and Waveform



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# TO-263 (封装厂I)



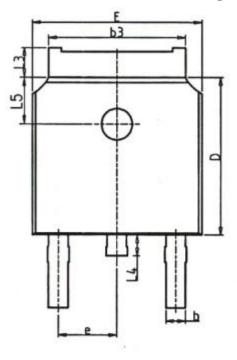
SECTION B-B&C-C

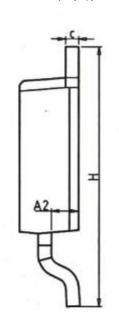
SYMBOL	MIN	NOM	MAX
Α	4.40	4.50	4.60
A1	0	0.10	0.25
A2	2.20	2.40	2.60
b	0.76		0.89
b1	0.75	0.80	0.85
b2	1.23		1.37
b3	1.22	1.27	1.32
С	0.47		0.60
c1	0.46	0.51	0.56
c2	1.25	1.30	1.35
D	9.10	9.20	9.30
D1	8.00	l — I	
E	9.80	9.90	10.00
E1	7.80		
е	2.	54 BSC	
Н	14.90	15.30	15.70
L	2.00	2.30	2.60
L1	1.17	1.27	1.40
L2			1.75
L3	0.25BSC		
L4	4.60 REF		
θ	0°		8°
<b>0</b> 1	1°	30	5°

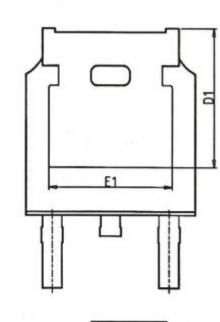
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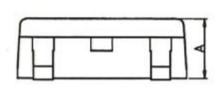


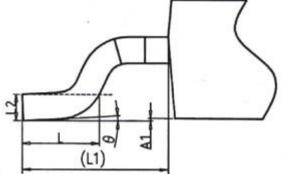
# TO-252 (封装厂H)











Unit:mm				
Symbol	Min.	Nom	Max.	
Α	2.20	2.30	2.38	
A1	0.00	-	0.20	
A2	0.97	1.07	1.17	
b	0.68	0.78	0.90	
b3	5.20	5.33	546	
С	0.43	0.53	0.61	
D	5.98	6.10	6.22	
D1	5.30 REF			
Е	6.40	6.60	6.73	
E1	4.63	-	_	

Unit:mm				
Symbol	Min.	Nom	Max.	
е		2.286 BSC		
Н	9.40	10.10	10.50	
L	1.38	1.50	1.75	
L1	2.90 REF			
L2	0.51 BSC			
L3	0.88	-	1.28	
L4	0.50	-	1.00	
L5	1.65	1.80	1.95	
θ	0°	-	8°	



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