

# **1000V N-Channel MOSFET**

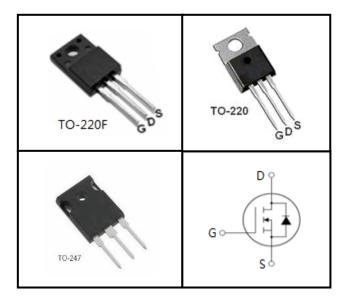
#### FEATURES

- Fast switching
- 100% avalanche tested
- Improved dv/dt capability

#### APPLICATIONS

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)

Device Marking and Package Information					
Device	Device Package Marking				
CS6N100F	TO-220F	CS6N100F			
CS6N100P	TO-220	CS6N100P			
CS6N100W	TO-247	CS6N100W			



<b>Absolute Maximum Ratings</b> $T_c = 25^{\circ}C$ , unless otherwise noted						
Parameter	Symbol	Value			Unit	
		TO-220F	TO-220	TO-247	Unit	
Drain-Source Voltage (V <sub>GS</sub> = 0V)	V <sub>DSS</sub>		1000	-	V	
Continuous Drain Current	I <sub>D</sub>		6		А	
Pulsed Drain Current (note1)	I <sub>DM</sub>	24			А	
Gate-Source Voltage	V <sub>GSS</sub>		±30		V	
Single Pulse Avalanche Energy (note2)	E <sub>AS</sub>	405		mJ		
Avalanche Current (note1)	I <sub>AS</sub>	9			А	
Repetitive Avalanche Energy (note1)	E <sub>AR</sub>	243			mJ	
Power Dissipation ( $T_c = 25^{\circ}C$ )	P <sub>D</sub>	65	14	47	W	
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55~+150		°C		

Thermal Resistance					
Desembles	Symbol	Value			
Parameter		TO-220F	TO-220	TO-247	Unit
Thermal Resistance, Junction-to-Case	R <sub>thJC</sub>	1.92	0.85		°C/W
Thermal Resistance, Junction-to-Ambient	R <sub>thJA</sub>	62.5	60		°C/W



<b>Specifications</b> $T_J = 25^{\circ}C$ , ur			Value					
rameter Symbol Test Conditions		Test Conditions	Min.	Тур.	Max.	Unit		
Static								
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$V_{GS} = 0V, I_{D} = 250 \mu A$	1000			V		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 1000V, V <sub>GS</sub> = 0V, T <sub>J</sub> = 25°C			1	μA		
Gate-Source Leakage	I <sub>GSS</sub>	$V_{GS}$ = $\pm 30V$			±100	nA		
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \mu A$	3.0		4.0	V		
Drain-Source On-Resistance (Note3)	$R_{DS(on)}$	V <sub>GS</sub> = 10V, I <sub>D</sub> = 3A		1.2	1.5	Ω		
Dynamic								
Input Capacitance	C <sub>iss</sub>	$\mathcal{V} = \mathcal{O}\mathcal{V}$		1994		pF		
Output Capacitance	C <sub>oss</sub>	$V_{GS} = 0V,$ $V_{DS} = 25V,$		180				
Reverse Transfer Capacitance	C <sub>rss</sub>	f = 1.0MHz		35				
Gate Resistance	R <sub>g</sub>	$V_{GS} = 0V, V_{DS} = 0V, f = 1.0MHz$		0.7		Ω		
Total Gate Charge	Q <sub>g</sub>			83				
Gate-Source Charge	Q <sub>gs</sub>	$V_{DD} = 800V, I_{D} = 6.0A, V_{GS} = 10V$		9		nC		
Gate-Drain Charge	Q <sub>gd</sub>			46				
Turn-on Delay Time	t <sub>d(on)</sub>			48				
Turn-on Rise Time	t <sub>r</sub>	V <sub>DD</sub> = 500V, I <sub>D</sub> = 6.0A,		28				
Turn-off Delay Time	t <sub>d(off)</sub>	$V_{\text{DD}} = 500\text{V}, \text{ I}_{\text{D}} = 6.0\text{A}, \\ \text{R}_{\text{G}} = 25 \ \Omega$		360		ns		
Turn-off Fall Time	t <sub>f</sub>			70				
Drain-Source Body Diode Characteristics								
Continuous Body Diode Current	I <sub>s</sub>	T 25 0C			6			
Pulsed Diode Forward Current	I <sub>SM</sub>	T <sub>C</sub> = 25 °C			24	A		
Body Diode Voltage	V <sub>SD</sub>	$T_{J} = 25^{\circ}C, I_{SD} = 3A, V_{GS} = 0V$			1.4	V		
Reverse Recovery Time	t <sub>rr</sub>	V <sub>R</sub> = 500V,I <sub>S</sub> = 6A,		541		ns		
Reverse Recovery Charge	Q <sub>rr</sub>	di <sub>F</sub> /dt =100A /µs		8.5		μC		

#### Notes

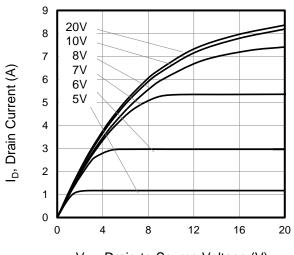
- 1. Repetitive Rating: Pulse width limited by maximum junction temperature
- 2. L=10mH,  $V_{DD}$  = 70V,  $R_G$  = 25  $\Omega$ , Starting  $T_J$  = 25 °C
- 3. Pulse Test: Pulse width  $\leq$  300µs, Duty Cycle  $\leq$  1%



Figure 2. Body Diode Forward Voltage

#### **Typical Characteristics** $T_J = 25^{\circ}C$ , unless otherwise noted

Figure 1. Output Characteristics (T<sub>J</sub> = 25°C)



 $V_{DS}$ , Drain-to-Source Voltage (V)



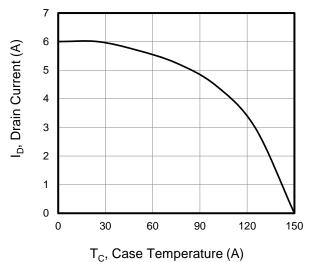
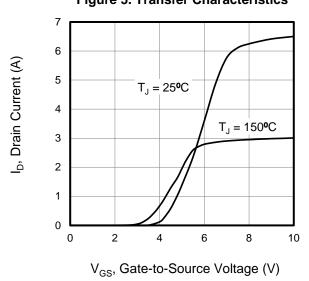
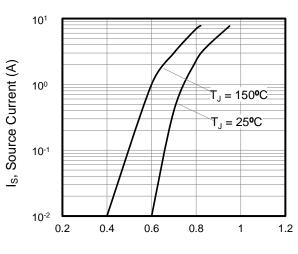
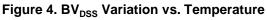


Figure 5. Transfer Characteristics





V<sub>SD</sub>, Source-to-Drain Voltage (V)



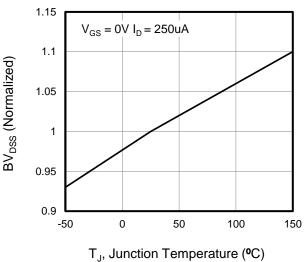
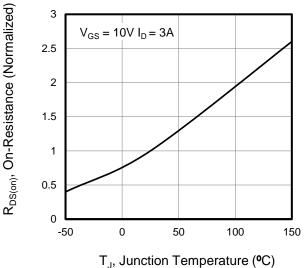
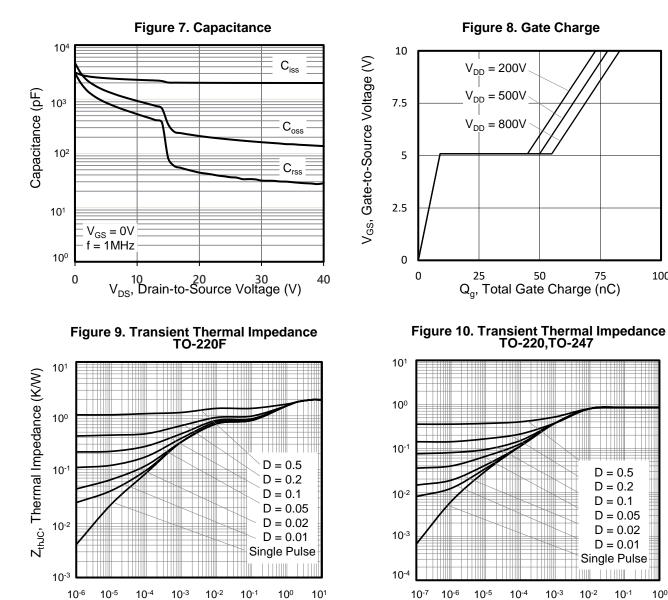


Figure 6. On-Resistance vs. Temperature





#### **Typical Characteristics** $T_J = 25^{\circ}C$ , unless otherwise noted



T<sub>p</sub>, Pulse Width (s)

T<sub>p</sub>, Pulse Width (s)

100

D = 0.5

D = 0.2

D = 0.1

D = 0.05

D = 0.02

D = 0.01

10-1

100



Figure A: Gate Charge Test Circuit and Waveform

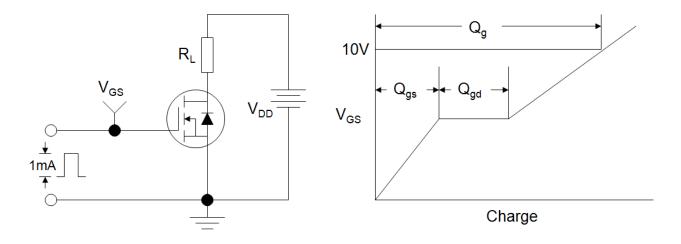


Figure B: Resistive Switching Test Circuit and Waveform

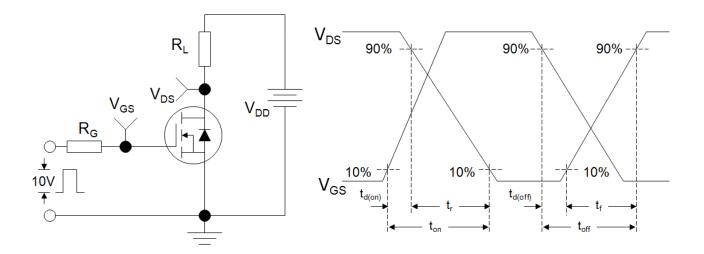
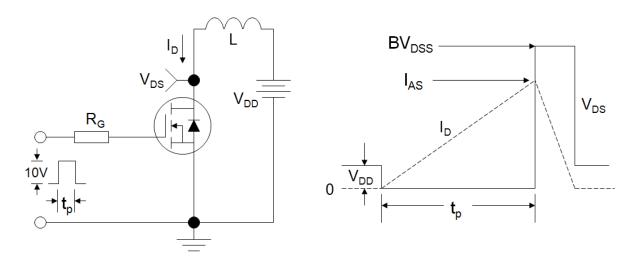


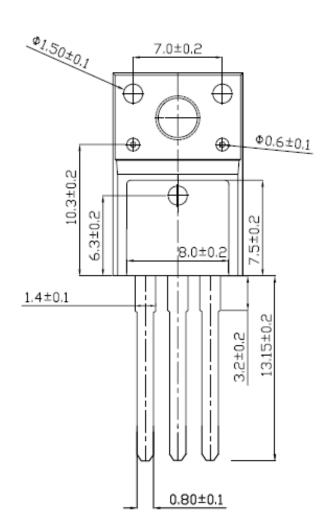
Figure C: Unclamped Inductive Switching Test Circuit and Waveform

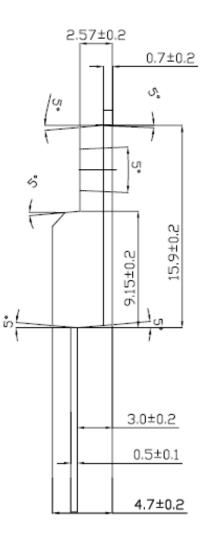


5



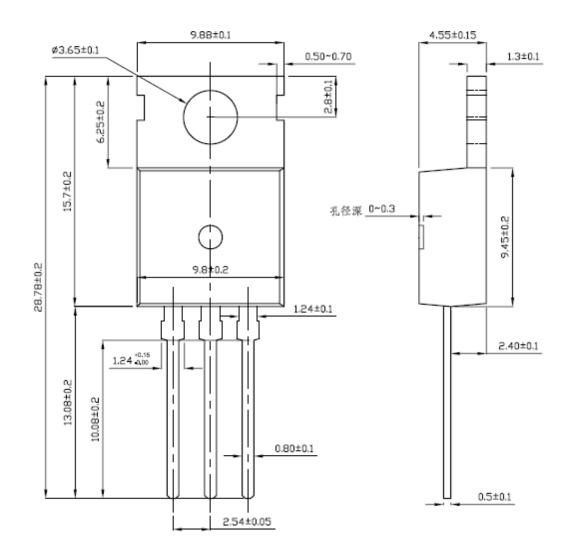
TO-220F





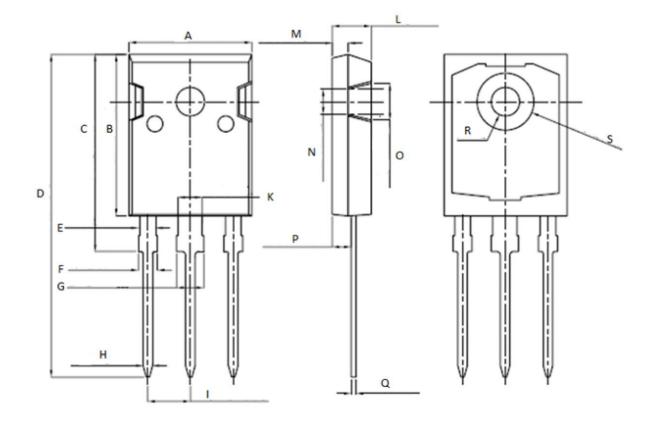


**TO-220** 





TO-247



Unit: mm		Unit: mm			
Symbol	Min.	Max.	Symbol	Symbol Min. Ma	
Α	15.95	16. 25	K	2.90	3.10
В	20.85	21.25	L	4.90	5.30
C	20.95	21.35	М	1.90	2.10
D	40.5	40.9	Ν	4. 50	4. 70
E	1.9	2.1	0	5.40	5.60
F	2.1	2. 25	Р	2. 29	2.49
G	3.1	3. 25	Q	0. 51	0. 71
Н	1.1	1.3	R	φ 3. 5	φ 3. 7
I	5.40	5.50	S	φ 7. 1	φ 7. 3



#### Disclaimer

All product specifications and data are subject to change without notice.

For documents and material available from this datasheet, Suzhou Convert does not warrant or assume any legal liability or responsibility for the accuracy, completeness of any product or technology disclosed hereunder.

No license, express or implied, by estoppels or otherwise, to any intellectual property rights is granted by this document or by any conduct of Suzhou Convert.

The products shown herein are not designed for use in medical, life-saving, or life-sustaining applications unless. Customers using or selling Suzhou Convert products not expressly indicated for use in such applications do so entirely at their own risk and agree to fully indemnify Suzhou Convert for any damages arising or resulting from such use or sale.

Suzhou Convert disclaims any and all liability arising out of the use or application of any product described herein or of any information provided herein to the maximum extent permitted by law. The product specifications do not expand or otherwise modify Suzhou Convert's terms and conditions of purchase, including but not limited to the warranty expressed therein, which apply to these products.

Suzhou Convert SemiConductor CO., Ltd. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.

In the event that any or all Suzhou Convert products (including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.

Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production. Suzhou Convert believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.