60V N-Channel MOSFET

FEATURES

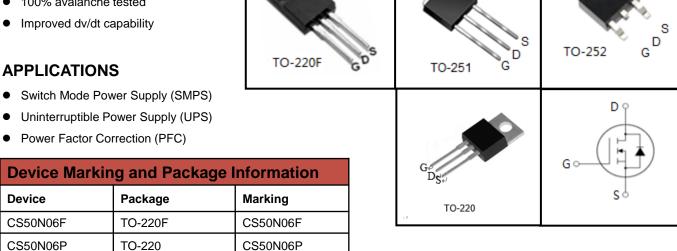
- Fast switching
- 100% avalanche tested

CS50N06D

CS50N06U

TO-252

TO-251



CS50N06D

CS50N06U

Absolute Maximum Ratings $T_C = 25^{\circ}C$, unless otherwise noted								
Parameter	Symbol		l lasit					
		TO-220F	TO-220	TO-251	TO-252	Unit		
Drain-Source Voltage (V _{GS} = 0V)	V _{DSS}	60			V			
Continuous Drain Current	I _D	50			Α			
Pulsed Drain Current (note1)	I _{DM}	200			Α			
Gate-Source Voltage	V _{GSS}	±20			V			
Single Pulse Avalanche Energy (note2)	E _{AS}	288			mJ			
Avalanche Current (note1)	I _{AS}	24			Α			
Repetitive Avalanche Energy (note1)	E _{AR}	172.8			mJ			
Power Dissipation (T _C = 25°C)	P _D	83 110		W				
Operating Junction and Storage Temperature Range	T _J , T _{stg}	-55~+150			°C			

Thermal Resistance							
Parameter	Symbol	Value				l lmit	
		TO-220F	TO-220	TO-251	TO-252	Unit	
Thermal Resistance, Junction-to-Case	R _{thJC}	1.5	1.14			°C/W	
Thermal Resistance, Junction-to-Ambient	R _{thJA}	62.5	60				



Specifications $T_J = 25^{\circ}$ C, unless otherwise noted									
Parameter	Comple of	Tost Conditions	Value			l lmit			
Parameter	eter Symbol Test Conditions		Min.	Тур.	Max.	Unit			
Static									
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_{D} = 250\mu A$	60			V			
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 60V, V_{GS} = 0V, T_{J} = 25^{\circ}C$			1	μΑ			
Gate-Source Leakage	I _{GSS}	$V_{GS} = \pm 20V$			±100	nA			
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2.0		4.0	V			
Drain-Source On-Resistance (Note3)	R _{DS(on)}	$V_{GS} = 10V, I_D = 25A$		14	22	mΩ			
Dynamic									
Input Capacitance	C _{iss}	V _{GS} = 0V,		1320		pF			
Output Capacitance	C _{oss}	$V_{DS} = 25V$,		510					
Reverse Transfer Capacitance	C _{rss}	f = 1.0MHz		235					
Gate Resistance	R_g	$V_{GS} = 0V, V_{DS} = 0V, f = 1.0MHz$		2.5		Ω			
Total Gate Charge	Q_g			57		nC			
Gate-Source Charge	Q_{gs}	$V_{DD} = 48V, I_{D} = 50A,$ $V_{GS} = 10V$		7					
Gate-Drain Charge	Q_{gd}	63		34.5					
Turn-on Delay Time	t _{d(on)}			42		ns			
Turn-on Rise Time	t _r	$V_{DD} = 30V, I_{D} = 50A,$		80					
Turn-off Delay Time	t _{d(off)}	$R_G = 25 \Omega$		214					
Turn-off Fall Time	t _f			128					
Drain-Source Body Diode Character	istics								
Continuous Body Diode Current	Is	T _ 25.0C			50	А			
Pulsed Diode Forward Current	I _{SM}	T _C = 25 °C			200				
Body Diode Voltage	V _{SD}	$T_J = 25^{\circ}C$, $I_{SD} = 25A$, $V_{GS} = 0V$			1.4	V			
Reverse Recovery Time	t _{rr}	$V_R = 30V, I_S = 20A,$		67.5		ns			
Reverse Recovery Charge	Q_{rr}	di _F /dt =100A /µs		0.15		μC			

Notes

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature
- 2. L=1mH, V_{DD} = 25V, R_G = 25 Ω , Starting T_J = 25 $^{\circ}C$
- 3. Pulse Test: Pulse width ≤ 300µs, Duty Cycle ≤ 1%

Typical Characteristics $T_J = 25^{\circ}\text{C}$, unless otherwise noted

Figure 1. Output Characteristics ($T_J = 25^{\circ}C$)

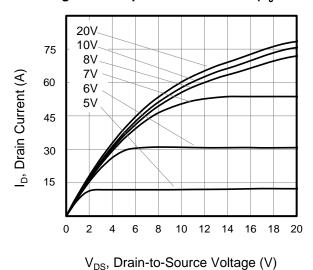


Figure 3. Drain Current vs. Temperature

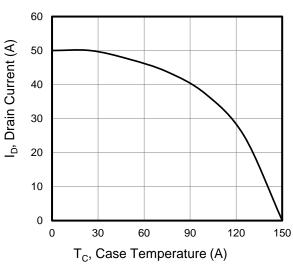


Figure 5. Transfer Characteristics

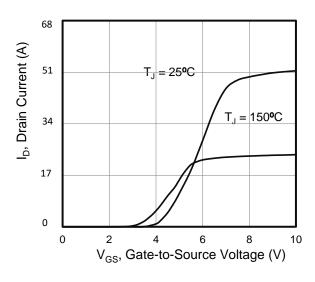


Figure 2. Body Diode Forward Voltage

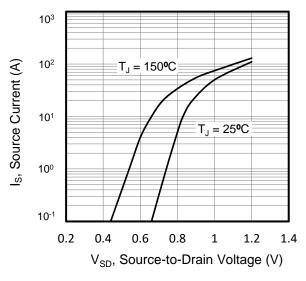


Figure 4. BV_{DSS} Variation vs. Temperature

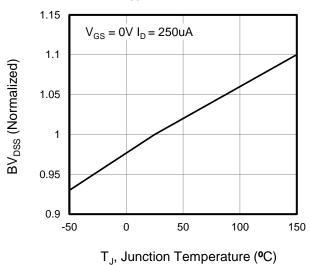
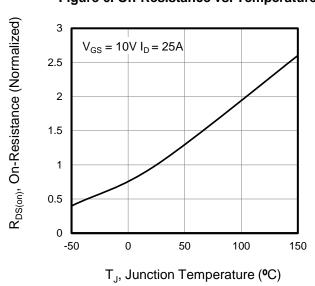
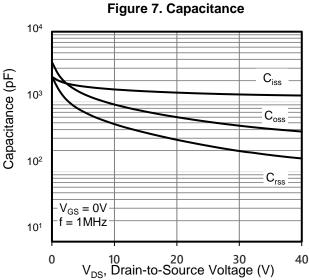


Figure 6. On-Resistance vs. Temperature



Typical Characteristics $T_J = 25^{\circ}C$, unless otherwise noted



b V_{GS}, Gate-to-Source Voltage (V)

Figure 8. Gate Charge

10

V_{DD} = 12V

7.5

V_{DD} = 30V

5

V_{DD} = 48V

2.5

Q_g, Total Gate Charge (nC)

60

Figure 9. Transient Thermal Impedance

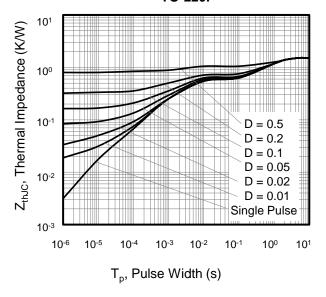


Figure 10. Transient Thermal Impedance TO-251,TO-252,TO-220

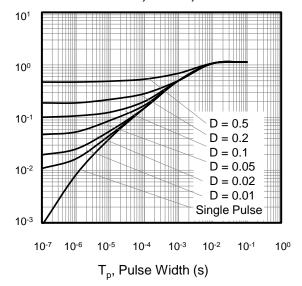


Figure A: Gate Charge Test Circuit and Waveform

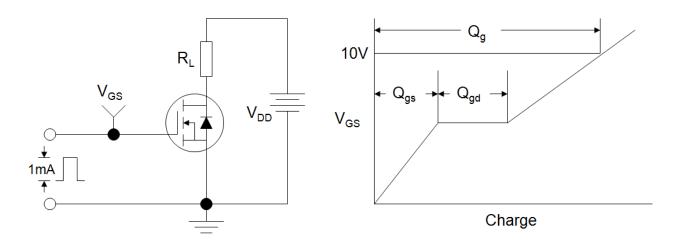


Figure B: Resistive Switching Test Circuit and Waveform

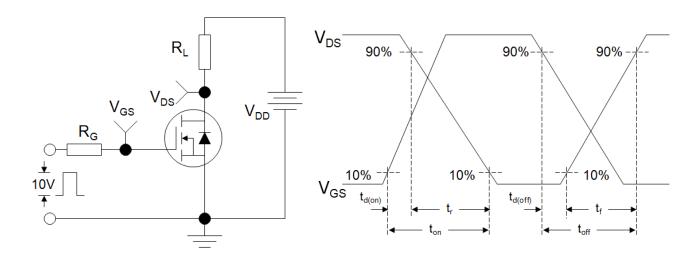
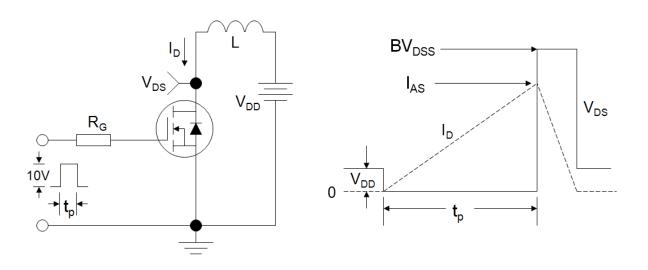
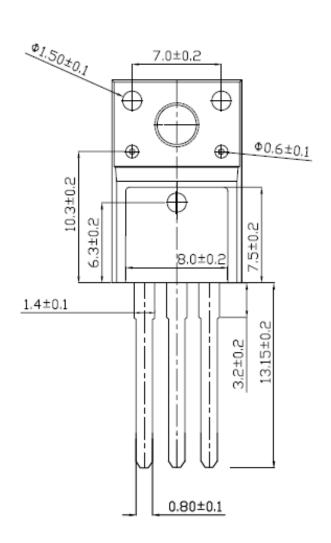


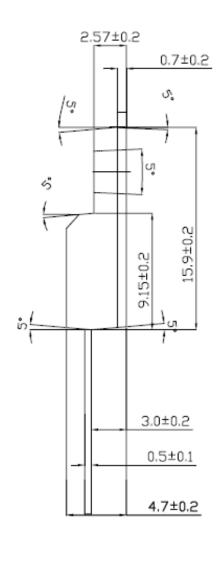
Figure C: Unclamped Inductive Switching Test Circuit and Waveform





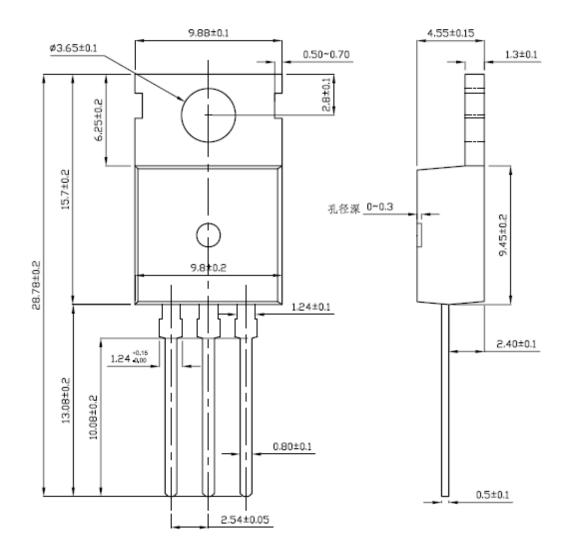
TO-220F







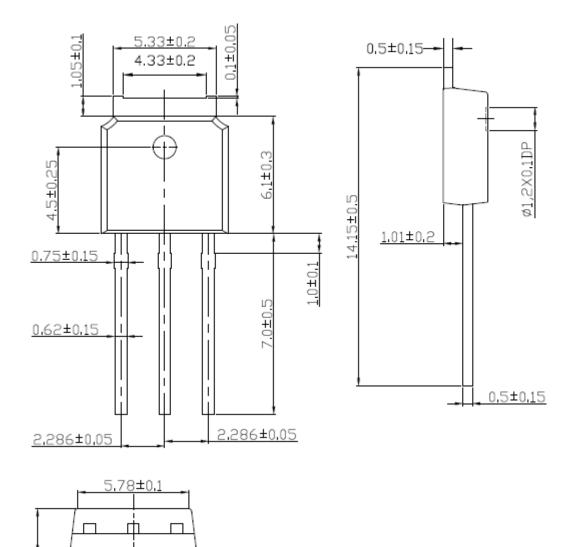
TO-220



7



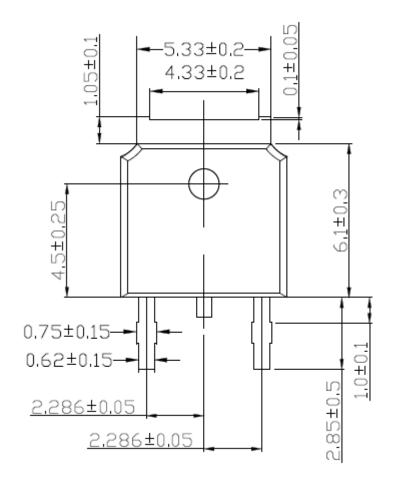
TO-251

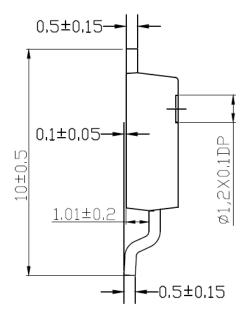


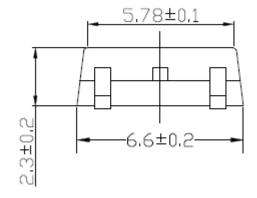
6,6±0,2



TO-252









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