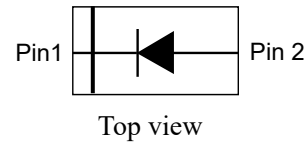


**Feature**

- Small mold type. (DFN1608-2L)
- Low  $I_R$
- High reliability.


**Applications**

- Low current rectification

**Construction**

- Silicon epitaxial planar

**Mechanical Characteristics**

- Lead finish:100% matte Sn(Tin)
- Mounting position: Any
- Qualified max reflow temperature:260°C
- Device meets MSL 1 requirements
- Pure tin plating: 7 ~ 17 um
- Pin flatness : ≤3mil

**Electrical characteristics per line@25°C**

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Forward voltage	$V_F$	-	0.53	0.56	V	$I_F=1A$
Forward voltage	$V_F$	-	0.60	0.65	V	$I_F=1.5A$
Reverse current	$I_R$	-	-	50	uA	$V_R=40V$
Junction Capacitance	$C_j$	-	150	-	pF	$V_R=0V, f=1MHz$
Reverse Recovery Time	$t_{rr}$		25		ns	$I_F=0.5A, I_R=1.0A, I_{rr}=0.25A$

**Absolute maximum rating@25°C**

Parameter	Symbol	limits	Unit
Reverse voltage (DC)	$V_{RM}$	40	V
Average rectified forward current	$I_o$	2	A
Typical Thermal Resistance Junction to Ambient	$R_{thja}$	80	°C/W
Peak Forward Surge Current 8.3ms Single Half Sine Wave Superimposed on Rated Load	$I_{FSM}$	12	A
Repetitive Peak Forward Current (Pulse Wave=10ms, Duty Cycle = 25%)	$I_{FRM}$	5	A
Operating and Storage Temperature Range	$T_j, T_{stg}$	-55 to 125	°C

Typical Characteristics

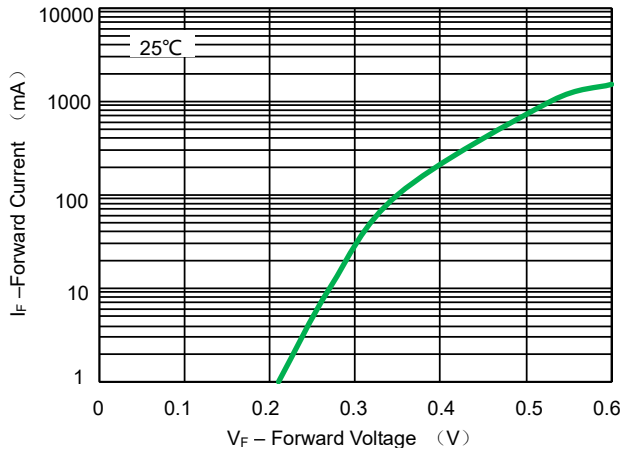


Fig 1. Forward Voltage

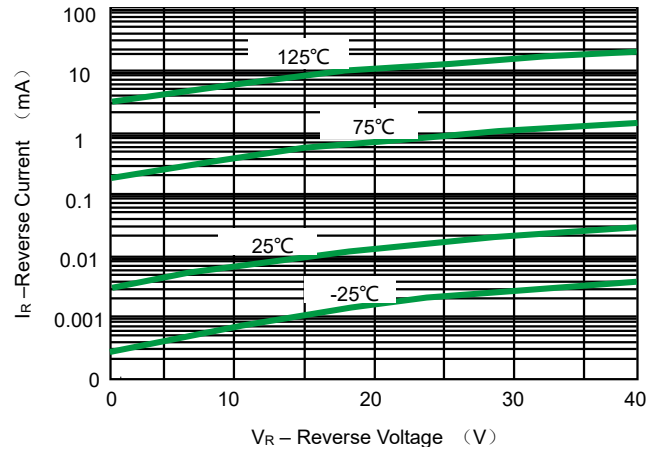
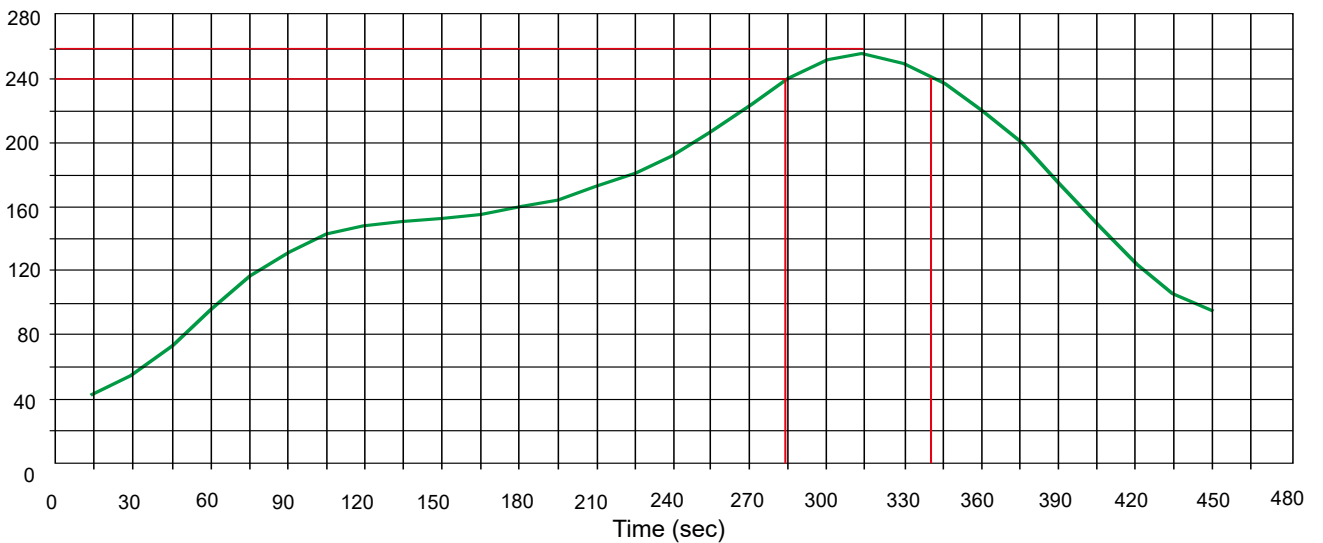


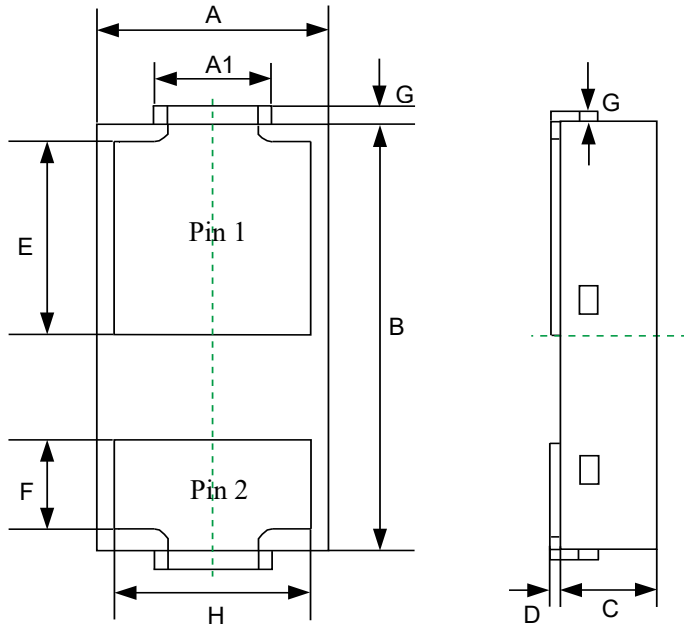
Fig 2. Leakage Current

Solder Reflow Recommendation

Peak Temp=257°C, Ramp Rate=0.802deg. °C/sec

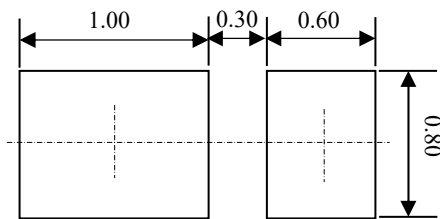


Product dimension (DFN1608-2L)



Bottom view

Dim	Millimeters	
	MIN	MAX
A	0.75	0.85
A1	0.00	0.25
B	1.55	1.65
C	0.46	0.55
D	0.00	0.04
E	0.72	0.80
F	0.32	0.40
G	0.00	0.10
H	0.67	0.75



Notes: This recommended land pattern is for reference purposes only. Please consult your manufacturing group to ensure your PCB design guidelines are met.


Ordering information

Device	Package	Shipping
PSBD2ED40V2H	DFN1608-2L(Pb-free)	5000 / Tape & Reel

Marking




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