

550V N-Channel MOSFET

FEATURES

- Fast switching
- 100% avalanche tested
- Improved dv/dt capability

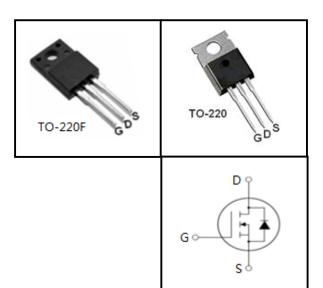
APPLICATIONS

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)

Device Marking and Package Information			
Device	Package	Marking	
CS7N55F	TO-220F	CS7N55F	
CS7N55P	TO-220	CS7N55P	

Absolute Maximum Ratings $T_c = 25^{\circ}C$, unless otherwise noted					
Parameter		Symbol	Va	Unit	
			TO-220F	TO-220	– Unit
Drain-Source Voltage ($V_{GS} = 0V$)		V _{DSS}	550		V
Continuous Drain Current		I _D	7		А
Pulsed Drain Current	(note1)	I _{DM}	28		A
Gate-Source Voltage		V _{GSS}	±	30	V
Single Pulse Avalanche Energy	(note2)	E _{AS}	112		mJ
Avalanche Current	(note1)	I _{AS}	4.8		A
Repetitive Avalanche Energy	(note1)	E _{AR}	67		mJ
Power Dissipation (T _C = 25 ^o C)		P _D	25	70	W
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55~+150		°C

Thermal Resistance				
Peremeter	Symbol	Va	Unit	
Parameter	Symbol	TO-220F	TO-220	- Unit
Thermal Resistance, Junction-to-Case	R _{thJC}	5	1.78	K/W
Thermal Resistance, Junction-to-Ambient	R_{thJA}	62.5	60	r./ v v





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Specifications $T_J = 25^{\circ}C$, unless otherwise noted							
Parameter	Symbol	Test Conditions	Value			11	
		Test conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_{D} = 250 \mu A$	550			V	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 550V, V_{GS} = 0V, T_{J} = 25^{\circ}C$			1	μA	
Gate-Source Leakage	I _{GSS}	V_{GS} = $\pm 30V$			±100	nA	
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250 \mu A$	3		4	V	
Drain-Source On-Resistance (Note3)	$R_{DS(on)}$	V _{GS} = 10V, I _D =3.5A		1.1	1.3	Ω	
Dynamic							
Input Capacitance	C _{iss}	V _{GS} = 0V,		603		pF	
Output Capacitance	C _{oss}	$V_{DS} = 25V,$		71			
Reverse Transfer Capacitance	C _{rss}	f = 1.0MHz		10			
Total Gate Charge	Q _g			19		nC	
Gate-Source Charge	Q_gs	$V_{DD} = 440V, I_D = 7A, V_{GS} = 10V$		3.2			
Gate-Drain Charge	Q_{gd}	55		9			
Turn-on Delay Time	t _{d(on)}			37			
Turn-on Rise Time	t _r	V _{DD} = 275V, I _D = 7A,		19		ns	
Turn-off Delay Time	t _{d(off)}	$R_{\rm G} = 25 \Omega$		82			
Turn-off Fall Time	t _f			40			
Drain-Source Body Diode Character	istics						
Continuous Body Diode Current	۱ _s				7	А	
Pulsed Diode Forward Current	I _{SM}	T _C = 25 °C			28		
Body Diode Voltage	V _{SD}	T _J = 25°C, I _{SD} = 3.5A, V _{GS} = 0V			1.4	V	
Reverse Recovery Time	t _{rr}	V _{GS} = 0V,I _S = 7A,		505		ns	
Reverse Recovery Charge	Q _{rr}	$di_F/dt = 100A /\mu s$		1.5		uC	

Notes

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature
- 2. L=10mH, V_{DD} = 50V, R_G = 25 Ω , Starting T_J = 25 °C
- 3. Pulse Test: Pulse width \leq 300µs, Duty Cycle \leq 1%



Typical Characteristics $T_J = 25^{\circ}C$, unless otherwise noted

Figure 1. Output Characteristics ($T_J = 25^{\circ}C$)

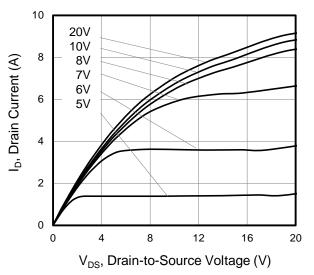
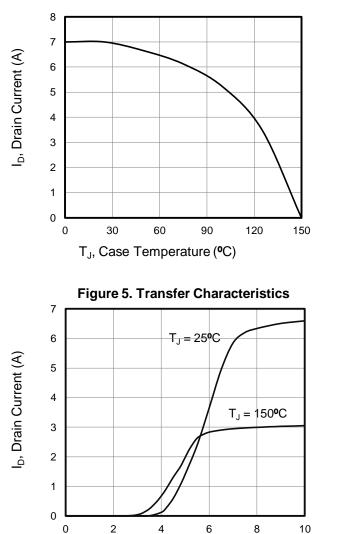


Figure 3. Drain Current vs. Temperature



V_{GS}, Gate-to-Source Voltage (V)

Figure 2. Body Diode Forward Voltage

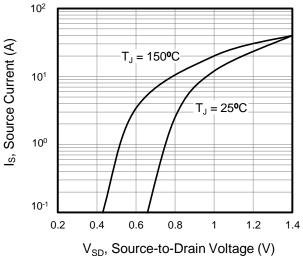


Figure 4. BV_{DSS} Variation vs. Temperature

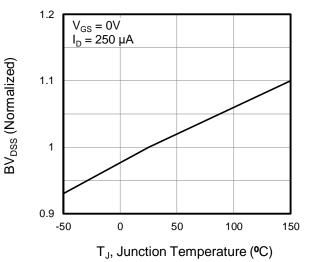
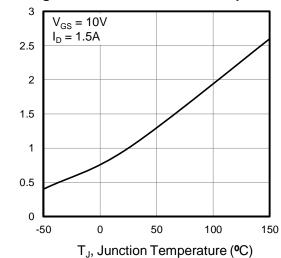


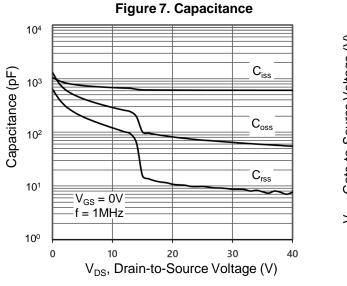
Figure 6. On-Resistance vs. Temperature



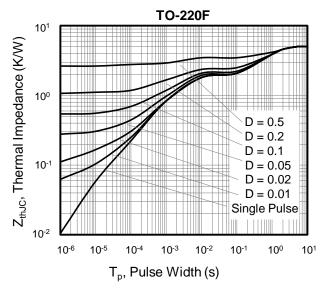
R_{DS(on)}, On-Resistance (Normalized)



Typical Characteristics $T_J = 25^{\circ}C$, unless otherwise noted







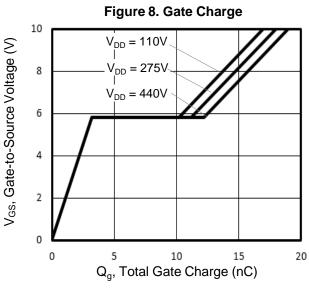
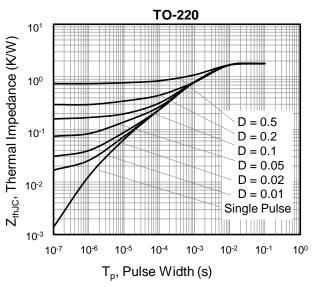


Figure 10. Transient Thermal Impedance





CS7N55F, CS7N55P



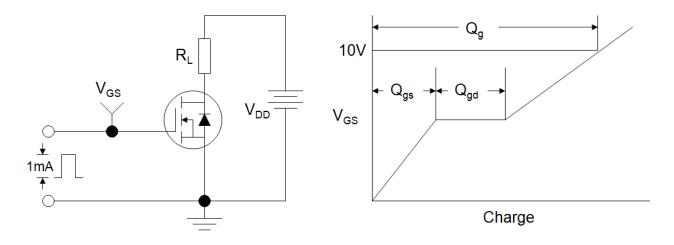


Figure B: Resistive Switching Test Circuit and Waveform

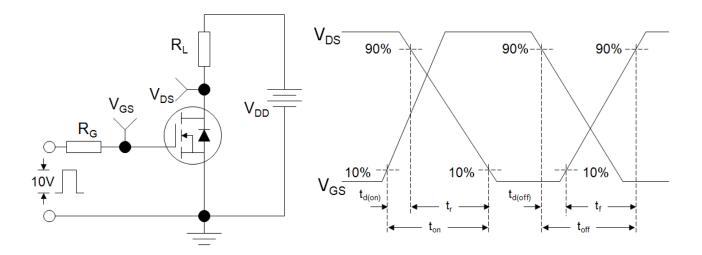
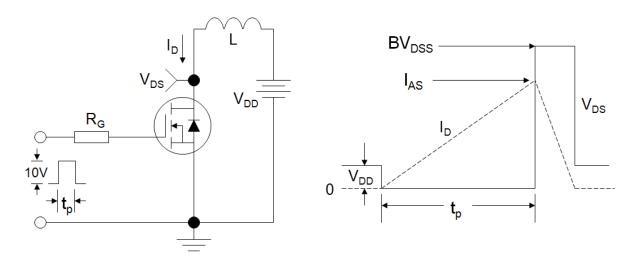
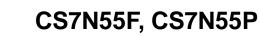


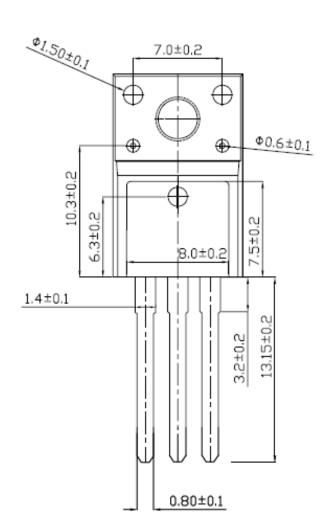
Figure C: Unclamped Inductive Switching Test Circuit and Waveform

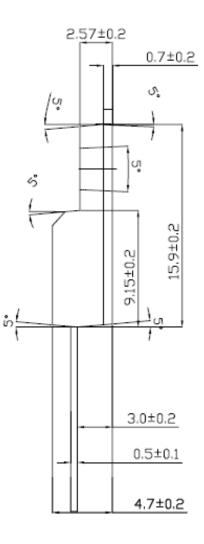






TO-220F

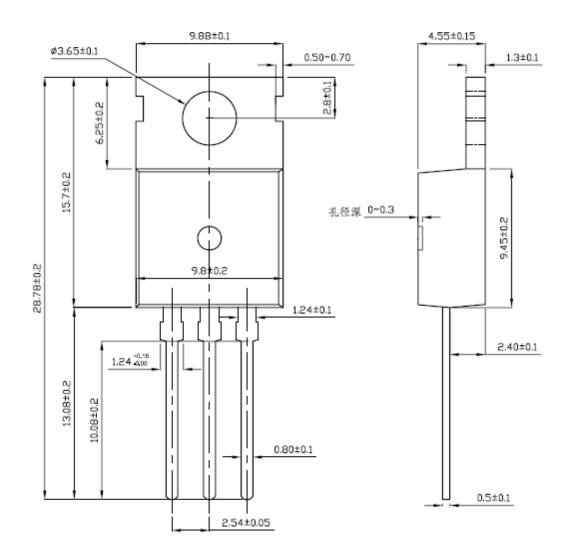








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