

500V N-Channel MOSFET

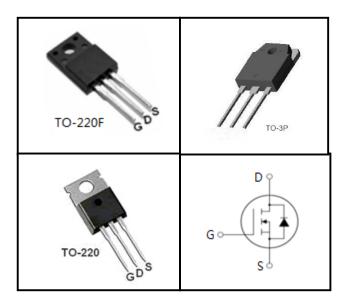
FEATURES

- Fast switching
- 100% avalanche tested
- Improved dv/dt capability

APPLICATIONS

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)

Device Marking and Package Information				
Device	Package	Marking		
CS18N50F	TO-220F	CS18N50F		
CS18N50P	TO-220	CS18N50P		
CS18N50V	ТО-ЗР	CS18N50V		



Absolute Maximum Ratings $T_c = 25^{\circ}C$, unless otherwise noted						
Parameter	Symbol	Value			Unit	
		TO-220F	TO-3P	TO-220		
Drain-Source Voltage ($V_{GS} = 0V$)	V _{DSS}	500		V		
Continuous Drain Current	Ι _D	18		А		
Pulsed Drain Current (note1)	I _{DM}	72		А		
Gate-Source Voltage	V _{GSS}	±30		V		
Single Pulse Avalanche Energy (note2)	E _{AS}	980		mJ		
Avalanche Current (note1)	I _{AS}	14		А		
Repetitive Avalanche Energy (note1)	E _{AR}	588		588		mJ
Power Dissipation ($T_c = 25^{\circ}C$)	P _D	98	160)	W	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	-55~+150		°C		

Thermal Resistance					
Devementer	Symbol	Value			11
Parameter		TO-220F	TO-3P	TO-220	Unit
Thermal Resistance, Junction-to-Case	R_{thJC}	1.27	0.6		к/w
Thermal Resistance, Junction-to-Ambient	R_{thJA}	62.5	40		N/ VV



CS18N50F,CS18N50P,CS18N50V

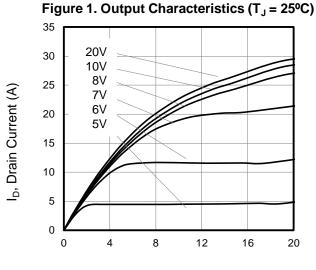
Specifications $T_J = 25^{\circ}C$, unless otherwise noted							
Parameter	Symbol Toot Conditions		Value			11 14	
Farameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0V, I_D = 250\mu A$	500			V	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 500V, V_{GS} = 0V, T_{J} = 25^{\circ}C$			1	μΑ	
Gate-Source Leakage	I _{GSS}	V_{GS} = $\pm 30V$			±100	nA	
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \mu A$	3.0		4.0	V	
Drain-Source On-Resistance (Note3)	R _{DS(on)}	$V_{GS} = 10V, I_{D} = 9A$		0.28	0.34	Ω	
Dynamic							
Input Capacitance	C _{iss}	– V _{GS} = 0V,		2367		pF	
Output Capacitance	C _{oss}	$V_{DS} = 25V,$		228			
Reverse Transfer Capacitance	C _{rss}	f = 1.0MHz		15			
Total Gate Charge	Q _g			53.4		nC	
Gate-Source Charge	Q _{gs}	$V_{DD} = 400 V, I_D = 18 A, V_{GS} = 10 V$		10			
Gate-Drain Charge	Q _{gd}			20			
Turn-on Delay Time	t _{d(on)}			51.3			
Turn-on Rise Time	t _r	V _{DD} = 250V, I _D =18A,		36.5		ns	
Turn-off Delay Time	t _{d(off)}	$R_{G} = 25 \ \Omega$		232			
Turn-off Fall Time	t _f			61			
Drain-Source Body Diode Character	ristics						
Continuous Body Diode Current	۱ _s	T 0500			18	A	
Pulsed Diode Forward Current	I _{SM}	T _C = 25 °C			72		
Body Diode Voltage	V_{SD}	$T_J = 25^{\circ}C, I_{SD} = 9A, V_{GS} = 0V$			1.4	V	
Reverse Recovery Time	t _{rr}	V _{GS} = 0V,I _S = 18A,		497		ns	
Reverse Recovery Charge	Q _{rr}	di _F /dt =100A /µs		4		μC	

Notes

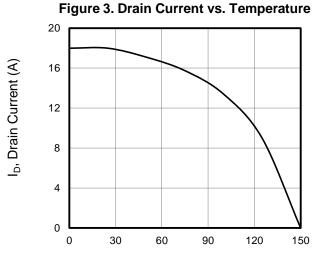
- 1. Repetitive Rating: Pulse width limited by maximum junction temperature
- 2. L=10mH, V_{DD} = 50V, R_G = 25 $\Omega,$ Starting T_J = 25 °C
- 3. Pulse Test: Pulse width \leq 300µs, Duty Cycle \leq 1%



Typical Characteristics $T_J = 25^{\circ}C$, unless otherwise noted

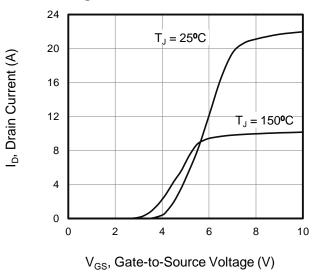


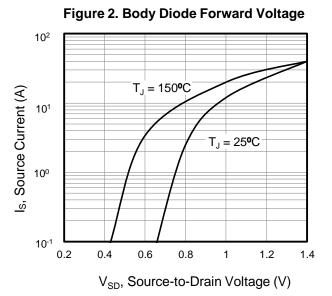
 V_{DS} , Drain-to-Source Voltage (V)



T_C, Case Temperature (A)









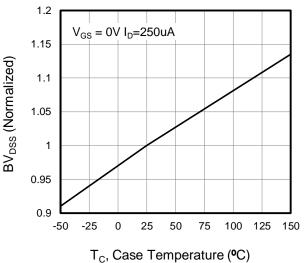
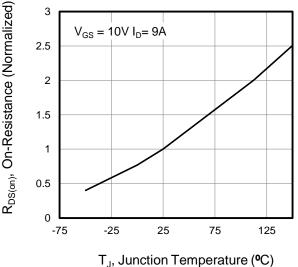
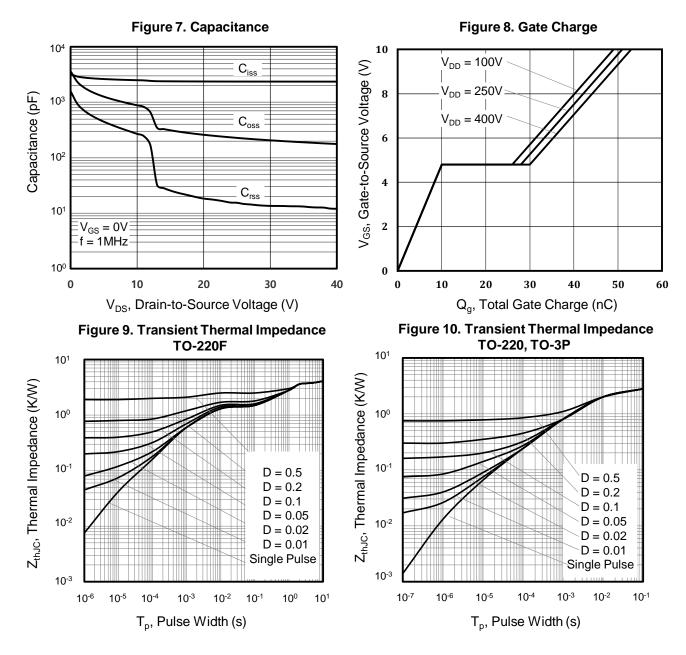


Figure 6. On-Resistance vs. Temperature





Typical Characteristics $T_J = 25^{\circ}C$, unless otherwise noted







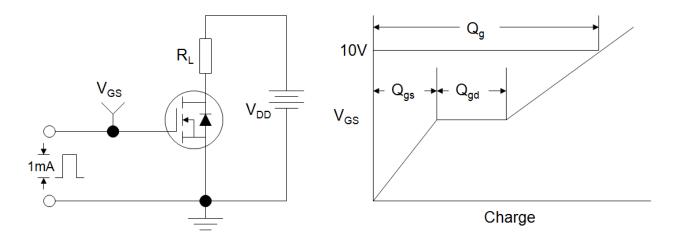


Figure B: Resistive Switching Test Circuit and Waveform

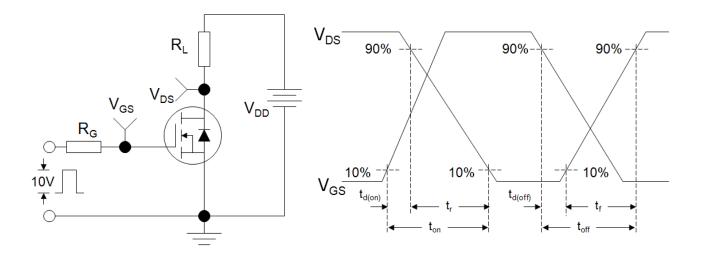
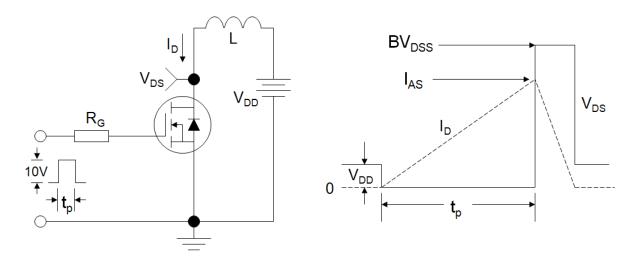
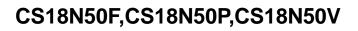


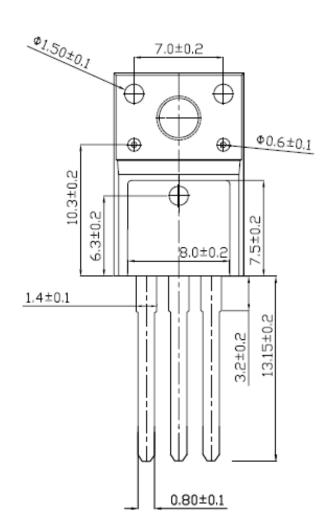
Figure C: Unclamped Inductive Switching Test Circuit and Waveform

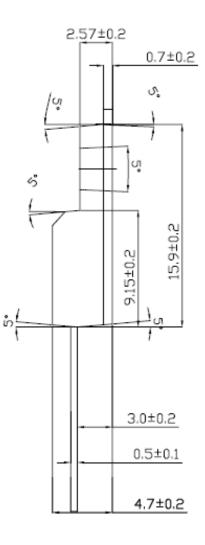






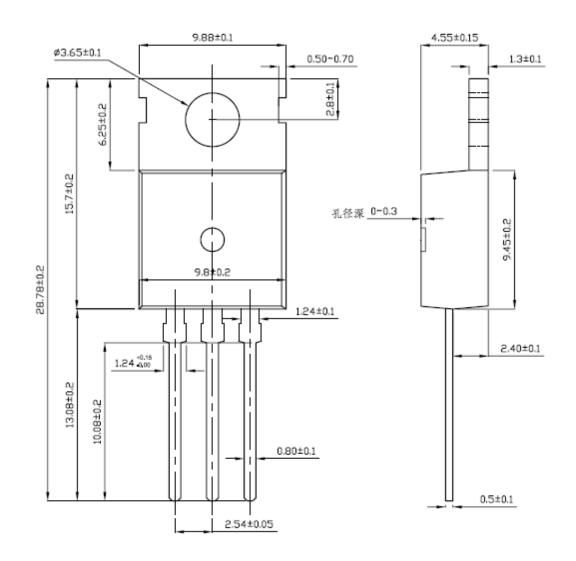
TO-220F







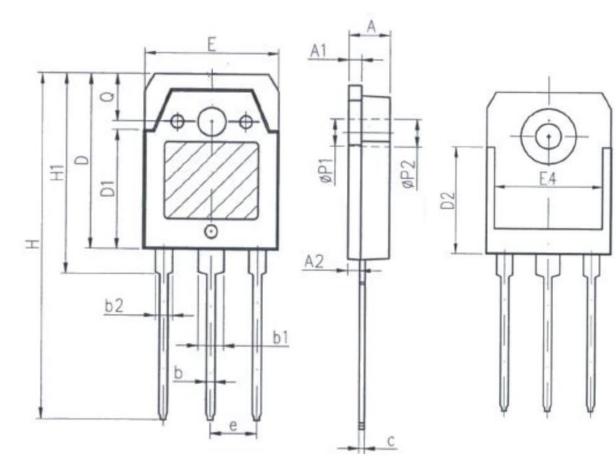
TO-220



CS25N50F,CS25N50V



TO-3P



Unit:mm				
Symbol	Min.	Max.		
Α	4.6	5		
A1	1.4	1.65		
A2	1. 18	1. 58		
b	0.8	1.2		
b 1	2.8	3.2		
b2	1.8	2.2		
с	0.5	0.75		
D	19.6	20.2		
D1	13.55	14. 25		
D2	12. 9REF			
E	15.35	15.85		
E4	12.6	-		
е	5. 45TYP			
Н	40.1	40.9		
H1	23.15	23.65		
P1	3. 2REF			
P2	3. 5REF			



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