

500V N-Channel MOSFET

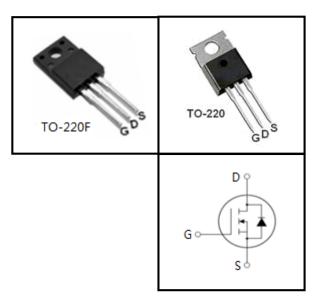
FEATURES

- Fast switching
- 100% avalanche tested
- Improved dv/dt capability

APPLICATIONS

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)

Device Marking and Package Information			
Device	Package	Marking	
CS15N50F	TO-220F	CS15N50F	
CS15N50P	TO-220	CS15N50P	



Absolute Maximum Ratings $T_c = 25^{\circ}C$, unless otherwise noted					
Beromotor	Cumhal	Val			
Parameter	Symbol	TO-220F	TO-220	Unit	
Drain-Source Voltage (V _{GS} = 0V)	V _{DSS}	500		V	
Continuous Drain Current	I _D	15		A	
Pulsed Drain Current (note1)	I _{DM}	60		А	
Gate-Source Voltage	V _{GSS}	±30		V	
Single Pulse Avalanche Energy (note2)	E _{AS}	352.8		mJ	
Avalanche Current (note1)	I _{AS}	8.4		А	
Repetitive Avalanche Energy (note1)	E _{AR}	229.3		mJ	
Power Dissipation ($T_c = 25^{\circ}C$)	P _D	32	65	W	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	-55~+150		°C	

Thermal Resistance				
Devemeter	Symbol	Va	l l mit	
Parameter		TO-220F	TO-220	- Unit
Thermal Resistance, Junction-to-Case	R _{thJC}	1.92	0.89	
Thermal Resistance, Junction-to-Ambient	R _{thJA}	62.5	60	K/W



CS15N50F,CS15N50P

Specifications T _J = 25°C, unless otherwise noted								
Parameter	Symbol	Test Conditions	Value			Unit		
		Test conditions	Min.	Тур.	Max.	Unit		
Static					_			
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_{D} = 250 \mu A$	500			V		
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 500V, V_{GS} = 0V, T_{J} = 25^{o}C$	-		1	μA		
Gate-Source Leakage	I _{GSS}	V_{GS} = $\pm 30V$			±100	nA		
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \mu A$	3.0		4.0	V		
Drain-Source On-Resistance (Note3)	$R_{DS(on)}$	V _{GS} = 10V, I _D = 7.5A		0.35	0.42	Ω		
Dynamic								
Input Capacitance	C _{iss}			1752		pF		
Output Capacitance	C _{oss}	$V_{GS} = 0V,$ $V_{DS} = 25V,$		185				
Reverse Transfer Capacitance	C _{rss}	f = 1.0MHz		13.5				
Total Gate Charge	Q _g	V _{DD} = 400V, I _D = 15A, V _{GS} = 10V		44		nC		
Gate-Source Charge	Q _{gs}			7.5				
Gate-Drain Charge	Q _{gd}	65		19				
Turn-on Delay Time	t _{d(on)}	V _{DD} = 250V, I _D =15A,		45.5		ns		
Turn-on Rise Time	t _r			27				
Turn-off Delay Time	t _{d(off)}	$R_{\rm G} = 25 \Omega$		193				
Turn-off Fall Time	t _f			45				
Drain-Source Body Diode Character	istics							
Continuous Body Diode Current	۱ _s				15	A		
Pulsed Diode Forward Current	I _{SM}	T _C = 25 °C			60			
Body Diode Voltage	V _{SD}	T _J = 25°C, I _{SD} = 7.5A, V _{GS} = 0V			1.4	V		
Reverse Recovery Time	t _{rr}	V _{GS} = 0V,I _S = 15A,		472		ns		
Reverse Recovery Charge	Q _{rr}	di _F /dt =100A /µs		3.24		μC		

Notes

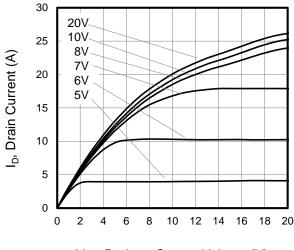
- 1. Repetitive Rating: Pulse width limited by maximum junction temperature
- 2. L = 10.0mH, V_{DD} = 50V, R_{G} = 25 Ω , Starting T_{J} = 25 °C
- 3. Pulse Test: Pulse width \leq 300µs, Duty Cycle \leq 1%



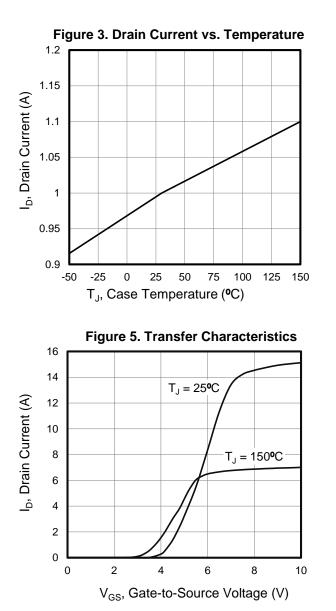
Typical Characteristics $T_J = 25^{\circ}C$, unless otherwise noted

Figure 1. Output Characteristics (T_J = 25°C)





V_{DS}, Drain-to-Source Voltage (V)



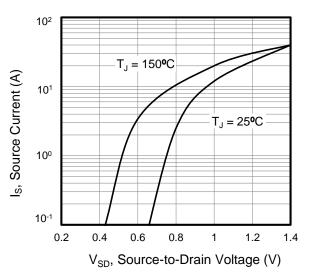
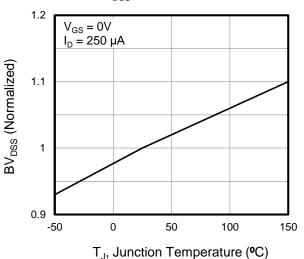
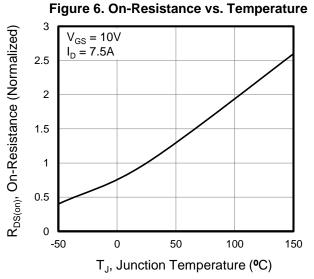


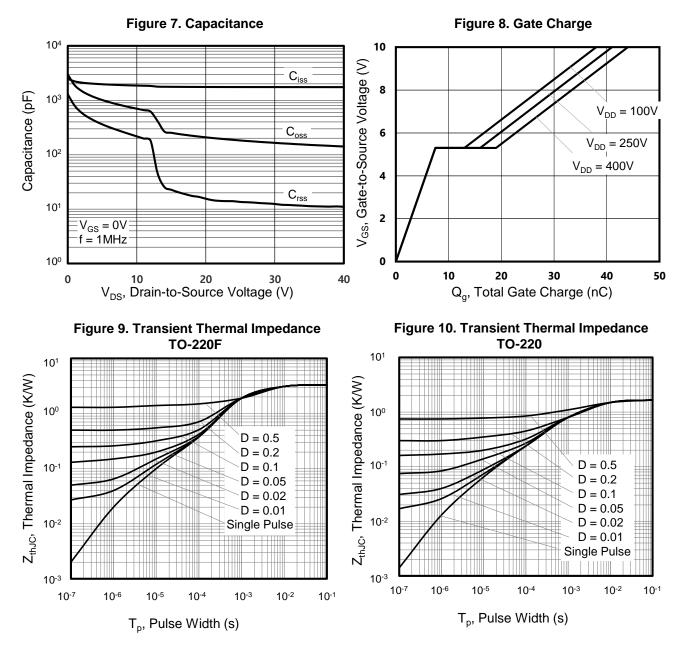
Figure 4. BV_{DSS} Variation vs. Temperature







Typical Characteristics $T_J = 25^{\circ}C$, unless otherwise noted





CS15N50F,CS15N50P



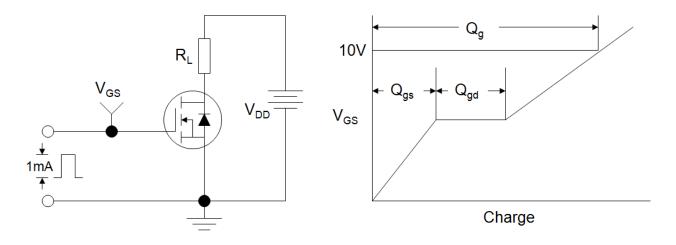


Figure B: Resistive Switching Test Circuit and Waveform

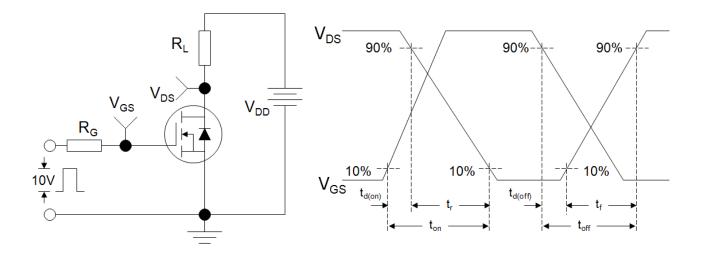
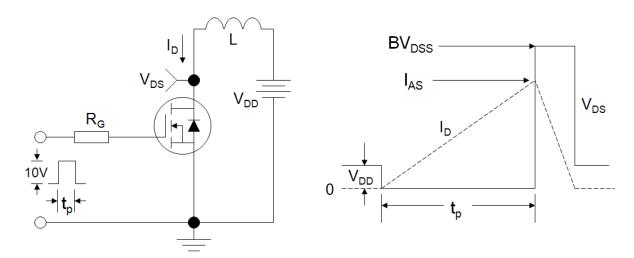


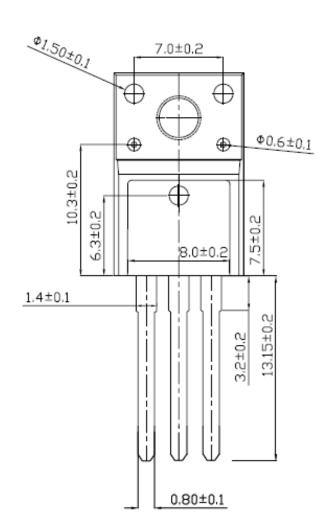
Figure C: Unclamped Inductive Switching Test Circuit and Waveform

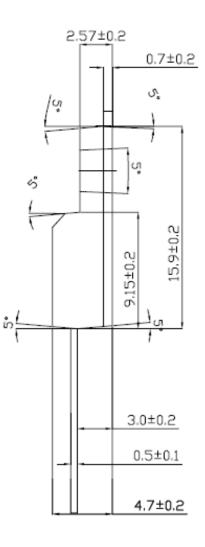






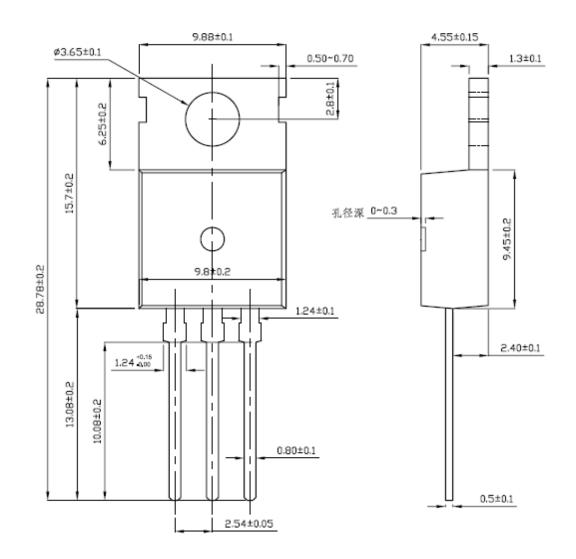
TO-220F







TO-220





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