

600V Super-junction Power MOSFET

Description

600V Super-junction Power MOSFET

Super-junction power MOSFET is a revolutionary technology for high voltage power MOSFETs, designed according to the SJ principle and pioneered. The Multi-EPI SJ MOSFET provide an extremely fast and robust body diode. Also provide an extremely low switching, communication and conduction losses device with highest robustness make especially resonant switching applications more reliable, more efficient, lighter and cooler, designed by Wuxi Unigroup Microelectronics Company.

Features		Applications		
 Features Ultra-fast body diode Very low FOM R_{DS(on)}×Q_g Easy to use/drive 100% avalanche tested RoHS compliant TO-220FP-NL TO-247 		 Applications Switch Mode Power Supply (SMPS) Uninterruptible Power Supply (UPS) Power Factor Correction (PFC) LLC Half-bridge Charger 		
Gos	G D S	Gate Gate Sour	Ce RoHS	
Device Marking and Pack	Package		Marking	
TPR60R120MFD	TO-220FP-NL		60R120MFD	
TPW60R120MFD	TO-247		60R120MFD	
Key Performance Paran	neters			
Parameter	Value		Unit	
V _{DS} @ T _{j,max}	650		V	
R _{DS(on),max}	0.12		Ω	
Q _{g,typ}	58.1		nC	
I _D	30		A	
I _{D,pulse}	90		A	
E _{oss} @ 400V	7.8		μJ	
Body Diode di _F /dt	900		A/µs	
t _{rr}	209		ns	
Q _{rr}	1.3		μC	
I _{rm}	12.6		A	



Absolute Maximum Ratings $T_c = 25^{\circ}C$, unless otherwise noted						
Parameter			Symbol	Values	Unit	
	T _C = 25°C		- I _D	30	•	
Continuous Drain Current	T _C = 100°C			18	A	
Pulsed Drain Current	(no	:e1)	I _{D,pulse}	90	А	
Gate-Source Voltage			V _{GSS}	±30V	V	
Single Pulse Avalanche Energy	(not	e2)	E _{AS}	636	mJ	
Repetitive Avalanche Energy (note2)		e2)	E _{AR}	0.96	mJ	
Avalanche Current			I _{AR}	5.2	А	
MOSFET dv/dt Ruggedness, V _{DS} = 0480V			dv/dt	50	V/ns	
Power Dissipation For TO-220FP-NL			P _D	34		
Power Dissipation For TO-247				219	- W	
Continuous Diode Forward Current			I _S	30		
Diode Pulsed Current (note1)		te1)	I _{S,pulse}	90	- A	
Reverse Diode dv/dt (note3)		te3)	dv/dt	50	V/ns	
Maximum Diode Commutation Speed (note3)		te3)	di _f /dt	900	A/µs	
Operating Junction and Storage Temperature Range			T _J , T _{stg}	-55~+150	°C	

Thermal Resistance For TO-220FP-NL				
Parameter	Symbol	Value	Unit	
Thermal Resistance, Junction-to-Case	R _{thJC}	3.65	°C/W	
Thermal Resistance, Junction-to-Ambient	R _{thJA}	80	-0/00	

Thermal Resistance For TO-247				
Parameter	Symbol	Value	Unit	
Thermal Resistance, Junction-to-Case	R _{thJC}	0.57	°C/W	
Thermal Resistance, Junction-to-Ambient	R _{thJA}	62	-0/00	

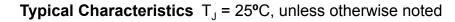


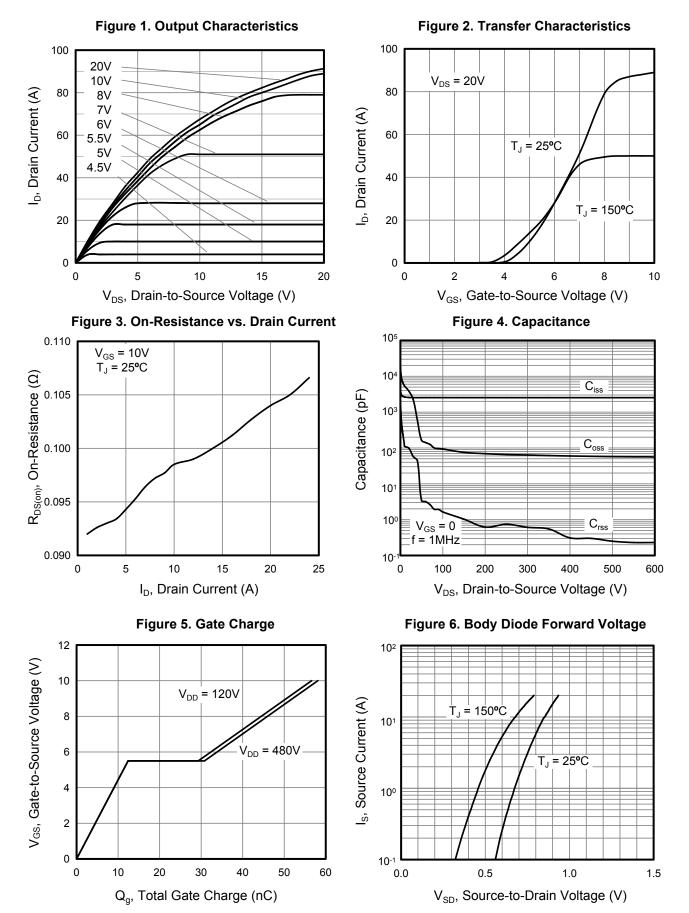
Electrical Characteristics	T _J = 25°C,	unless otherwise noted				
Parameter			Value			
Parameter	Symbol	bol Test Conditions		Тур.	Max.	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V_{GS} = 0V, I_{D} = 250µA	600			V
Zero Gate Voltage Drain Current	I _{DSS}	V_{DS} = 600V, V_{GS} = 0V, T_{J} = 25°C			3.5	μA
Gate-Source Leakage Current	I _{GSS}	$V_{GS} = \pm 30V$			±100	nA
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$	3.0		5.0	V
Drain-Source On-State-Resistance	$R_{DS(on)}$	V _{GS} = 10V, I _D = 15A		0.105	0.12	Ω
Gate Resistance	R _G	f = 1.0MHz open drain		1.74		Ω
Dynamic Characteristics						
Input Capacitance	C _{iss}	\/ − 0\/		2524		pF
Output Capacitance	C _{oss}	$V_{GS} = 0V,$ $V_{DS} = 100V$		94.1		
Reverse Transfer Capacitance	C _{rss}	f = 1.0MHz		1.71		
Total Gate Charge	Qg	V = 480V		58.1		nC
Gate-Source Charge	Q _{gs}	$V_{DD} = 480V ,$ $I_{D} = 30A ,$		12.4		
Gate-Drain Charge	Q_{gd}	$V_{GS} = 10V$		18.4		
Turn-on Delay Time	t _{d(on)}			57.8		
Turn-on Rise Time	t _r	$V_{DD} = 400V$		49.3		
Turn-off Delay Time	t _{d(off)}	$I_D = 30A$ $R_G = 25\Omega$		201.4		ns
Turn-off Fall Time	t _f			64.1		
Drain-Source Body Diode Characte	eristics					
Body Diode Forward Voltage	V_{SD}	T _J = 25°C, I _{SD} = 15A, V _{GS} = 0V		0.9	1.5	V
Reverse Recovery Time	t _{rr}			110		ns
Reverse Recovery Charge	Q _{rr}	V _R = 400V, I _F = 30A, di _F /dt = 100A/µs		0.5		μC
Peak Reverse Recovery Current	I _{rrm}			8.2		А

Notes

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature
- 2. I_D = 10A, V_{DD} = 50V, R_G = 25 Ω , Starting T_J = 25°C
- 3. Identical low side and high side switch with identical ${\sf R}_{\sf G}$

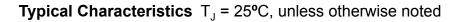


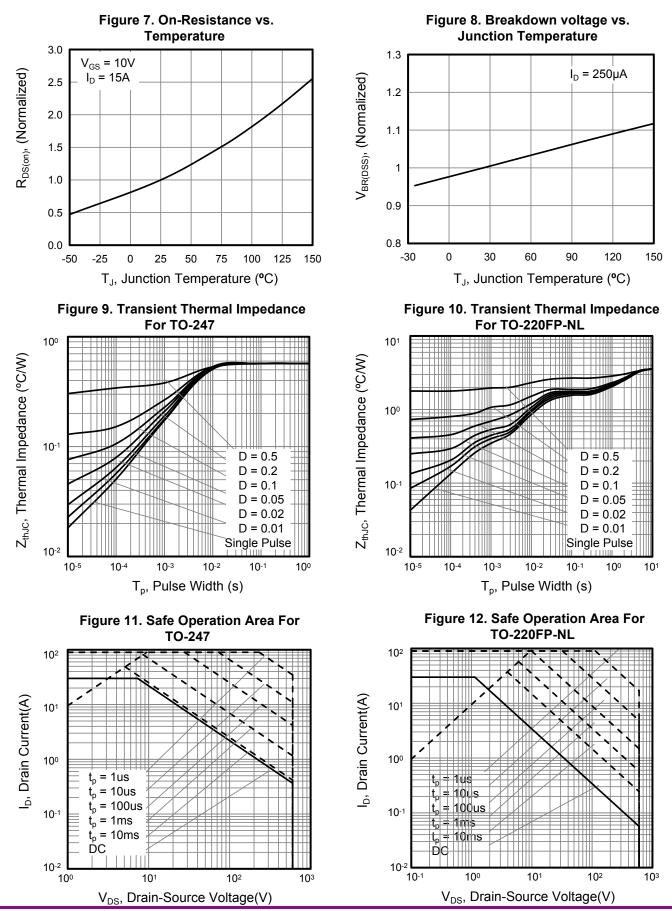




V1.0



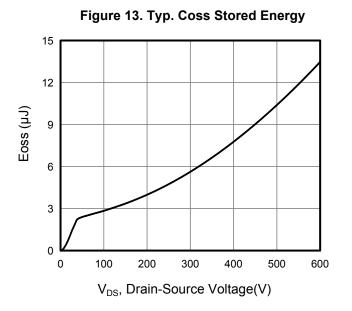


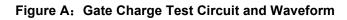






Typical Characteristics $T_J = 25^{\circ}C$, unless otherwise noted





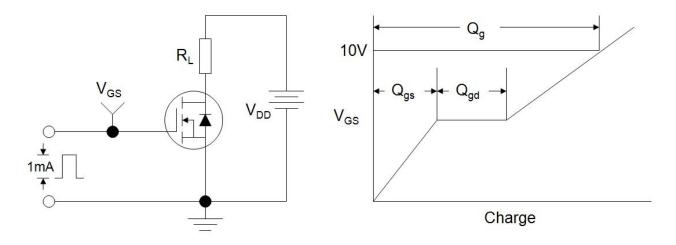


Figure B: Resistive Switching Test Circuit and Waveform

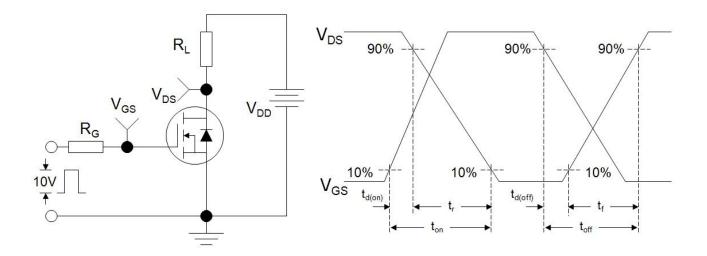
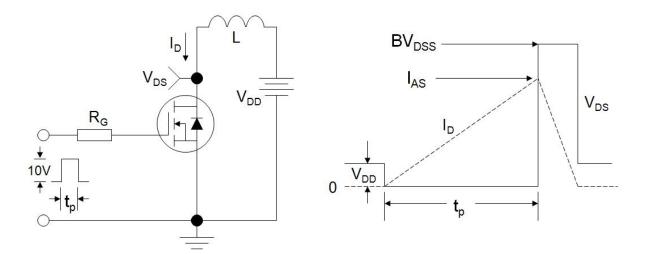
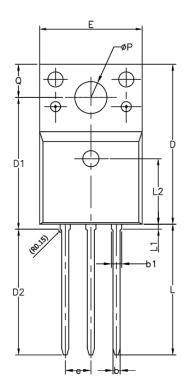


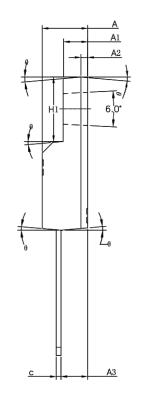
Figure C: Unclamped Inductive Switching Test Circuit and Waveform

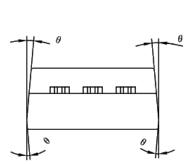




TO-220FP-NL







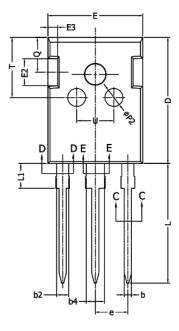
SYMBOL	MIN	NOM	MAX		
А	4.50	4.70	4.83		
A1	2.34	2.54	2.74		
A2		0.70 RI	EF		
A3	2.56	2.76	2.93		
b	0.60	-	0.80		
b1	0.90	Ι	1.10		
С	0.45	0.50	0.60		
D	15.67	15.87	16.07		
D1	12.87	13.07	13.27		
D2	12.28	12.48	12.68		
E	9.96	10.16	10.36		
е	2.54BSC				
H1	6.48	6.68	6.88		
L	12.68	12.98	13.28		
L1	_	_	0.85		
L2	6.50REF				
øР	3.08	3.18	3.28		
Q	3.20	_	3.40		
θ1	1°	3°	5°		

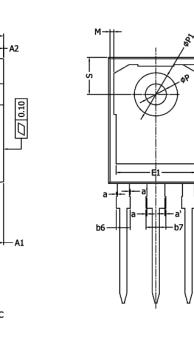


ğ

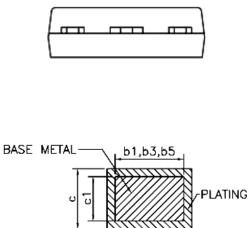
5

TO-247





c



b,b2,b4

SYMBOL	MIN	NOM	MAX
А	4.90	5.00	5.10
A1	2.31	2.41	2.51
A2	1.90	2.00	2.10
а	0		0.15
a'	0		0.15
b	1.16		1.26
b1	1.15	1.2	1.22
b2	1.96		2.06
b3	1.95	2.00	2.02
b4	2.96		3.06
b5	2.96	3.00	3.02
b6			2.25
b7			3.25
с	0.59		0.66
c1	0.58	0.60	0.62
D	20.90	21.00	21.10
D1	16.25	16.55	16.85
D2	1.05	1.17	1.35
E	15.70	15.80	15.90
E1	13.10	13.30	13.50
E2	4.40	4.50	4.60
E3	2.40	2.50	2.60
е		5.436 BSC	
L	19.80	19.92	20.10
L1			4.30
М	0.35		0.95
Р	3.40	3.50	3.60
P1	7.00		7.40
P2	2,40	2.50	2.60
Q	5.60		6.00
S	6.05	6.15	6.25
Т	9,80		10,20
U	6.00		6.40



Disclaimer

All product specifications and data are subject to change without notice.

For documents and material available from this datasheet, Wuxi Unigroup does not warrant or assume any legal liability or responsibility for the accuracy, completeness of any product or technology disclosed hereunder.

No license, express or implied, by estoppels or otherwise, to any intellectual property rights is granted by this document or by any conduct of Wuxi Unigroup.

The products shown herein are not designed for use in medical, life-saving, or life-sustaining applications. Customers using or selling Wuxi Unigroup products not expressly indicated for use in such applications do so entirely at their own risk and agree to fully indemnify Wuxi Unigroup for any damages arising or resulting from such use or sale.

Wuxi Unigroup disclaims any and all liability arising out of the use or application of any product described herein or of any information provided herein to the maximum extent permitted by law. The product specifications do not expand or otherwise modify Wuxi Unigroup's terms and conditions of purchase, including but not limited to the warranty expressed therein, which apply to these products.

Wuxi Unigroup Microelectronics CO., LTD. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.

In the event that any or all Wuxi Unigroup products (including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.

Information (including circuit diagrams and circuit parameters) herein is for example only. It is not guaranteed for volume production. Wuxi Unigroup believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.