

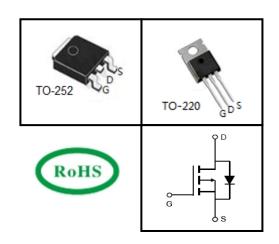
# **40V N-Channel Trench MOSFET**

#### **FEATURES**

- Trench Power MOSFET Technology
- Low R<sub>DS(ON)</sub>
- Low Gate Charge
- Optimized For Fast-switching Applications

#### **APPLICATIONS**

- Synchronous Rectification in DC/DC and AC/DC Converters
- Isolated DC/DC Converters in Telecom and Industrial



Device Marking and Package Information			
Device	Package	Marking	
TTD100N04AT	TO-252	100N04AT	
TTP100N04AT	TO-220	100N04AT	

<b>Absolute Maximum Ratings</b> $T_C = 25^{\circ}C$ , unless otherwise noted			
Barranatas	Compleal	Value	Unit
Parameter	Symbol	TO-252,TO-220	
Drain-Source Voltage (V <sub>GS</sub> = 0V)	V <sub>DSS</sub>	40	V
Continuous Drain Current	I <sub>D</sub>	100	А
Pulsed Drain Current (note	) I <sub>DM</sub>	400	А
Gate-Source Voltage	V <sub>GSS</sub>	±20	V
Single Pulse Avalanche Energy (note2	) E <sub>AS</sub>	86	mJ
Avalanche Current (note1	) I <sub>As</sub>	24	А
Power Dissipation (T <sub>C</sub> = 25°C)	P <sub>D</sub>	120	W
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55~+175	°C

Thermal Resistance				
Boxometox	Oh ad	Value	- Unit	
Parameter	Symbol	TO-252,TO-220		
Thermal Resistance, Junction-to-Case	R <sub>thJC</sub>	1.24	12001	
Thermal Resistance, Junction-to-Ambient	R <sub>thJA</sub>	60	K/W	

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Parameter	Symbol	To at Oos dittions	Value			
		Test Conditions	Min.	Тур.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$V_{GS} = 0V, I_D = 250\mu A$	40			V
Zava Cata Valtana Dunin Cumant		$V_{DS} = 40V, V_{GS} = 0V, T_{J} = 25^{\circ}C$			1	μА
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = 40V, V_{GS} = 0V, T_{J} = 150^{\circ}C$			100	
Gate-Source Leakage	I <sub>GSS</sub>	$V_{GS} = \pm 20V$			±100	nA
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	1.0	1.7	2.4	V
D : 0		$V_{GS} = 10V, I_{D} = 30A$		3.6	4.5	mΩ
Drain-Source On-Resistance (Note3)	$R_{DS(on)}$	$V_{GS} = 4.5V, I_D = 30A$		5.3	7.0	mΩ
Forward Transconductance (Note3)	9 <sub>fs</sub>	V <sub>DS</sub> = 10V, I <sub>D</sub> =20A		27.6		S
Dynamic						
Input Capacitance	C <sub>iss</sub>	V - 0V		6170		
Output Capacitance	C <sub>oss</sub>	$V_{GS} = 0V,$ $V_{DS} = 20V,$		356		pF
Reverse Transfer Capacitance	C <sub>rss</sub>	f = 1.0MHz		266		
Total Gate Charge	$Q_g$	$V_{DD} = 20V, I_{D} = 20A,$ $V_{GS} = 10V$		80		
Gate-Source Charge	$Q_gs$			14		nC
Gate-Drain Charge	$Q_{gd}$	GS -		14		
Turn-on Delay Time	$t_{d(on)}$			16		
Turn-on Rise Time	t <sub>r</sub>	$V_{DD} = 20V, I_D = 20A,$		18		
Turn-off Delay Time	t <sub>d(off)</sub>	$R_G = 3\Omega$		50		ns
Turn-off Fall Time	t <sub>f</sub>			22		
Drain-Source Body Diode Characteris	stics					
Continuous Body Diode Current	I <sub>S</sub>	T 2500			100	^
Pulsed Diode Forward Current	I <sub>SM</sub>	$T_{\rm C} = 25^{\rm o}{\rm C}$			400	Α
Body Diode Voltage	V <sub>SD</sub>	$T_J = 25^{\circ}C$ , $I_{SD} = 20A$ , $V_{GS} = 0V$			1.2	V
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 20A,		40		ns
Reverse Recovery Charge	Q <sub>rr</sub>	$di_F/dt = 100A/\mu s$		44		nC

#### **Notes**

- 1. Repetitive Rating: Pulse Width limited by maximum junction temperature
- 2.  $I_{AS}$  = 24A,  $V_{DD}$  = 20V,  $R_{G}$  = 25 $\Omega$ , Starting  $T_{J}$  = 25 $^{\circ}$ C
- 3. Pulse Test: Pulse Width  $\leq$  300 $\mu$ s, Duty Cycle  $\leq$  1%

### **Typical Characteristics** $T_J = 25^{\circ}\text{C}$ , unless otherwise noted

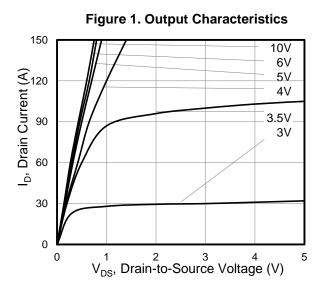
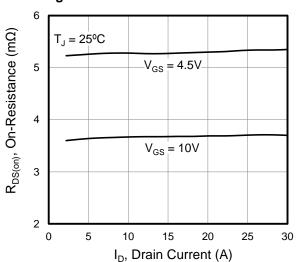
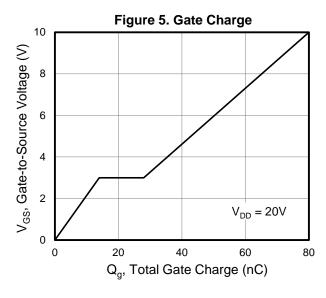


Figure 3. On-Resistance vs. Drain Current





**Figure 2. Transfer Characteristics** 

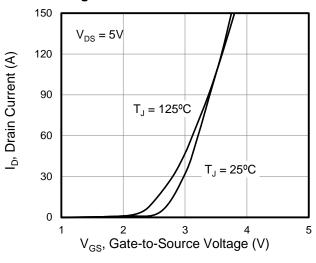


Figure 4. Capacitance

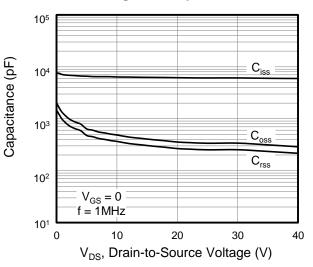
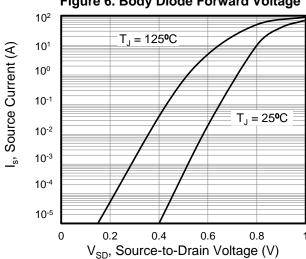


Figure 6. Body Diode Forward Voltage





## **Typical Characteristics** $T_J = 25^{\circ}\text{C}$ , unless otherwise noted

Figure 7. On-Resistance vs.

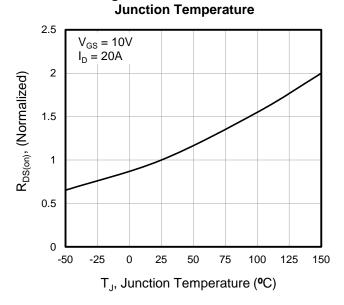


Figure 8. Threshold Voltage vs. Junction Temperature

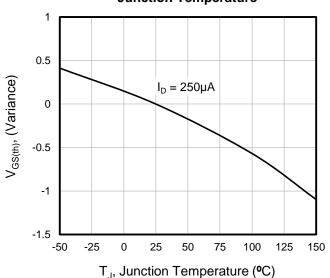
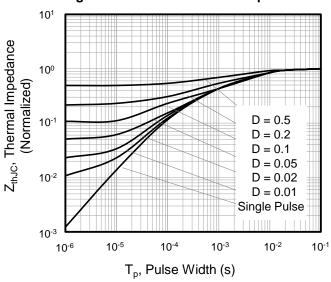


Figure 9. Transient Thermal Impedance



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Figure A: Gate Charge Test Circuit and Waveform

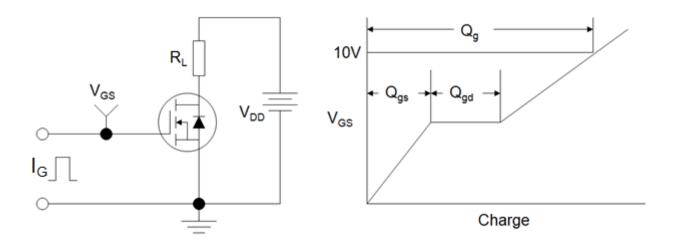


Figure B: Resistive Switching Test Circuit and Waveform

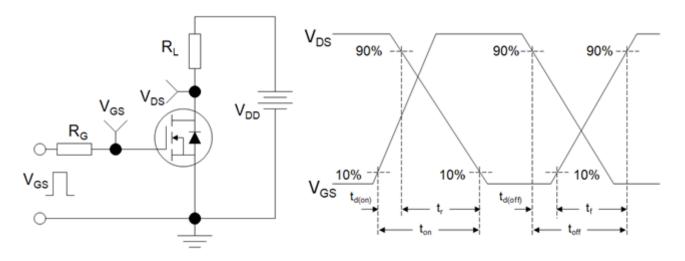
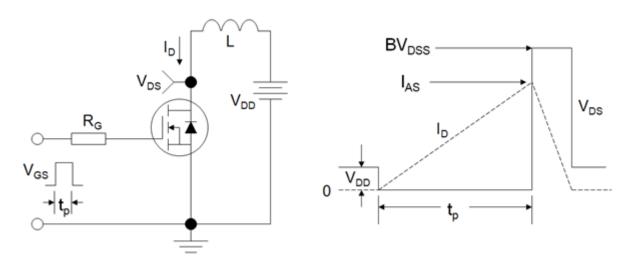
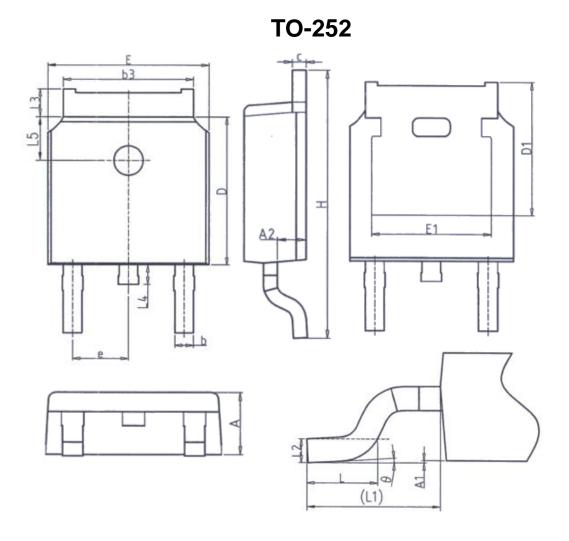


Figure C: Unclamped Inductive Switching Test Circuit and Waveform



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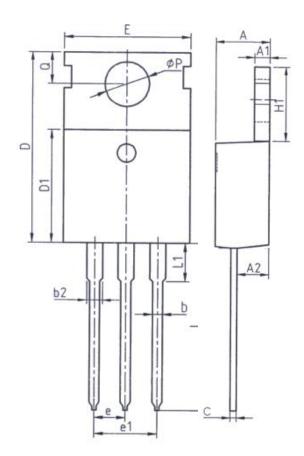


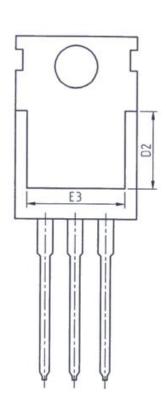


Unit: mm			
Symbol	Min.	Max.	
Α	2. 20	2. 40	
A1	0.00	0. 20	
A2	0. 97	1. 17	
b	0. 68	0.90	
b3	5. 20	5. 50	
С	0. 43	0. 63	
D	5. 98	6. 22	
D1	D1 5. 30REF		
E	6. 40	6. 80	
E1	4. 63	_	

Unit: mm			
Symbol	Min.	Max.	
е	2. 286BSC		
Н	9. 40	10.50	
L	1. 38	1. 75	
L1	2. 90REF		
L2	0. 51BSC		
L3	0.88	1. 28	
L4	_	1.00	
L5	1. 65	1. 95	
θ	0°	8°	

# **TO-220**





Unit: mm		
Symbol	Min.	Max.
Α	4. 37	4. 77
A1	1. 25	1. 45
A2	2. 20	2. 60
b	0. 70	0. 95
b2	1. 17	1. 47
С	0.40	0. 65
D	15. 10	16. 10
D1	8. 80	9. 40
D2	5. 50	_

Unit: mm			
Symbol	Min.	Max.	
E	9. 70	10. 30	
E3	7. 00	-	
е	2. 54BSC		
e1	5. 08BSC		
H1	6. 25	6. 85	
L	12. 75	13.80	
L1	-	3. 40	
P	3. 40	3. 80	
Q	2. 60	3. 00	



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