

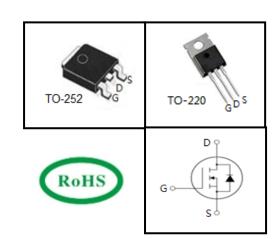
# **40V N-Channel Trench MOSFET**

#### **FEATURES**

- Trench Power MOSFET Technology
- Low R<sub>DS(ON)</sub>
- Low Gate Charge
- Optimized For Fast-switching Applications

#### **APPLICATIONS**

- Synchronous Rectification in DC/DC and AC/DC Converters
- Isolated DC/DC Converters in Telecom and Industrial



Device Marking and Package Information		
Device	Package	Marking
TTD70N04AT	TO-252	70N04AT
TTP70N04AT	TO-220	70N04AT

<b>Absolute Maximum Ratings</b> $T_C = 25^{\circ}C$ , unless otherwise noted				
Barranatar	Symbol	Value		11.7
Parameter		TO-252	TO-220	Unit
Drain-Source Voltage (V <sub>GS</sub> = 0V)	V <sub>DSS</sub>	4	0	V
Continuous Drain Current	I <sub>D</sub>	7	0	А
Pulsed Drain Current (note1)	I <sub>DM</sub>	28	30	А
Gate-Source Voltage	$V_{GSS}$	±	20	V
Single Pulse Avalanche Energy (note2)	E <sub>AS</sub>	66	5.2	mJ
Avalanche Current	I <sub>As</sub>	2	1	А
Power Dissipation (T <sub>C</sub> = 25°C)	$P_{D}$	10	)8	W
Operating Junction and Storage Temperature Range	$T_J,T_stg$	-55~-	+175	°C

Thermal Resistance				
Parameter	Symbol	Va	Unit	
Parameter		TO-252	TO-220	Onit
Thermal Resistance, Junction-to-Case	R <sub>thJC</sub>	1.	38	12/1/1
Thermal Resistance, Junction-to-Ambient	R <sub>thJA</sub>	6	0	K/W

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Specifications T <sub>J</sub> = 25°C, unless otherwise noted						
Parameter	Symbol Test Conditions -	Test Conditions	Value			Unit
		Min.	Тур.	Max.		
Static						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_{D} = 250\mu A$	40			V
Zero Gate Voltage Drain Current		$V_{DS} = 40V, V_{GS} = 0V, T_{J} = 25^{\circ}C$			1	пΔ
Zero Gate Voltage Drain Guirent	I <sub>DSS</sub>	$V_{DS} = 40V, V_{GS} = 0V, T_{J} = 150^{\circ}C$			100	μΑ
Gate-Source Leakage	$I_{GSS}$	$V_{GS} = \pm 20V$	-		±100	nA
Gate-Source Threshold Voltage	$V_{\rm GS(th)}$	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	1.0	1.7	2.4	V
Drain Course On Begintages (Nets2)	Б	$V_{GS} = 10V, I_{D} = 30A$		4.5	6.4	mΩ
Drain-Source On-Resistance (Note3)	$R_{DS(on)}$	$V_{GS} = 4.5V, I_{D} = 30A$		7.1	9.4	mΩ
Forward Transconductance (Note3)	g <sub>fs</sub>	V <sub>DS</sub> = 10V, I <sub>D</sub> =20A		33		S
Dynamic						
Input Capacitance	C <sub>iss</sub>	V 0V		4355		
Output Capacitance	C <sub>oss</sub>	$V_{GS} = 0V,$ $V_{DS} = 20V,$		251		pF
Reverse Transfer Capacitance	C <sub>rss</sub>	f = 1.0MHz		188		
Total Gate Charge	$Q_g$			57		
Gate-Source Charge	$Q_{gs}$	$V_{DD} = 20V, I_{D} = 20A,$ $V_{GS} = 10V$		10		nC
Gate-Drain Charge	$Q_{gd}$			10		
Turn-on Delay Time	t <sub>d(on)</sub>			7		
Turn-on Rise Time	t <sub>r</sub>	$V_{DD} = 20V, I_{D} = 20A,$		4		
Turn-off Delay Time	t <sub>d(off)</sub>	$R_G = 3\Omega$		25		ns
Turn-off Fall Time	t <sub>f</sub>			5		
Drain-Source Body Diode Characteristics						
Continuous Body Diode Current	Is	T <sub>C</sub> = 25°C			70	
Pulsed Diode Forward Current	I <sub>SM</sub>				280	А
Body Diode Voltage	V <sub>SD</sub>	$T_J = 25^{\circ}C$ , $I_{SD} = 20A$ , $V_{GS} = 0V$			1.2	V
Reverse Recovery Time	t <sub>rr</sub>	$I_F = 20A,$ $di_F/dt = 100A/\mu s$		15.5		ns
Reverse Recovery Charge	Q <sub>rr</sub>			31		nC

#### **Notes**

- 1. Repetitive Rating: Pulse Width limited by maximum junction temperature
- 2.  $V_{DD}$  = 40V,  $R_G$  = 25 $\Omega$ , Starting  $T_J$  = 25 $^{\circ}C$
- 3. Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 1%



### **Typical Characteristics** $T_J = 25^{\circ}\text{C}$ , unless otherwise noted

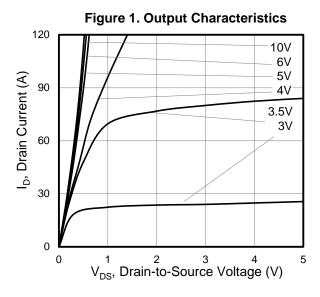


Figure 3. On-Resistance vs. Drain Current

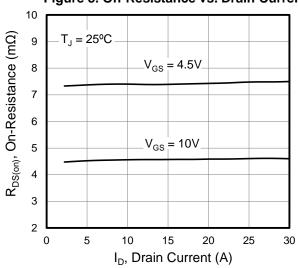


Figure 5. Gate Charge

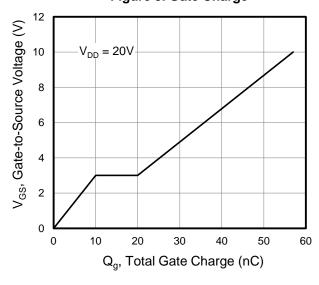


Figure 2. Transfer Characteristics

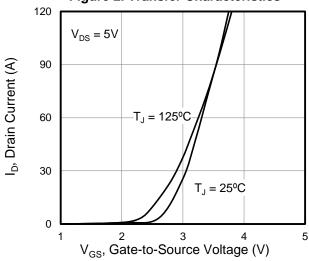


Figure 4. Capacitance

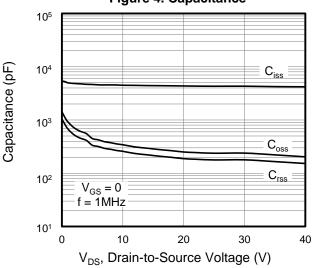
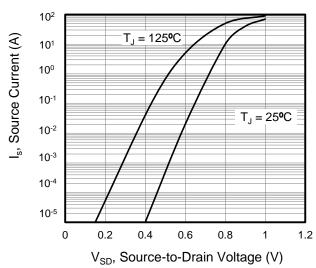


Figure 6. Body Diode Forward Voltage





### **Typical Characteristics** $T_J = 25^{\circ}\text{C}$ , unless otherwise noted

Figure 7. On-Resistance vs.

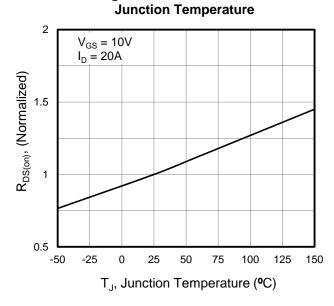


Figure 8. Threshold Voltage vs. Junction Temperature

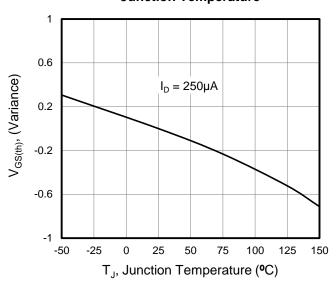
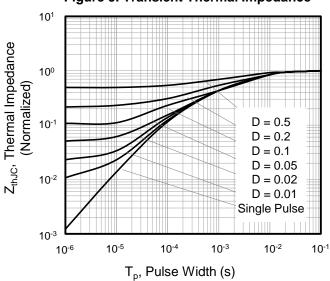


Figure 9. Transient Thermal Impedance



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Figure A: Gate Charge Test Circuit and Waveform

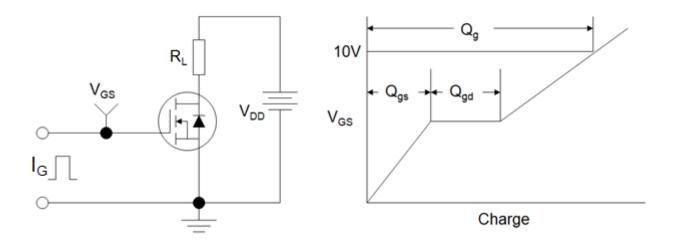


Figure B: Resistive Switching Test Circuit and Waveform

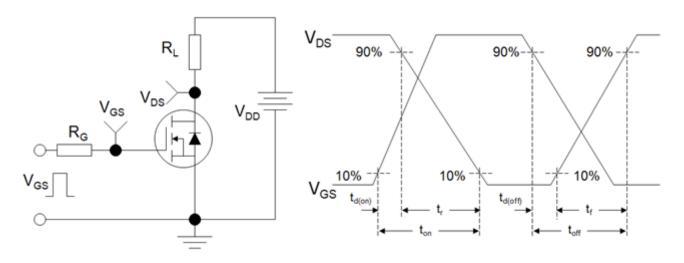
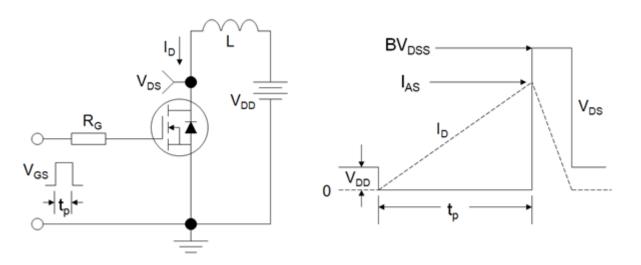
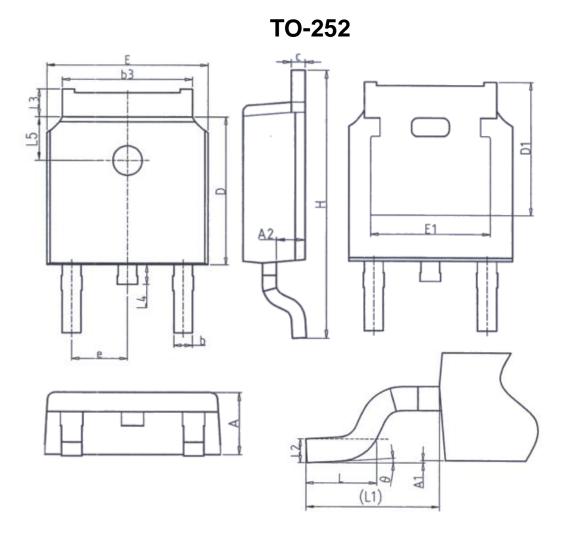


Figure C: Unclamped Inductive Switching Test Circuit and Waveform



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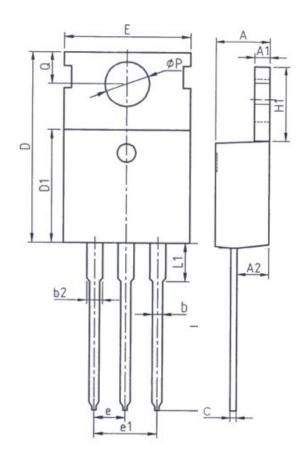


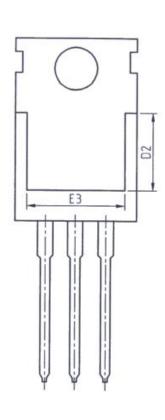


Unit: mm		
Symbol	Min.	Max.
Α	2. 20	2. 40
A1	0.00	0. 20
A2	0. 97	1. 17
b	0. 68	0. 90
b3	5. 20	5. 50
С	0. 43	0. 63
D	5. 98	6. 22
D1	5. 30	REF
E	6. 40	6. 80
E1	4. 63	-

Unit: mm			
Symbol	Min.	Max.	
е	2. 28	6BSC	
Н	9. 40	10.50	
L	1. 38	1. 75	
L1	.1 2. 90REF		
L2	0. 51BSC		
L3	0.88	1. 28	
L4	_	1.00	
L5	1. 65	1. 95	
θ	0°	8°	

# **TO-220**





Unit: mm		
Symbol	Min.	Max.
Α	4. 37	4. 77
A1	1. 25	1. 45
A2	2. 20	2. 60
b	0. 70	0. 95
b2	1. 17	1. 47
С	0. 40	0. 65
D	15. 10	16. 10
D1	8. 80	9. 40
D2	5. 50	_

Unit: mm			
Symbol	Min.	Max.	
E	9. 70	10. 30	
E3	7. 00	_	
е	2. 54BSC		
e1	5. 08BSC		
H1	6. 25	6. 85	
L	12. 75	13.80	
L1	_	3. 40	
Р	3. 40	3. 80	
Q	2. 60	3. 00	



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