600V Super-Junction Power MOSFET

DESCRIPTION

600V super-junction Power MOSFET

Super-junction power MOSFET is a revolutionary technology for high voltage power MOSFETs, designed according to the SJ principle. The SJ MOSFET is a price-performance optimized product enabling to target cost sensitive applications in Consumer and Lighting markets, designed by Wuxi Unigroup Microelectronics Company.

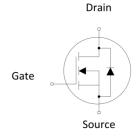
FEATURES

- Very low FOM $R_{DS(on)} \times Q_g$
- 100% avalanche tested
- RoHS compliant

APPLICATIONS

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)







Device Marking and Package Information

Device	evice Package Marking	
TPA60R330M	TO-220F	60R330M

Key Performance Parameters

Parameter	Value	Unit
V _{DS} @ T _{j,max}	600	V
R _{DS(on),max}	0.33	Ω
I _D	11	A
$Q_{g,typ}$	19	nC
I _{DM}	33	A



Absolute Maximum Ratings $T_C = 25^{\circ}C$, unless otherwise noted					
Parameter			Value		
		Symbol	TO-220F	Unit	
Drain-Source Voltage (V _{GS} = 0V)		V _{DSS}	600	V	
Continuous Drain Current	$T_{\rm C} = 25^{\rm o}{\rm C}$		11	A	
Continuous Diain Current	TC = 100°C	l _D	6.6		
Pulsed Drain Current	(note1)	I _{DM}	33	А	
Gate-Source Voltage		V _{GSS}	±30	V	
Single Pulse Avalanche Energy (note2)		E _{AS}	210	mJ	
Repetitive Avalanche Energy (note2)		E _{AR}	0.32	mJ	
Avalanche Current		I _{AR}	1.8	А	
MOSFET dv/dt ruggedness, V _{DS} = 0480V		dv/dt	50	V/ns	
Power Dissipation		P _D	31	W	
Continuous Body Diode Current		I _S	9.4	A	
Pulsed Diode Forward Current	de Forward Current (note1)		33		
Reverse diode dv/dt (note3)		dv/dt	15	V/ns	
Maximum diode commutation speed (note3)		di _f /dt	500	A/us	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55~+150	°C	

Thermal Resistance			
		Value	Unit
Parameter	Symbol	TO-220F	
Thermal Resistance, Junction-to-Case	R _{thJC}	4	°C/W
Thermal Resistance, Junction-to-Ambient	ction-to-Ambient R _{thJA} 80		1 -0/00



D			Value				
Parameter	Symbol	Symbol Test Conditions		Тур.	Max.	Unit	
Static	-						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0V, I_D = 250\mu A$	600			V	
7 O-to Valta D - : O - :		$V_{DS} = 600V, V_{GS} = 0V, T_{J} = 25^{\circ}C$			1	μΑ	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 600V, V_{GS} = 0V, T_{J} = 150^{\circ}C$			100		
Gate-Source Leakage	I _{GSS}	$V_{GS} = \pm 30V$			±100	nA	
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2.5		4.5	V	
Drain-Source On-Resistance	R _{DS(on)}	V _{GS} = 10V, I _D = 5.5A		0.29	0.33	Ω	
Gate resistance	R _G	f = 1.0MHz open drain		18		Ω	
Dynamic	•			•			
Input Capacitance	C _{iss}	\/ O\/		1021		pF	
Output Capacitance	C _{oss}	$V_{GS} = 0V,$ $V_{DS} = 100V,$		43			
Reverse Transfer Capacitance	C _{rss}	f = 1.0MHz		6			
Total Gate Charge	Q_g			19.0		nC	
Gate-Source Charge	Q_{gs}	$V_{DD} = 480V, I_{D} = 11A, V_{GS} = 10V$		4.8			
Gate-Drain Charge	Q_{gd}	30		7.2			
Turn-on Delay Time	t _{d(on)}			28			
Turn-on Rise Time	t _r	$V_{DD} = 400V, I_{D} = 11A,$		61		ns	
Turn-off Delay Time	t _{d(off)}	$R_G = 25\Omega$		89			
Turn-off Fall Time	t _f			41			
Drain-Source Body Diode Characte	ristics						
Body Diode Voltage	V _{SD}	$T_J = 25^{\circ}\text{C}, I_{SD} = 5.5\text{A}, V_{GS} = 0\text{V}$		0.9	1.2	V	
Reverse Recovery Time	t _{rr}			377		ns	
Reverse Recovery Charge	Q _{rr}	$V_R = 400V, I_F = I_S,$ $di_F/dt = 100A/\mu s$		3.4		μC	
Peak Reverse Recovery Current	I _{rrm}	31-131 - 1007 V po		17.8		Α	

Notes

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature
- 2. I_{AS} = 1.8A, V_{DD} = 50V, R_{G} = 25 Ω , Starting T_{J} = 25 $^{\circ}$ C
- 3. Identical low side and high side switch with identical ${\rm R}_{\rm G}$



Typical Characteristics $T_J = 25^{\circ}\text{C}$, unless otherwise noted

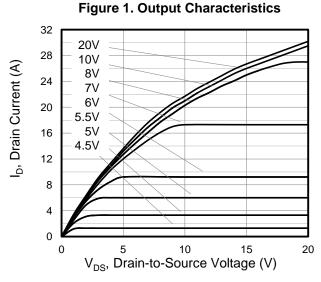


Figure 3. On-Resistance vs. Drain Current

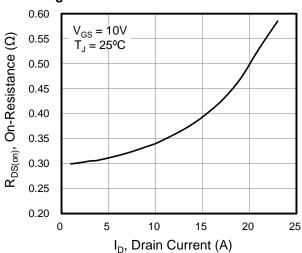


Figure 5. Gate Charge 12 V_{GS}, Gate-to-Source Voltage (V) 10 $V_{DD} = 120V$ 8 6 $V_{DD} = 480V$ 2 Q_g, Total Gate Charge (nC) 20

Figure 2. Transfer Characteristics

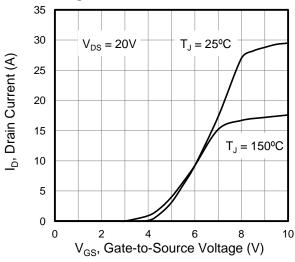
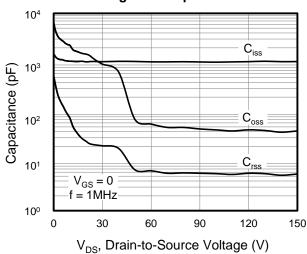
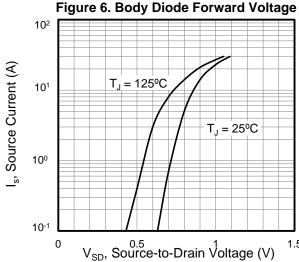


Figure 4. Capacitance







Typical Characteristics $T_J = 25^{\circ}C$, unless otherwise noted

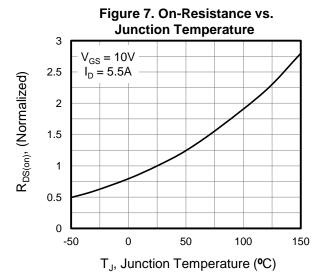


Figure 9. Transient Thermal Impedance TO-220F

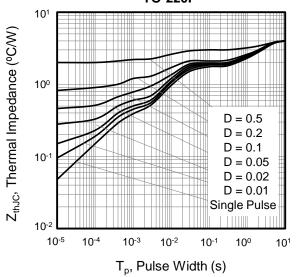


Figure 8. Breakdown voltage vs. Junction Temperature

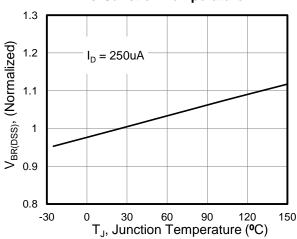
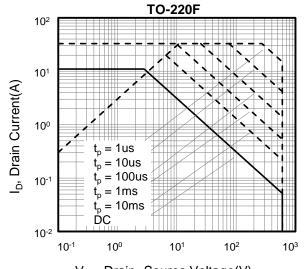


Figure 10. Safe operation area for



 V_{DS} , Drain- Source Voltage(V)



Figure A: Gate Charge Test Circuit and Waveform

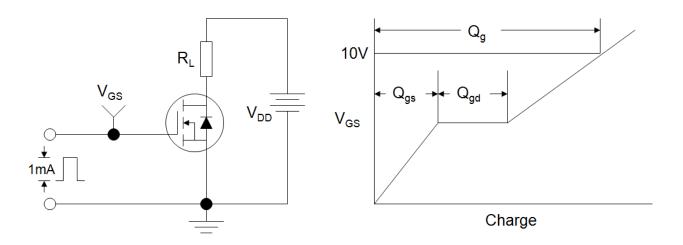


Figure B: Resistive Switching Test Circuit and Waveform

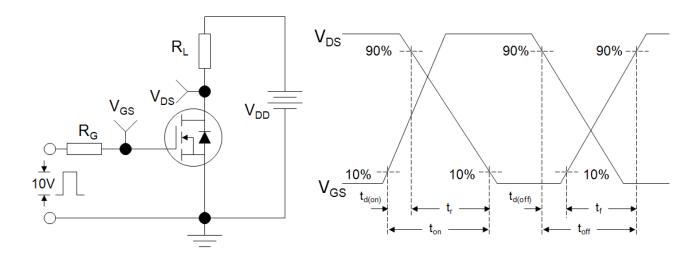
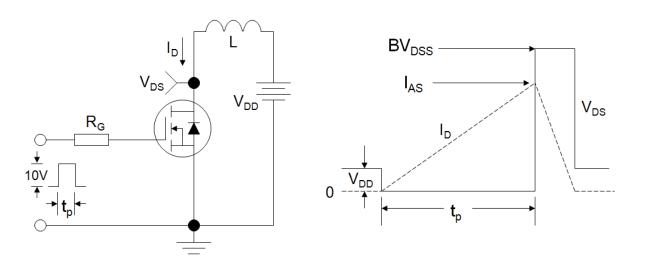


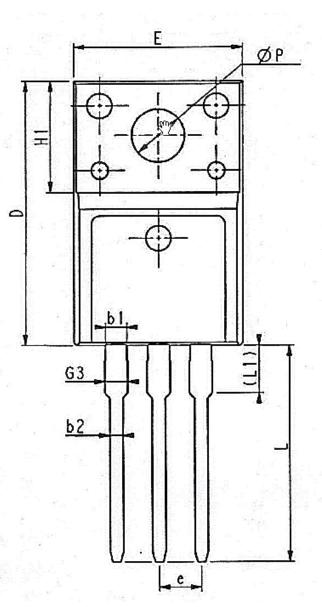
Figure C: Unclamped Inductive Switching Test Circuit and Waveform

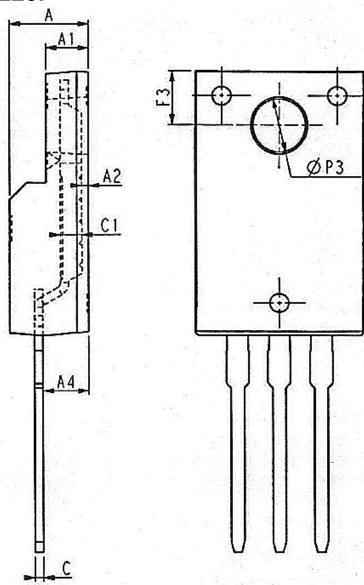






TO-220F





Max.

13.28

3.18

3.38

3.65

3.45

1.55

1.43

0.95

Unit:mm				Unit	:mm	
Symbol	Min.	Nom	Max.	Symbol	Min.	Nom
Е	9.96	10.16	10.36	е		2.54BSC
А	4.50	4.70	4.90	L	12.68	12.98
A1	2.34	2.54	2.74	L1	2.88	3.03
A2	0.30	0.45	0.60	ФР	3.03	3.18
A4	2.56	2.76	2.96	ФР3	3.15	3.45
С	0.40	0.50	0.65	F3	3.15	3.30
c1	1.20	1.30	1.35	G3	1.25	1.35
D	15.57	15.87	16.17	b1	1.18	1.28
H1		6.70REF		b2	0.70	0.80



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