

## **600V Super-junction Power MOSFET**

#### DESCRIPTION

#### 600V Super-junction Power MOSFET

Super-junction power MOSFET is a revolutionary technology for high voltage power MOSFETs, designed according to the SJ principle and pioneered. The Multi-EPI SJ MOSFET provide an extremely fast and robust body diode. Also provide an extremely low switching, communication and conduction losses device with highest robustness make especially resonant switching applications more reliable, more efficient, lighter and cooler, designed by Wuxi Unigroup Microelectronics Company.

#### **FEATURES** APPLICATIONS Ultra-fast body diode Switch Mode Power Supply (SMPS) Very low FOM $R_{DS(on)} \times Q_g$ Uninterruptible Power Supply (UPS) Easy to use/drive Power Factor Correction (PFC) 100% avalanche tested LLC Half-bridge **RoHS** compliant Charger Drain TO-220 Gate Source

Device Marking and Package Information					
Device	Package	Marking			
TPA60R170MFD	TO-220F	60R170MFD			
Key Performance Parameters					
Parameter	Value	Unit			
V <sub>DS</sub> @ T <sub>j,max</sub>	600	V			
R <sub>DS(on),max</sub>	0.17	Ω			
I <sub>D</sub>	20	А			
Q <sub>g,typ</sub>	41	nC			
I <sub>DM</sub>	60	A			
t <sub>rr</sub>	144	ns			
Q <sub>rr</sub>	0.76	μC			
I <sub>rrm</sub>	10.5	A			



<b>Absolute Maximum Ratings</b> $T_c = 25^{\circ}C$ , unless otherwise noted						
Parameter		Symbol	Value	Unit		
Drain-Source Voltage (V <sub>GS</sub> = 0V)		V <sub>DSS</sub>	600	V		
Continuous Drain Current	T <sub>C</sub> = 25°C		20	А		
	TC = 100°C	I <sub>D</sub>	12			
Pulsed Drain Current	(note1)	I <sub>DM</sub>	60	А		
Gate-Source Voltage		V <sub>GSS</sub>	±30	V		
Single Pulse Avalanche Energy (note2)		E <sub>AS</sub>	418	mJ		
Repetitive Avalanche Energy (note2)		E <sub>AR</sub>	0.63	mJ		
Avalanche Current		I <sub>AR</sub>	3.4	А		
MOSFET dv/dt ruggedness, V <sub>DS</sub> = 0480V		dv/dt	50	V/ns		
Power Dissipation		P <sub>D</sub>	34	W		
Continuous Body Diode Current		I <sub>S</sub>	20	A		
Pulsed Diode Forward Current (note1)		I <sub>SM</sub>	60			
Reverse diode dv/dt (note3)		dv/dt	50	V/ns		
Maximum diode commutation speed (note3)		di <sub>f</sub> /dt	900	A/us		
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	-55~+150	°C		

Thermal Resistance				
Parameter	Symbol	Value	Unit	
Thermal Resistance, Junction-to-Case	R <sub>thJC</sub>	3.7	°C/W	
Thermal Resistance, Junction-to-Ambient	R <sub>thJA</sub>	80		



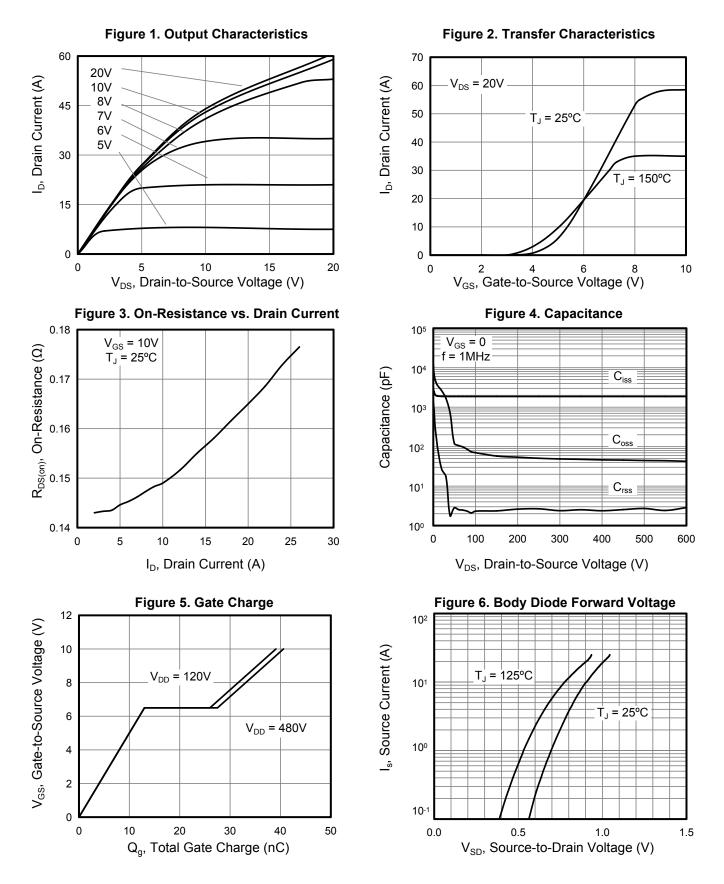
			Value			
Parameter	Symbol	Test Conditions	Min.	Typ. Max.		Unit
Static	•					
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$V_{GS}$ = 0V, I <sub>D</sub> = 250µA	600			V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS}$ = 600V, $V_{GS}$ = 0V, $T_{J}$ = 25°C			2.5	μA
Gate-Source Leakage	I <sub>GSS</sub>	$V_{GS}$ = $\pm 30V$			±100	nA
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = 250 \mu A$	3		5	V
Drain-Source On-Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 10A		0.15	0.17	Ω
Gate resistance	R <sub>G</sub>	f = 1.0MHz open drain		12		Ω
Dynamic						
Input Capacitance	C <sub>iss</sub>	)/ <u>-</u> 0)/		1867		pF
Output Capacitance	C <sub>oss</sub>	$V_{GS} = 0V,$ $V_{DS} = 100V,$		70		
Reverse Transfer Capacitance	C <sub>rss</sub>	f = 1.0MHz		2		
Total Gate Charge	Qg			41		nC
Gate-Source Charge	Q <sub>gs</sub>	$V_{DD} = 480V, I_D = 20A, V_{GS} = 10V$		13		
Gate-Drain Charge	Q <sub>gd</sub>			15		
Turn-on Delay Time	t <sub>d(on)</sub>			39		ns
Turn-on Rise Time	t <sub>r</sub>	V <sub>DD</sub> = 400V, I <sub>D</sub> = 20A,		39		
Turn-off Delay Time	t <sub>d(off)</sub>	$R_{G} = 25\Omega$		169		
Turn-off Fall Time	t <sub>f</sub>			49		
Drain-Source Body Diode Characte	eristics					
Body Diode Voltage	$V_{SD}$	T <sub>J</sub> = 25°C, I <sub>SD</sub> = 10A, V <sub>GS</sub> = 0V		1.0	1.5	V
Reverse Recovery Time	t <sub>rr</sub>			144		ns
Reverse Recovery Charge	Q <sub>rr</sub>	V <sub>R</sub> = 400V, I <sub>F</sub> = I <sub>S</sub> , di <sub>r</sub> /dt = 100A/µs		0.76		μC
Peak Reverse Recovery Current	I <sub>rrm</sub>	all at 100, pho		10.5		А

#### Notes

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature
- 2.  $I_{AS}$  = 3.4A,  $V_{DD}$  = 50V,  $R_G$  = 25 $\Omega$ , Starting  $T_J$  = 25°C
- 3. Identical low side and high side switch with identical  ${\sf R}_{\sf G}$

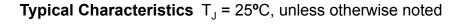


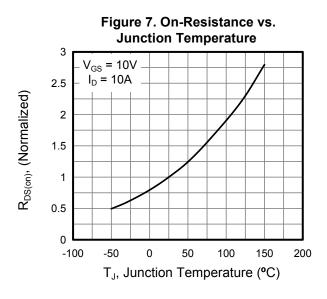
#### **Typical Characteristics** $T_J = 25^{\circ}C$ , unless otherwise noted

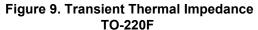


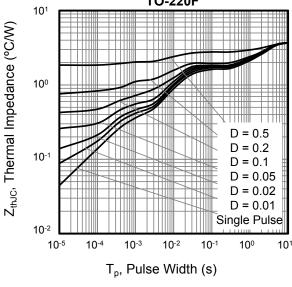
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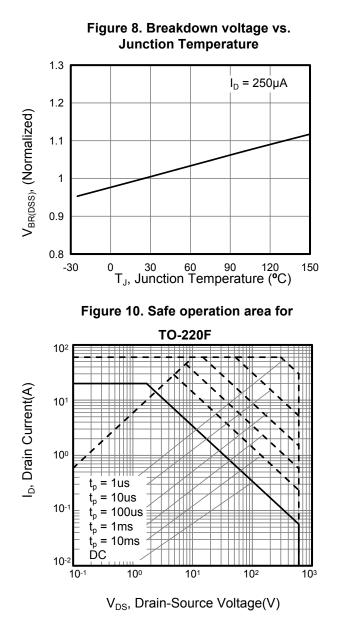
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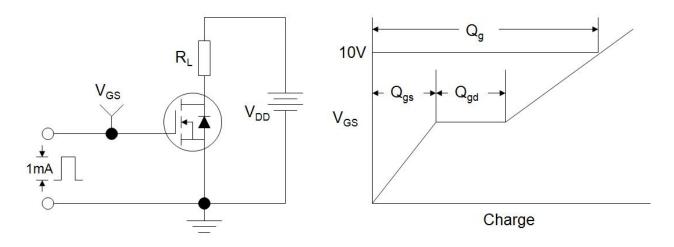


Figure B: Resistive Switching Test Circuit and Waveform

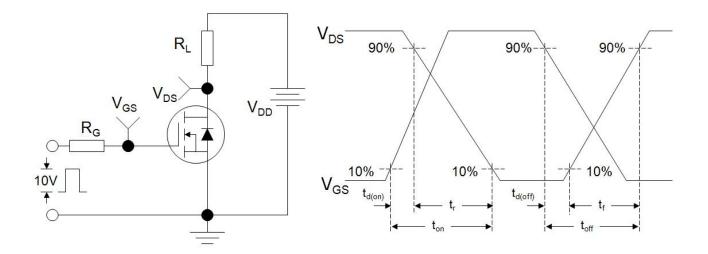
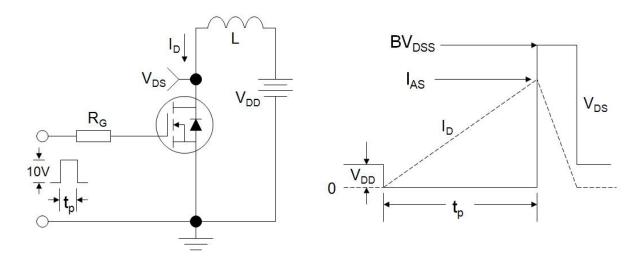
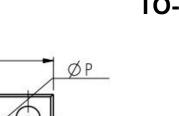


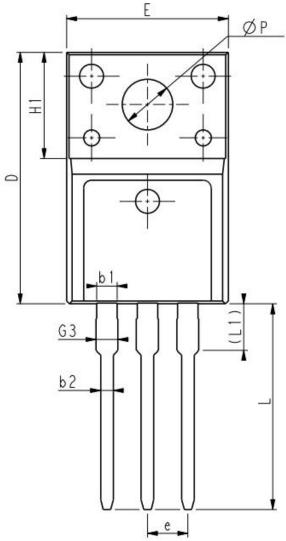
Figure C: Unclamped Inductive Switching Test Circuit and Waveform

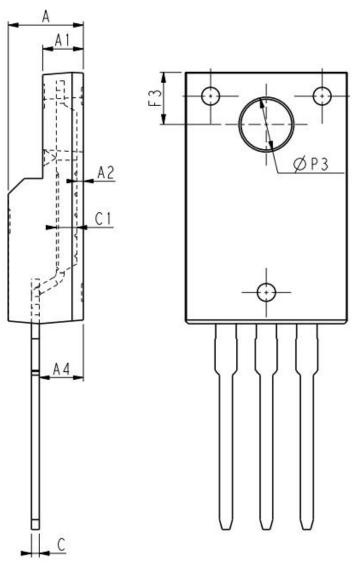






TO-220F





Max.

13.28

3.13

3.38

3.65

3.45

1.55

1.43

0.95

	Unit:mm			Unit:mm		
Symbol	Min.	Nom	Max.	Symbol	Min.	Nom
E	9.96	10.16	10.36	е		2.54BSC
А	4.50	4.70	4.90	L	12.68	12.98
A1	2.34	2.54	2.74	L1	2.93	3.03
A2	0.30	0.45	0.60	ΦΡ	3.03	3.18
A4	2.56	2.76	2.96	ΦΡ3	3.15	3.45
С	0.40	0.50	0.65	F3	3.15	3.30
c1	1.20	1.30	1.35	G3	1.25	1.35
D	15.57	15.87	16.17	b1	1.18	1.28
H1		6.70REF		b2	0.70	0.80



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