

XL2594-5.0 SOP8 XL2594-12 SOP8 XL2594-ADJ SOP8

FEATURES

- 5V, 12V, ADJ, and Adjustable Output Versions
- Adjustable Version Output Voltage Range,
 1.2V to 37V (57V for the HV Version)±4% Max
 Over Line and Load Conditions
- Available in 8-pin Surface Mount SOIC and PDIP-8 Package
- Ensured 0.5A Output Current
- Input Voltage Range up to 60V
- Requires only 4 External Components
- 150 kHz Fixed Frequency Internal Oscillator
- TTL Shutdown Capability
- Low Power Standby Mode, I_Q typically 85 μA
- High Efficiency
- Uses Readily Available Standard Inductors
- Thermal Shutdown and Current Limit Protection

APPLICATIONS

- Simple High-efficiency Step-down (Buck) Regulator
- Efficient Pre-regulator for Linear Regulators
- On-card Switching Regulators
- Positive to Negative Convertor

DESCRIPTION

The XL2594 series of regulators are monolithic integrated circuits that provide all the active functions for a step-down (buck) switching regulator, capable of driving a 0.5A load with excellent line and load regulation. These devices are available in fixed output voltages of 5V, 12V, ADJ and an adjustable output version, and are packaged in a 8-lead PDIP and a 8-lead surface mount SOIC package.

Requiring a minimum number of external components, these regulators are simple to use and feature internal frequency compensation[†], a fixed-frequency oscillator, and improved line and load regulation specifications.

The XL2594 series operates at a switching frequency of 150 kHz thus allowing smaller sized filter components than what would be needed with lower frequency switching regulators. Because of its high efficiency, the copper traces on the printed circuit board are normally the only heat sinking needed.

A standard series of inductors (both through hole and surface mount types) are available from several different manufacturers optimized for use with the XL2594 series. This feature greatly simplifies the design of switch-mode power supplies.

Other features include an ensured $\pm 4\%$ tolerance on output voltage under all conditions of input voltage and output load conditions, and $\pm 15\%$ on the oscillator frequency. External shutdown is included, featuring typically 85 μA standby current. Self protection features include a two stage frequency reducing current limit for the output switch and an over temperature shutdown for complete protection under fault conditions.

The XL2594 is for applications requiring an input voltage up to 60V.

Absolute Maximum Ratings(1)(2)

Maximum Supply Voltage				
XL2594		45∖		
ON /OFF Pin Input Voltage		-0.3 ≤ V ≤ +25V		
Feedback Pin Voltage		-0.3 ≤ V ≤+25V		
Output Voltage to Ground (Stea	ady State)	-1V		
Power Dissipation		Internally limited		
Storage Temperature Range		−65°C to +150°C		
ESD Susceptibility	Human Body Model (3)	2 kV		
Lead Temperature				
D8 Package	Vapor Phase (60 sec.)	+215		
	Infrared (15 sec.)	+220°		
P Package (Soldering, 10 sec.)		+260°C		
Maximum Junction Temperature		+150°C		

⁽¹⁾ Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is intended to be functional, but do not ensure specific performance limits. For ensured specifications and test conditions, see the Electrical Characteristics.

Operating Conditions

Temperature Range	-40°C ≤ T _J +125°C
Supply Voltage	
XL2594	4.5V to 40V

XL2594 Electrical Characteristics

Specifications with standard type face are for $T_J = 25$ °C, and those with **boldface type** apply over **full Operating Temperature Range.** V_{INmax} = 40V for the LM2594 and 60V for the LM2594HV.

Symbol	Parameter	Conditions	XL	XL2594	
			Typ ⁽¹⁾	Limit ⁽²⁾	(Limits)
SYSTEM	PARAMETERS (3) Test (Circuit Figure 16			
V _{OUT}	Output Voltage	$4.75V \le V_{IN} \le V_{INmax}$, $0.1A \le I_{LOAD} \le 0.5A$	3.3	3.432/ 3.465 3.168/ 3.135	V V(min) V(max)
η	Efficiency	V _{IN} = 12V, I _{LOAD} = 0.5A	80		%

⁽¹⁾ Typical numbers are at 25°C and represent the most likely norm.

⁽²⁾ All limits ensured at room temperature (standard type face) and at temperature extremes (bold type face). All room temperature limits are 100% production tested. All limits at temperature extremes are specified via correlation using standard Statistical Quality Control (SQC) methods. All limits are used to calculate Average Outgoing Quality Level (AOQL).

⁽³⁾ External components such as the catch diode, inductor, input and output capacitors, and voltage programming resistors can affect switching regulator system performance. When the XL2594 is used as shown in the Figure 16 test circuit, system performance will be as shown in system parameters section of Electrical Characteristics.

XL2594 Electrical Characteristics

Specifications with standard type face are for $T_J = 25$ °C, and those with **boldface type** apply over **full Operating Temperature Range**

Symbol	Parameter	Conditions	XL2594		Units
			Typ ⁽¹⁾	Limit ⁽²⁾	(Limits)
SYSTEM P	ARAMETERS (3) Test Circ	cuit Figure 16			
V _{OUT}	Output Voltage	$7V \le V_{IN} \le V_{INmax}$, $0.1A \le I_{LOAD} \le 0.5A$	5.0	4.800/ 4.750 5.200/ 5.250	V V(min) V(max)
η	Efficiency	V _{IN} = 12V, I _{LOAD} = 0.5A	82		%

- (1) Typical numbers are at 25°C and represent the most likely norm.
- (2) All limits ensured at room temperature (standard type face) and at temperature extremes (bold type face). All room temperature limits are 100% production tested. All limits at temperature extremes are specified via correlation using standard Statistical Quality Control (SQC) methods. All limits are used to calculate Average Outgoing Quality Level (AOQL).
- (3) External components such as the catch diode, inductor, input and output capacitors, and voltage programming resistors can affect switching regulator system performance. When the XL2594 is used as shown in the Figure 16 test circuit, system performance will be as shown in system parameters section of Electrical Characteristics.

XL2594 Electrical Characteristics

Specifications with standard type face are for $T_J = 25$ °C, and those with **boldface type** apply over **full Operating Temperature Range**

Symbol	Parameter	Conditions	XL	Units	
			Typ ⁽¹⁾	Limit ⁽²⁾	(Limits)
SYSTEM P	ARAMETERS (3) Test Ci	rcuit <i>Figure 16</i>	·		•
V _{OUT}	Output Voltage	$15V \le V_{\text{IN}} \le V_{\text{INmax}}, 0.1A \le I_{\text{LOAD}} \le 0.5A$	12.0	11.52/ 11.40 12.48/ 12.60	V V(min) V(max)
η	Efficiency	V _{IN} = 25V, I _{LOAD} = 0.5A	88		%

- (1) Typical numbers are at 25°C and represent the most likely norm.
- (2) All limits ensured at room temperature (standard type face) and at temperature extremes (bold type face). All room temperature limits are 100% production tested. All limits at temperature extremes are specified via correlation using standard Statistical Quality Control (SQC) methods. All limits are used to calculate Average Outgoing Quality Level (AOQL).
- (3) External components such as the catch diode, inductor, input and output capacitors, and voltage programming resistors can affect switching regulator system performance. When the XL2594 is used as shown in the Figure 16 test circuit, system performance will be as shown in system parameters section of Electrical Characteristics.

XL2594 Electrical Characteristics

Specifications with standard type face are for $T_J = 25^{\circ}C$, and those with **boldface type** apply over **full Operating Temperature Range**

0	D	0 11/1	VI 4	XL2594	
Symbol	Parameter	Conditions	ΛL	AL2094	
			Typ ⁽¹⁾	Limit ⁽²⁾	(Limits)
SYSTEM	PARAMETERS (3) Test C	ircuit Figure 16	•		•
V_{FB}	Feedback Voltage	$4.5 \text{V} \leq \text{V}_{\text{IN}} \leq \text{V}_{\text{INmax}}$, $0.1 \text{A} \leq \text{I}_{\text{LOAD}} \leq 0.5 \text{A}$ V _{OUT} programmed for 3V. Circuit of <i>Figure 16</i>	1.230	1.193/ 1.180 1.267/ 1.280	V V(min) V(max)
η	Efficiency	V _{IN} = 12V, I _{LOAD} = 0.5A	80		%

- (1) Typical numbers are at 25°C and represent the most likely norm.
- (2) All limits ensured at room temperature (standard type face) and at temperature extremes (bold type face). All room temperature limits are 100% production tested. All limits at temperature extremes are specified via correlation using standard Statistical Quality Control (SQC) methods. All limits are used to calculate Average Outgoing Quality Level (AOQL).
- (3) External components such as the catch diode, inductor, input and output capacitors, and voltage programming resistors can affect switching regulator system performance. When the XL2594 is used as shown in the Figure 16 test circuit, system performance will be as shown in system parameters section of Electrical Characteristics.

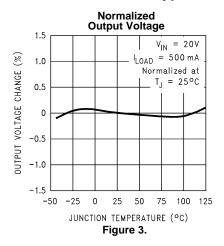
All Output Voltage Versions Electrical Characteristics

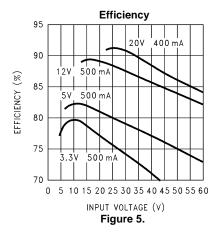
Specifications with standard type face are for T_J = 25°C, and those with **boldface type** apply over **full Operating Temperature Range** . Unless otherwise specified, V_{IN} = 12V for the 3.3V, 5V, and Adjustable version and V_{IN} = 24V for the 12V version. I_{LOAD} = 100 mA

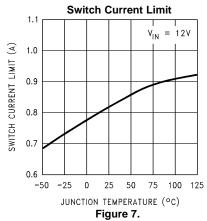
Symbol	Parameter	Conditions	XL2594		Units
			Typ ⁽¹⁾	Limit ⁽²⁾	(Limits)
DEVICE	PARAMETERS		·		
l _b	Feedback Bias Current	Adjustable Version Only, VFB = 1.3V	10	50/100	nA
f _O	Oscillator Frequency	See ⁽³⁾	150		kHz
				127/ 110	kHz(min)
				173/ 173	kHz(max)
V _{SAT}	Saturation Voltage	$I_{OUT} = 0.5A^{(4)(5)}$	0.9		V
				1.1/ 1.2	V(max)
DC	Max Duty Cycle (ON)	See ⁽⁵⁾	100		%
	Min Duty Cycle (OFF)	See (6)	0		
I _{CL}	Current Limit	Peak Current ⁽⁴⁾⁽⁵⁾	0.8		Α
				0.65/ 0.58	A(min)
				1.3/ 1.4	A(max)
IL	Output Leakage Current	Output = $0V^{(4)(6)(7)}$		50	μA(max)
		Output = −1V	2		mA
				15	mA(max)
lQ	Quiescent Current	See ⁽⁶⁾	5		mA
				10	mA(max)
I _{STBY}	Standby Quiescent Current	$ON/OFF pin = 5V (OFF)^{(7)}$	85		μA
		XL2594		200/ 250	μA(max)
			140	250/300	μA(max)
θ_{JA}	Thermal Resistance	P Package, Junction to Ambient ⁽⁸⁾	95		°C/W
		MDPackage, Junction to Ambient ⁽⁸⁾	150		
ON/OFF	CONTROL Test Circuit Figure	16	,	,	
	ON /OFF Pin Logic Input		1.3		V
V_{IH}	Threshold Voltage	Low (Regulator ON)		0.6	V(max)
V_{IL}		High (Regulator OFF)		2.0	V(min)
I _H	ON /OFF Pin Input Current	V _{LOGIC} = 2.5V (Regulator OFF)	5		μA
				15	μA(max)
IL		V _{LOGIC} = 0.5V (Regulator ON)	0.02		μA
				5	μA(max)

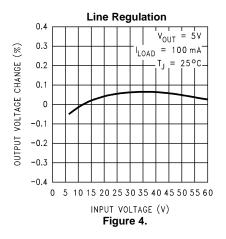
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- (2) All limits ensured at room temperature (standard type face) and at temperature extremes (bold type face). All room temperature limits are 100% production tested. All limits at temperature extremes are specified via correlation using standard Statistical Quality Control (SQC) methods. All limits are used to calculate Average Outgoing Quality Level (AOQL).
- (3) The switching frequency is reduced when the second stage current limit is activated. The amount of reduction is determined by the severity of current overload.
- (4) No diode, inductor or capacitor connected to output pin.
- (5) Feedback pin removed from output and connected to 0V to force the output transistor switch ON.
- (6) Feedback pin removed from output and connected to 12V for the 5V, and the ADJ. version, and 15V for the 12V version, to force the output transistor switch OFF.
- (7) $V_{IN} = 40V$ for the XL2594.

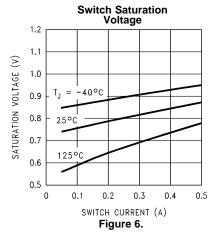
Typical Performance Characteristics

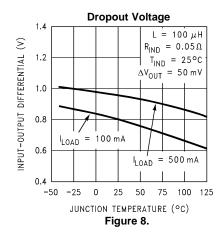




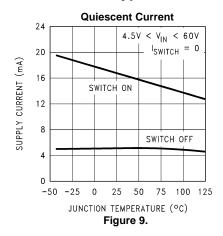


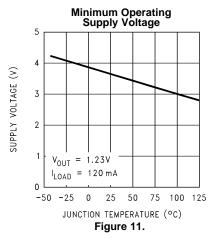


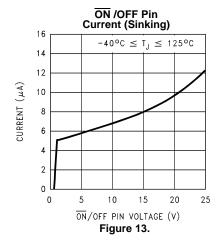


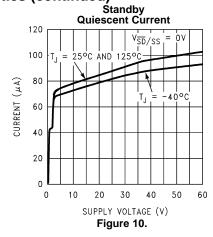


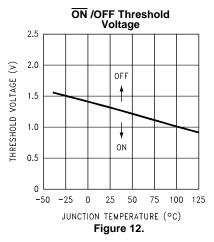
Typical Performance Characteristics (continued)

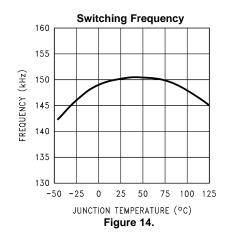




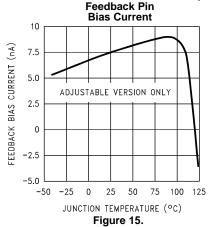






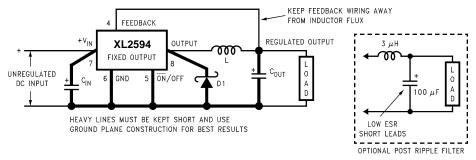


Typical Performance Characteristics (continued) Feedback Pin Bias Current



TYPICAL CIRCUIT AND LAYOUT GUIDELINES

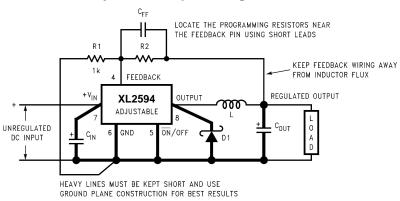
Fixed Output Voltage Versions



 C_{IN} — 68 µF, 35V, Aluminum Electrolytic Nichicon "PL Series" C_{OUT} — 120 µF, 25V Aluminum Electrolytic, Nichicon "PL Series" D1 — 1A, 40V Schottky Rectifier, 1N5819 L1 — 100 µH, L20

Select components with higher voltage ratings for designs using the XL2594 with an input voltage between 40V and 60V.

Adjustable Output Voltage Versions



$$R_2 = R_1 \left(\frac{V_{OUT}}{V_{REF}} - 1\right) \qquad \begin{array}{l} \text{Select } R_1 \text{ to be approximately 1 k}\Omega, \\ \text{use a 1\% resistor for best stability.} \\ \\ C_{IN} \longrightarrow 68 \ \mu\text{F}, \ 35\text{V}, \ \text{Aluminum Electrolytic Nichicon "PL Series"} \\ C_{OUT} \longrightarrow 120 \ \mu\text{F}, \ 25\text{V} \ \text{Aluminum Electrolytic, Nichicon "PL Series"} \\ D1 \longrightarrow 1A, \ 40\text{V} \ \text{Schottky Rectifier, 1N5819} \\ L1 \longrightarrow 100 \ \mu\text{H}, \ L20 \\ R_1 \longrightarrow 1 \ k\Omega, \ 1\% \\ \\ C_{FF} \longrightarrow \text{See Application Information Section} \\ \end{array}$$

where $V_{REF} = 1.23V$

 $V_{OUT} = V_{REF} \left(1 + \frac{R_2}{R_1} \right)$

Figure 16. Typical Circuits and Layout Guides

As in any switching regulator, layout is very important. Rapidly switching currents associated with wiring inductance can generate voltage transients which can cause problems. For minimal inductance and ground loops, the wires indicated by **heavy lines should be wide printed circuit traces and should be kept as short as possible.** For best results, external components should be located as close to the switcher IC as possible using ground plane construction or single point grounding.

If **open core inductors are used**, special care must be taken as to the location and positioning of this type of inductor. Allowing the inductor flux to intersect sensitive feedback, IC groundpath and C_{OUT} wiring can cause problems.

