

General Description

The WSD40120DN use advanced SGT MOSFET technology to provide low RDS(ON), low gate charge, fast switching and excellent avalanche characteristics.

This device is specially designed to get better ruggedness and suitable to use in

Features

Low RDS(on) & FOM
 Extremely low switching loss
 Excellent stability and uniformity or Invertors

Product Summary

BVDSS	RDSON	ID
40V	1.8mΩ	120A

Applications

- Consumer electronic power supply
- Synchronous-rectification
- Synchronous-rectification applications

DFN5X6-8 Pin Configuration



Absolute Maximum Ratings at Tj=25°C unless otherwise noted

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	40	V
V _{GS}	Gate-Source Voltage	±20	V
I _D @T _C =25°C	Continuous Drain Current, V _{GS} @ 10V ¹	120	A
I _D @T _C =100°C	Continuous Drain Current, V _{GS} @ 10V ¹	82	A
I _{DM}	Pulsed Drain Current ²	400	A
EAS	Single Pulse Avalanche Energy ³	400	mJ
I _{AS}	Avalanche Current	40	A
P _D @T _C =25°C	Total Power Dissipation ⁴	125	W
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction-Ambient ¹	---	50	°C/W
R _{θJC}	Thermal Resistance Junction-Case ¹	---	1.0	°C/W

Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	40	---	---	V
ΔBV _{DSS} /ΔT _J	BV _{DSS} Temperature Coefficient	Reference to 25°C, I _D =1mA	---	0.043	---	V/°C
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =10V, I _D =20A	---	1.4	1.8	mΩ
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =4.5V, I _D =20A	---	2.0	2.6	mΩ
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =250uA	1.2	1.6	2.2	V
ΔV _{GS(th)}	V _{GS(th)} Temperature Coefficient		---	-6.94	---	mV/°C
I _{DSS}	Drain-Source Leakage Current	V _{DS} =32V, V _{GS} =0V, T _J =25°C	---	---	1	uA
		V _{DS} =32V, V _{GS} =0V, T _J =55°C	---	---	5	
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V, V _{DS} =0V	---	---	±100	nA
g _{fs}	Forward Transconductance	V _{DS} =5V, I _D =20A	---	53	---	S
R _g	Gate Resistance	V _{DS} =0V, V _{GS} =0V, f=1MHz	---	1.0	---	Ω
Q _g	Total Gate Charge (10V)	V _{DS} =15V, V _{GS} =10V, I _D =20A	---	45	---	nC
Q _{gs}	Gate-Source Charge		---	12	---	
Q _{gd}	Gate-Drain Charge		---	18.5	---	
T _{d(on)}	Turn-On Delay Time	V _{DD} =15V, V _{GEN} =10V, R _G =3.3Ω, I _D =20A, RL=15Ω.	---	18.5	---	ns
T _r	Rise Time		---	9	---	
T _{d(off)}	Turn-Off Delay Time		---	58.5	---	
T _f	Fall Time		---	32	---	
C _{iss}	Input Capacitance	V _{DS} =20V, V _{GS} =0V, f=1MHz	---	3972	---	pF
C _{oss}	Output Capacitance		---	1119	---	
C _{rss}	Reverse Transfer Capacitance		---	82	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _S	Continuous Source Current	V _G =V _D =0V, Force Current	---	---	100	A
V _{SD}	Diode Forward Voltage ²	V _{GS} =0V, I _S =1A, T _J =25°C	---	---	1.2	V

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2 .The data tested by pulsed , pulse width ≅ 300us , duty cycle ≅ 2%
- 3.The EAS data shows Max. rating . The test condition is VDD=25V,VGS=10V,L=0.5mH,IAS=40A 4.The power dissipation is limited by 150°C junction temperature
- 5 .The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

Typical Characteristics

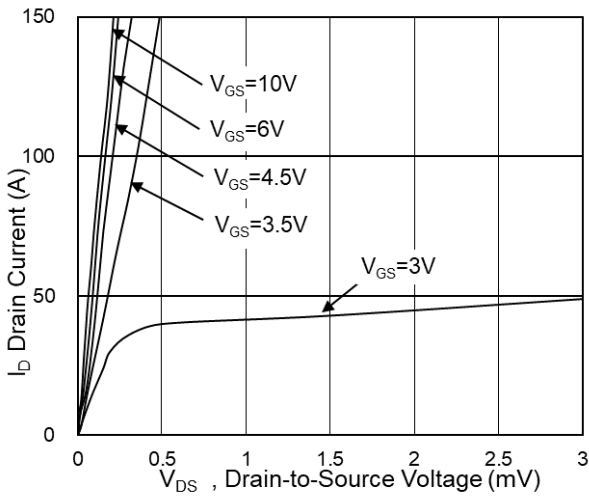


Fig.1 Typical Output Characteristics

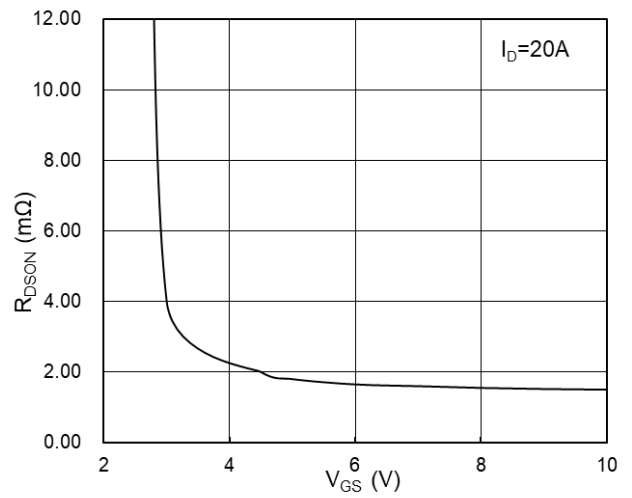


Fig.2 On-Resistance vs G-S Voltage

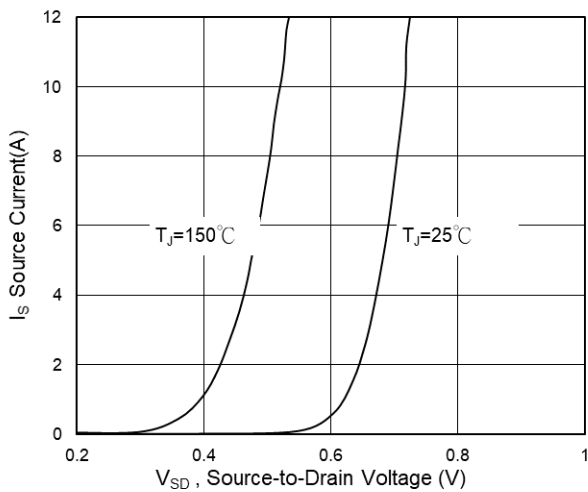


Fig.3 Source Drain Forward Characteristics

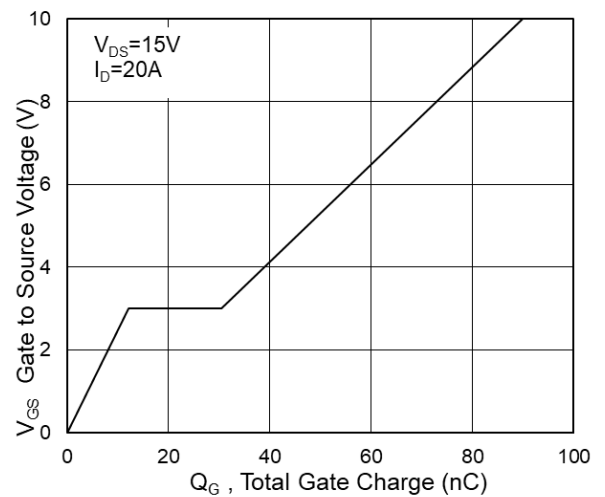


Fig.4 Gate-Charge Characteristics

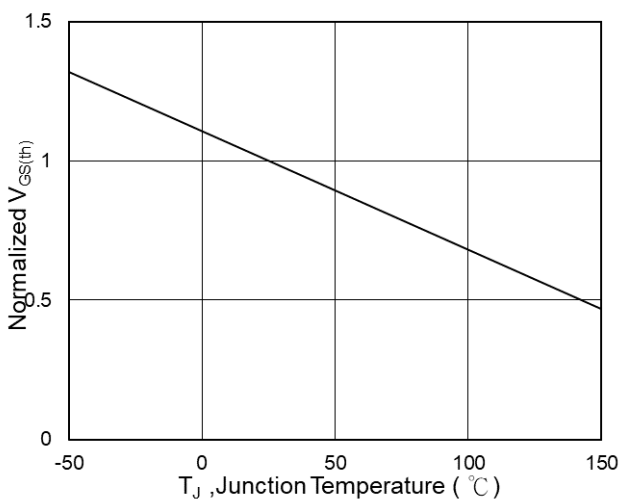


Fig.5 Normalized $V_{GS(th)}$ vs T_J

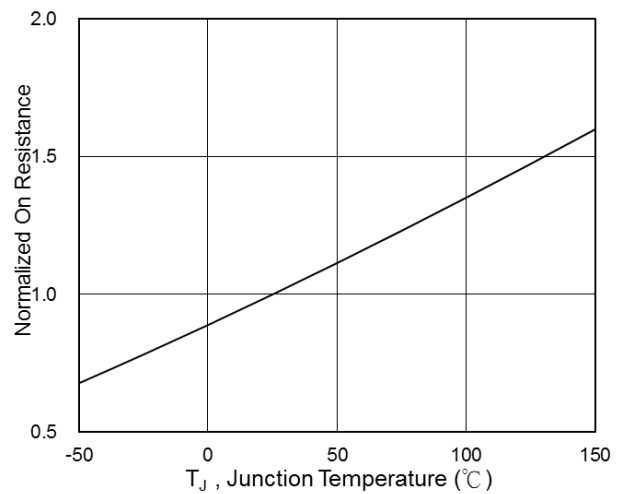


Fig.6 Normalized $R_{DS(on)}$ vs T_J

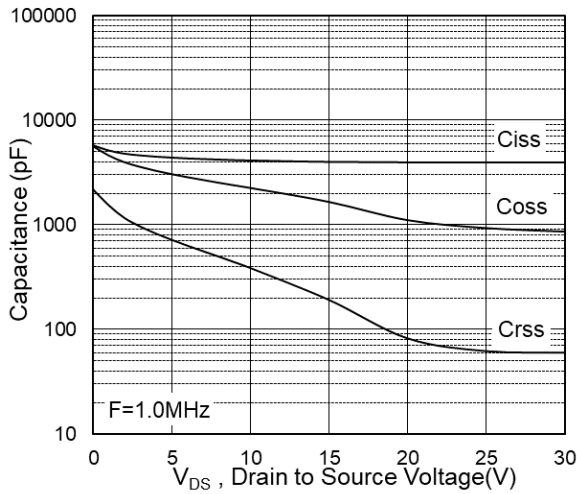


Fig.7 Capacitance

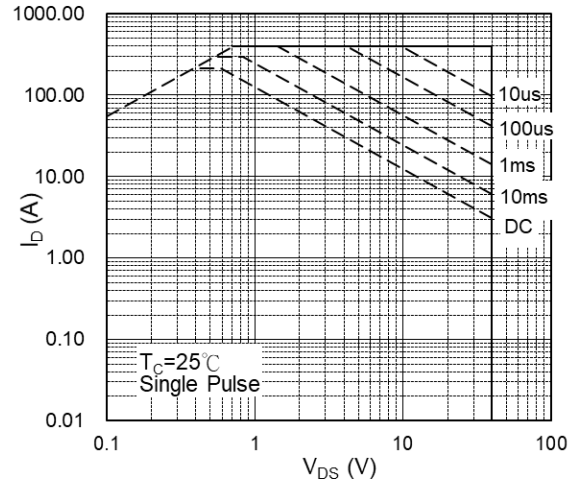


Fig.8 Safe Operating Area

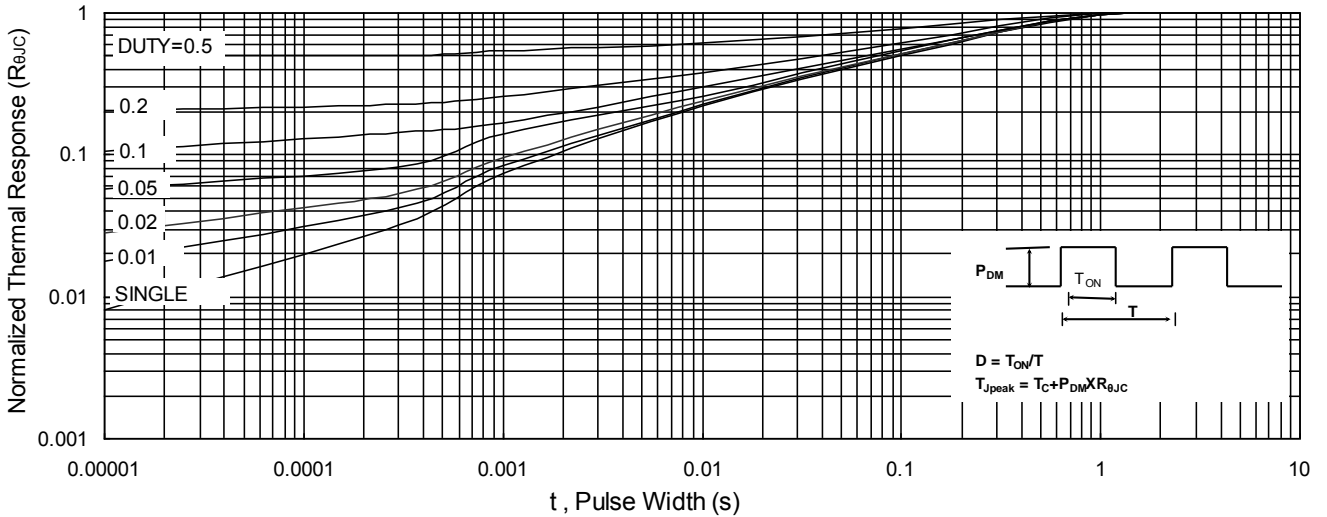


Fig.9 Normalized Maximum Transient Thermal Impedance

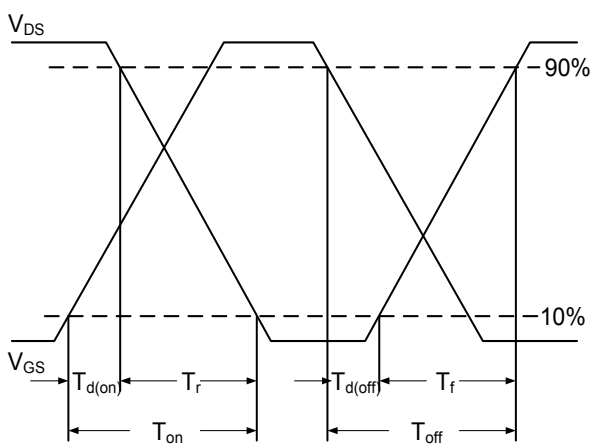
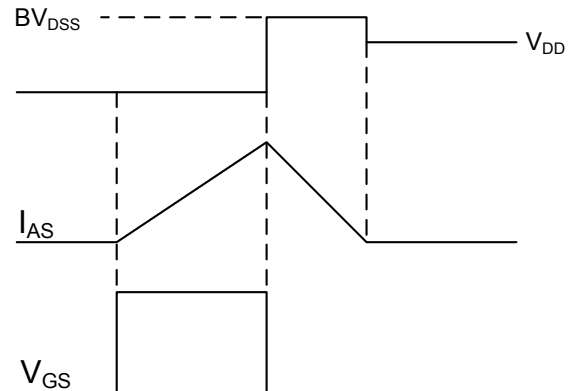


Fig.10 Switching Time Waveform

$$EAS = \frac{1}{2} L \times I_{AS}^2 \times \frac{BV_{DSS}}{BV_{DSS} - V_{DD}}$$





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