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November 2013

FDPF16N50UT N-Channel UniFET[™] Ultra FRFET[™] MOSFET

500 V, 15 A, 480 mΩ

Features

- $R_{DS(on)} = 370 \text{ m}\Omega \text{ (Typ.)} @ V_{GS} = 10 \text{ V}, I_D = 7.5 \text{ A}$
- Low Gate Charge (Typ. 32 nC)
- Low C_{rss} (Typ. 20 pF)
- 100% Avalanche Tested
- Improved dv/dt Capability
- RoHS Compliant

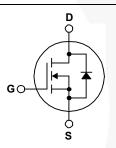
Applications

- LCD/LED/PDP TV
- Lighting
- Uninterruptible Power Supply

Description

UniFETTM MOSFET is Fairchild Semiconductor's high voltage MOSFET family based on planar stripe and DMOS technology. This MOSFET is tailored to reduce on-state resistance, and to provide better switching performance and higher avalanche energy strength. UniFET Ultra FRFETTM MOSFET has much superior body diode reverse recovery performance. Its t_{rr} is less than 50nsec and the reverse dv/dt immunity is 20V/nsec while normal planar MOSFETs have over 200nsec and 4.5V/nsec respectively. Therefore UniFET Ultra FRFET MOSFET can remove additional component and improve system reliability in certain applications that require performance improvement of the MOSFET's body diode. This device family is suitable for switching power converter applications such as power factor correction (PFC), flat panel display (FPD) TV power, ATX and electronic lamp ballasts.





MOSFET Maximum Ratings T_C = 25°C unless otherwise noted.

Symbol		Parameter	FDPF16N50UT	Unit		
V _{DSS}	Drain to Source Voltage			500	V	
V _{GSS}	Gate to Source Voltage			±30	V	
I _D	Drain Current	- Continuous (T _C = 25 ^o C)		15*		
		- Continuous (T _C = 100 ^o C)		9*	Α	
I _{DM}	Drain Current	- Pulsed (Note 1)		60*	А	
E _{AS}	Single Pulsed Avalanche Energy		(Note 2)	610	mJ	
I _{AR}	Avalanche Current		(Note 1)	15	А	
E _{AR}	Repetitive Avalanche Ener	ду	(Note 1)	20	mJ	
dv/dt	Peak Diode Recovery dv/dt		(Note 3)	20	V/ns	
P _D	Power Dissipation	$(T_{\rm C} = 25^{\rm o}{\rm C})$		38.5	W	
		- Derate above 25°C		0.3	W/ºC	
T _J , T _{STG}	Operating and Storage Temperature Range			-55 to +150	°C	
TL	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Seconds			300	°C	

Thermal Characteristics

Symbol	Parameter	FDPF16N50UT	Unit
$R_{ ext{ heta}JC}$	Thermal Resistance, Junction to Case, Max.	3.3	
$R_{ ext{ heta}JA}$	Thermal Resistance, Junction to Ambient, Max.	62.5	°C/W

Part Number To		Top Mark	Package	Packing Method	Reel Size	Tape Width		Quantity	
•		TO-220F			N/A		50 units		
Electrica	l Char	acteristics T _{c = 25}	°C unless otherwis	e noted.					
Symbol	Parameter			Test Conditions		Min.	Тур.	Max.	Unit
Off Charac	teristic	S							
BV _{DSS}	Drain to Source Breakdown Voltage		age Ir	I _D = 250μA, V _{GS} = 0V, T _J = 25°C		500	-	-	V
ΔBV _{DSS} / ΔT _J	Breakdown Voltage Temperature Coefficient			$I_D = 250\mu$ A, Referenced to 25°C		-	0.5	-	V/ºC
				$V_{DS} = 500V, V_{GS} = 0V$		-	-	25	
IDSS	Zero Gate Voltage Drain Current			$V_{DS} = 400V, T_{C} = 125^{\circ}C$		-	-	250	μA
I _{GSS}	Gate to	te to Body Leakage Current		$V_{GS} = \pm 30V, V_{DS} = 0V$		-	-	±100	nA
On Charac	teristic	s						•	·
V _{GS(th)}	Gate Threshold Voltage		V	$V_{GS} = V_{DS}, I_{D} = 250 \mu A$		3.0	- 1	5.0	V
R _{DS(on)}		Static Drain to Source On Resistance		$V_{GS} = 10V, I_D = 7.5A$		-	0.37	0.48	Ω
9FS	Forward Transconductance			$V_{\rm DS} = 40V, I_{\rm D} = 7.5A$		-	23	-	S
Dynamic (C _{iss} C _{oss} C _{rss}	Input Ca Output	apacitance Capacitance e Transfer Capacitance		V _{DS} = 25V, V _{GS} = 0V f = 1MHz		-	1495 235 20	1945 310 30	pF pF pF
Q _{g(tot)}		ate Charge at 10V				-	32	45	nC
Q _{gs}		Source Gate Charge	V	ν _{DS} = 400V, I _D = 15Α	-	-	8.5	-	nC
Q _{gd}		Drain "Miller" Charge	V	′ _{GS} = 10V	(Note 4)		14	-	nC
∽gu									
	Charac	teristics					40	90	ns
Switching		Delay Time				-	40		
	Turn-Or		v	_{DD} = 250V, I _D = 15A	_	-	40 150	310	ns
Switching t _{d(on)}	Turn-Or Turn-Or	n Delay Time		_{DD} = 250V, I _D = 15A _G = 25Ω		· ·	-		ns ns
Switching t _{d(on)} t _r	Turn-Or Turn-Or Turn-Of	n Delay Time n Rise Time			(Note 4)		150	310	-
Switching t _{d(on)} t _r t _{d(off)} t _f	Turn-Or Turn-Or Turn-Of Turn-Of	n Delay Time n Rise Time f Delay Time			(Note 4)	-	150 65	310 140	ns
Switching $t_{d(on)}$ t_r $t_{d(off)}$ t_f Drain-Sou	Turn-Or Turn-Or Turn-Of Turn-Of	n Delay Time n Rise Time f Delay Time f Fall Time	R	e _G = 25Ω	(Note 4)	-	150 65	310 140	ns
Switching t _{d(on)} t _r t _{d(off)} t _f Drain-Sou I _S	Turn-Or Turn-Or Turn-Of Turn-Off Turn-Off	n Delay Time n Rise Time f Delay Time f Fall Time de Characteristics	R ource Diode F	$r_{\rm G} = 25\Omega$	(Note 4)		150 65 80	310 140 170	ns ns
Switching t _{d(on)} t _r t _{d(off)} t _f Drain-Sou I _S I _{SM}	Turn-Or Turn-Of Turn-Off Turn-Off Turn-Off Turn-Off Maximu Maximu	n Delay Time n Rise Time f Delay Time f Fall Time de Characteristics m Continuous Drain to S	ource Diode F e Diode Forwa	G = 25Ω forward Current ard Current	(Note 4)		150 65 80	310 140 170 15	ns ns A
Switching $t_{d(on)}$ t_r $t_{d(off)}$ t_f Drain-Sou l_S	Turn-Or Turn-Or Turn-Of Turn-Of Turn-Of Turn-Of Maximu Maximu Drain to	n Delay Time n Rise Time f Delay Time f Fall Time de Characteristics m Continuous Drain to S m Pulsed Drain to Sourc	ource Diode F e Diode Forwa /oltage V	$r_{\rm G} = 25\Omega$	(Note 4)		150 65 80 - -	310 140 170 15 60	ns ns A A

NOTES:

1. Repetitive rating: pulse-width limited by maximum junction temperature.

2. L = 5.5 mH, I_{AS} = 15 A, V_{DD} = 50 V, R_G = 25 $\Omega,$ starting T_J = 25°C.

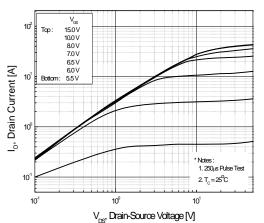
3. I_{SD} \leq 16 A, di/dt \leq 200 A/µs, V_{DD} \leq BV_{DSS}, starting T_J = 25°C.

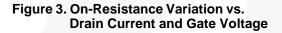
4. Essentially independent of operating temperature typical characteristics.

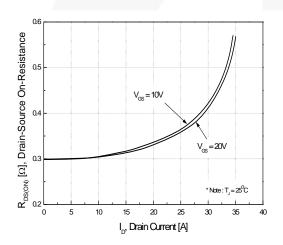


Typical Performance Characteristics











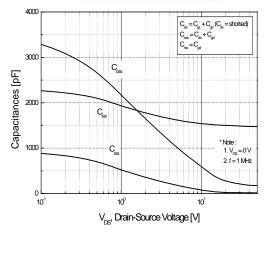
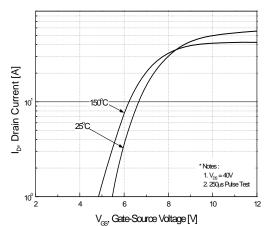


Figure 2. Transfer Characteristics





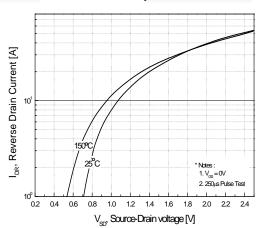
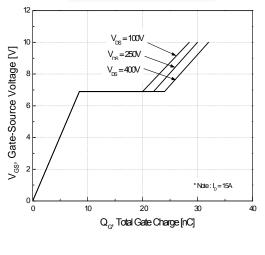
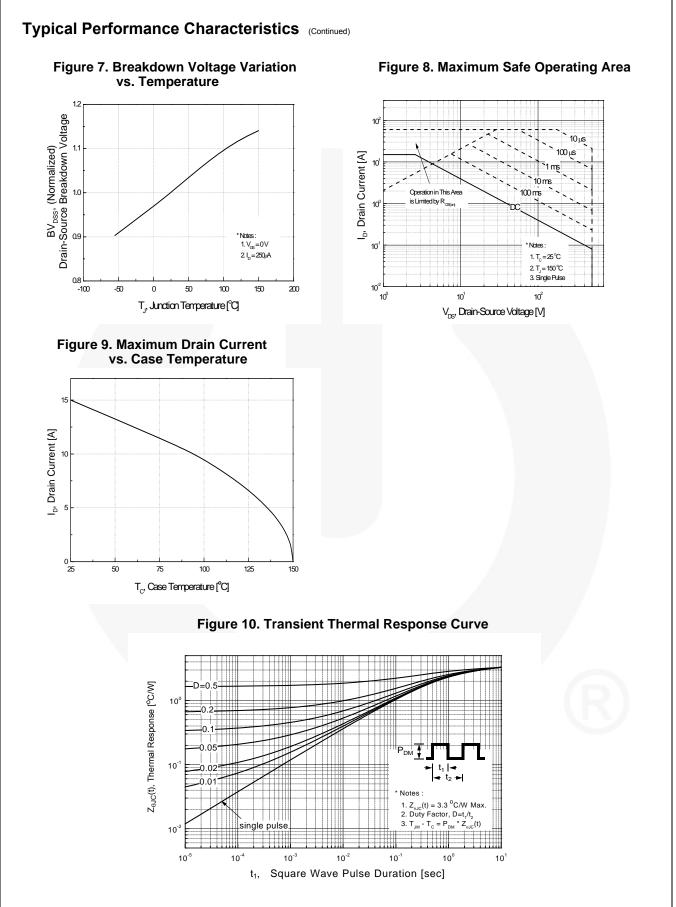


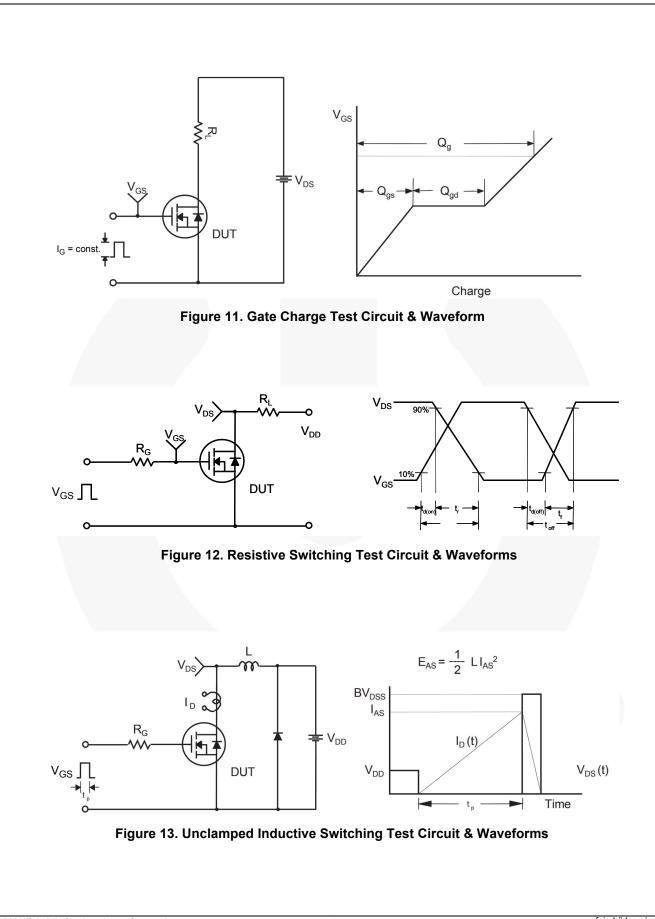
Figure 6. Gate Charge Characteristics



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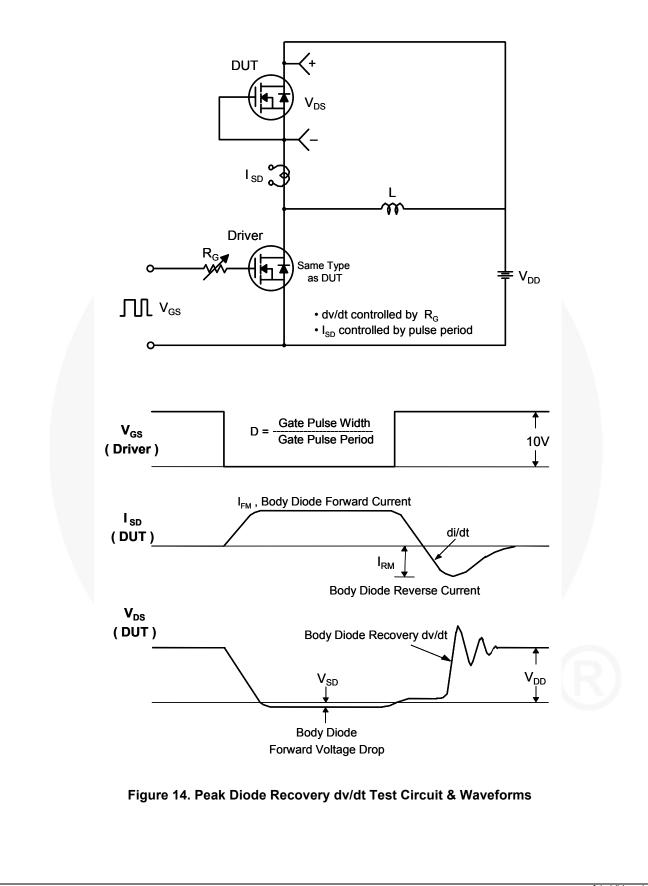


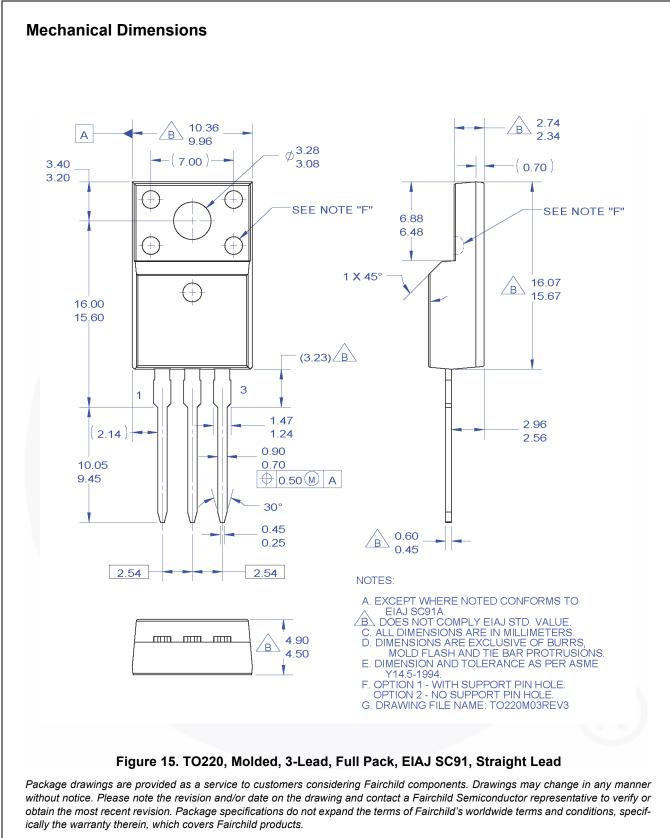
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