

# **HD74LS145**

R04DS0007EJ0400 (Previous: REJ03D0436-0300)

Rev.4.00 Dec 24, 2010

BCD-to-Decimal Decoder / Driver (with 15 V outputs)

This BCD-to-decimal decoder / driver consists of eight inverters and ten four-input NAND gates. The inverters are connected in pairs to make BCD input data available for decoding by the NAND gates. Full decoding of valid BCD input logic ensures that all outputs remain off for all invalid binary input conditions. This decoder features high-performance, n-p-n output transistors designed for use as indicator / relay drivers or as open-collector logic-circuit drivers.

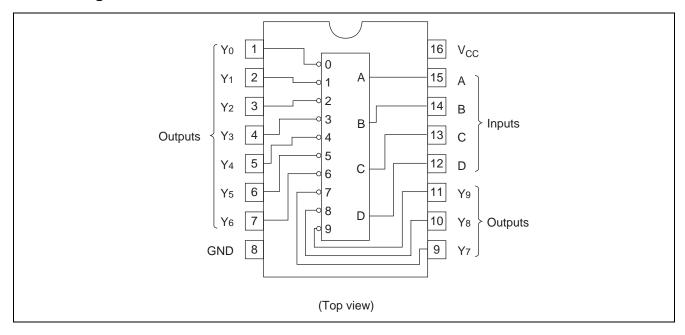
#### **Features**

• Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)	
HD74LS145P	DILP-16 pin	PRDP0016AE-B (DP-16FV)	Р	_	
HD74LS145FPEL	SOP-16 pin (JEITA)	PRSP0016DH-B (FP-16DAV)	FP	EL (2,000 pcs/reel)	
HD74LS145RPEL	SOP-16 pin (JEDEC)	PRSP0016DG-A (FP-16DNV)	RP	EL (2,500 pcs/reel)	

Note: Please consult the sales office for the above package availability.

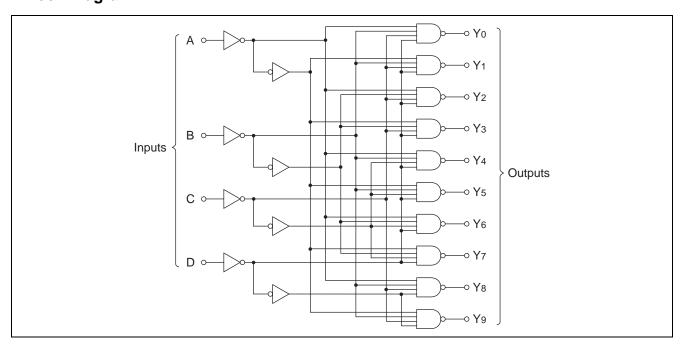
#### **Pin Arrangement**



### **Function Table**

No.	Inputs			Outputs										
NO.	D	С	В	Α	0	1	2	3	4	5	6	7	8	9
0	L	L	L	L	L	Н	Н	Н	Н	Н	Н	Н	Н	Н
1	L	L	L	Н	Н	L	Н	Н	Н	Н	Н	Н	Н	Н
2	L	L	Н	L	Н	Н	L	Н	Н	Н	Н	Н	Н	Н
3	L	L	Н	Н	Н	Н	Н	L	Н	Н	Н	Н	Н	Н
4	L	Н	L	L	Н	Н	Н	Н	L	Н	Н	Н	Н	Н
5	L	Н	L	Н	Ι	Н	Η	Ι	Н	L	Η	Н	Ι	Н
6	L	Н	Ι	L	Ι	Н	Η	Ι	Н	Ι	L	Н	Ι	Н
7	L	Н	Ι	Н	Ι	Н	Η	Ι	Н	Ι	Η	L	Ι	Н
8	Η	L	L	L	Ι	Н	Η	Ι	Н	Ι	Η	Н	L	Н
9	Η	L	L	Н	Ι	Н	Η	Ι	Н	Ι	Η	Н	Ι	L
	Ι	L	Ι	L	Ι	Н	Η	Ι	Н	Ι	Η	Н	Ι	Н
	Η	L	Ι	Н	Ι	Н	Η	Ι	Н	Ι	Η	Н	Ι	Н
Invalid	Η	Н	L	L	Ι	Н	Η	Ι	Н	Ι	Η	Н	Ι	Н
ilivaliu	Н	Н	L	Н	Н	Н	Н	Н	Н	Η	Н	Н	Η	Н
	Н	Н	Н	L	Н	Н	Н	Н	Н	Η	Н	Н	Η	Н
	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н

# **Block Diagram**



# **Absolute Maximum Ratings**

Item	Symbol	Ratings	Unit	
Supply voltage	V <sub>CC</sub>	7	V	
Input voltage	V <sub>IN</sub>	7	V	
Power dissipation	P <sub>T</sub>	400	mW	
Storage temperature	Tstg	-65 to +150	°C	

Note: Voltage value, unless otherwise noted, are with respect to network ground terminal.

# **Recommended Operating Conditions**

Item	Symbol	Min	Тур	Max	Unit
Supply voltage	$V_{CC}$	4.75	5.00	5.25	V
Off-state output voltage	$V_{O(off)}$	_	_	15	V
Low level output current	I <sub>OL</sub>	_	_	80	mA
Operating temperature	Topr	-20	25	75	°C

### **Electrical Characteristics**

 $(Ta = -20 \text{ to } +75 \text{ }^{\circ}\text{C})$ 

Item	Symbol	min.	typ.*	max.	Unit	Condition
Input voltage	V <sub>IH</sub>	2.0		_	V	
Input voltage	$V_{IL}$	_	_	0.8	V	
Off-state output current	I <sub>O(off)</sub>	_	_	250	μА	$V_{CC} = 4.75 \text{ V}, V_{IH} = 2 \text{ V}, V_{IL} = 0.8 \text{ V}, V_{O(off)} = 15 \text{ V}$
On state systems	V <sub>O(on)</sub>	_		0.4	V	I <sub>OL</sub> = 12 mA
On-state output voltage		_	_	0.5		$V_{CC} = 4.75 \text{ V}, V_{IH} = 2 \text{ V},$ $V_{II} = 0.8 \text{ V}$
voltage				3.0		I <sub>OL</sub> = 80 mA
	I <sub>IH</sub>			20	μΑ	$V_{CC} = 5.25 \text{ V}, V_{I} = 2.7 \text{ V}$
Input current	I <sub>IL</sub>	_	_	-0.4	mA	$V_{CC} = 5.25 \text{ V}, V_I = 0.4 \text{ V}$
	l <sub>i</sub>			0.1	mA	$V_{CC} = 5.25 \text{ V}, V_I = 7 \text{ V}$
Supply current**	Icc	_	7	13	mA	V <sub>CC</sub> = 5.25 V
Input clamp voltage	$V_{IK}$	_		-1.5	V	$V_{CC} = 4.75 \text{ V}, I_{IN} = -18 \text{ mA}$

Notes:  $V_{CC} = 5 \text{ V}$ ,  $Ta = 25^{\circ}\text{C}$ 

# **Switching Characteristics**

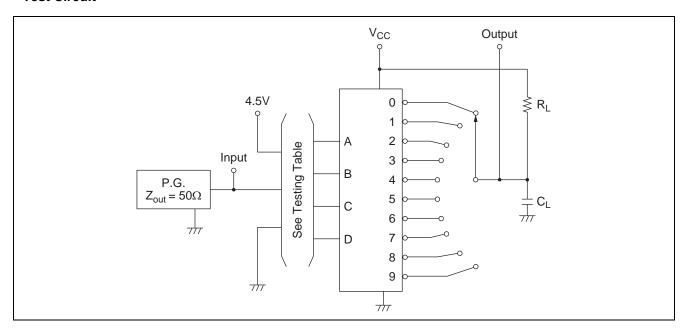
 $(V_{CC} = 5 \text{ V}, \text{ Ta} = 25^{\circ}\text{C})$ 

Item	Symbol	min.	typ.	max.	Unit	Condition
Propagation delay time	t <sub>PLH</sub>	_	_	50	ns	$C_L = 45 \text{ pF}, R_L = 665 \Omega$
Fropagation delay time	t <sub>PHL</sub>	_	_	50	115	$C_L = 45 \text{ pr}, R_L = 665 \Omega$

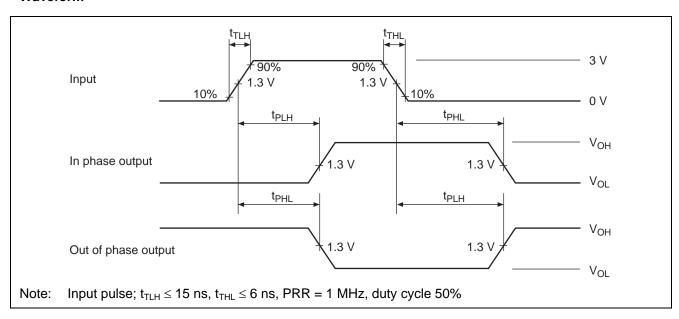
 $<sup>^{\</sup>star\star}$   $I_{\text{CC}}$  is measured with all outputs open and all inputs grounded.

# **Testing Method**

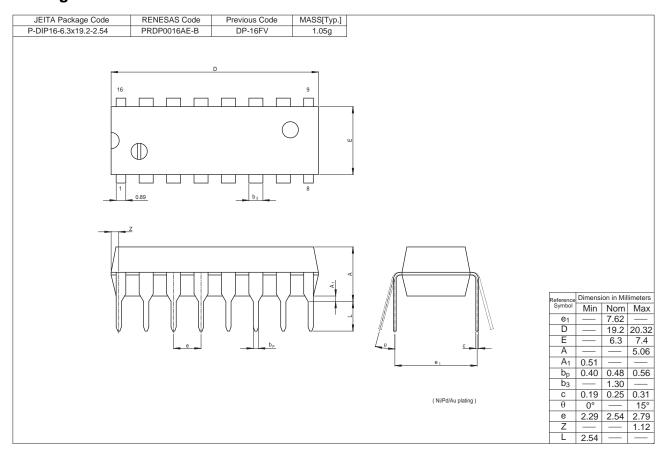
### **Test Circuit**

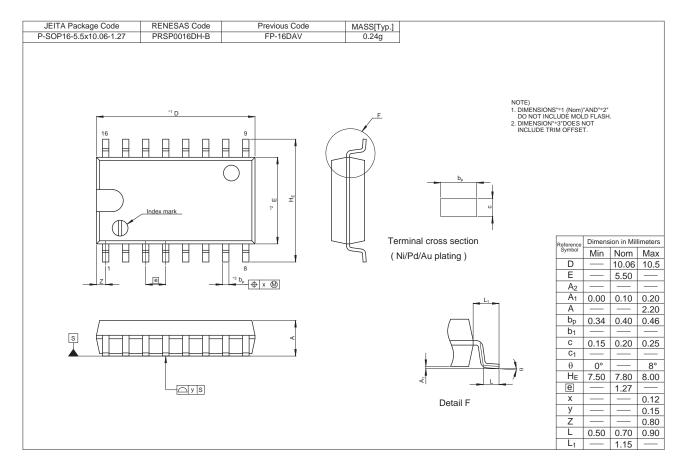


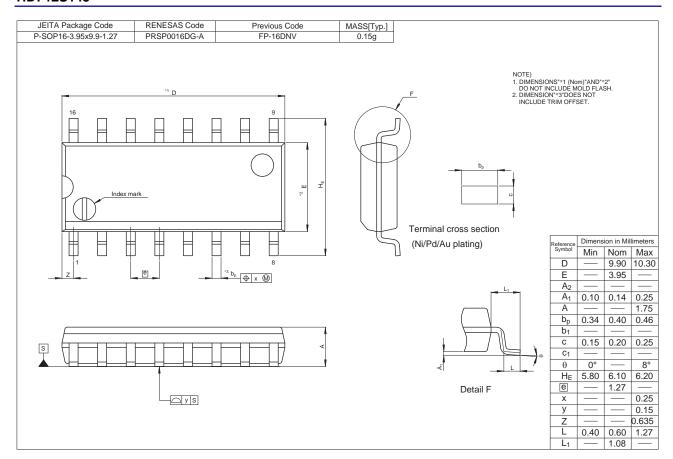
### Waveform



### **Package Dimensions**







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