

Lonten N-channel 650V, 47A, 0.07Ω LonFET[™] Power MOSFET

Description

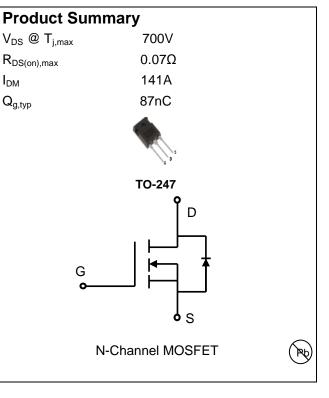
LonFETTM Power MOSFET is fabricated using advanced super junction technology. The resulting device has extremely low on resistance, making it especially suitable for applications which require superior power density and outstanding efficiency.

Features

- ◆ Ultra low R_{DS(on)}
- ◆ Ultra low gate charge (typ. Q_g = 87nC)
- ◆ 100% UIS tested
- RoHS compliant

Applications

- Power faction correction (PFC).
- Switched mode power supplies (SMPS).
- Uninterruptible power supply (UPS).



Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V _{DSS}	650	V
Continuous drain current (T _C = 25°C)	I _D	47	Α
(T _C = 100°C)		30	Α
Pulsed drain current 1)	I _{DM}	141	Α
Gate-Source voltage	V_{GSS}	±30	V
Avalanche energy, single pulse 2)	E _{AS}	1200	mJ
Power Dissipation TO-247 (T _C = 25°C)		290	W
- Derate above 25°C	P _D	2.32	W/°C
Operating and Storage Temperature Range	T_J , T_{STG}	-55 to +150	°C
Continuous diode forward current	Is	47	Α
Diode pulse current	I _{S,pulse}	141	Α

Thermal Characteristics TO-247

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case	R _{eJC}	0.43	°C/W
Thermal Resistance, Junction-to-Ambient	R _{θJA}	60	°C/W
Soldering temperature, wavesoldering only allowed	т	260	°C
at leads. (1.6mm from case for 10s)	I sold		



Package Marking and Ordering Information

Device	Device Package	Marking	Units/Tube	Units/Real
LSB65R070GT	TO-247	LSB65R070GT	30	

Electrical Characteristics T_c = 25°C unless otherwise noted

Parameter	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Static characteristics						•
Drain-source breakdown voltage	BV _{DSS}	V _{GS} =0 V, I _D =0.25 mA	650	-	-	V
Gate threshold voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =0.25mA	2.5	3.5	4.5	V
Drain cut-off current	I _{DSS}	V _{DS} =650 V, V _{GS} =0 V,				μΑ
		$T_j = 25$ °C	-	-	1	
		T _j = 125°C	-	10	-	
Gate leakage current, Forward	I _{GSSF}	V _{GS} =30 V, V _{DS} =0 V	-	-	50	nA
Gate leakage current, Reverse	I _{GSSR}	V _{GS} =-30 V, V _{DS} =0 V	-	-	-50	nA
Drain-source on-state resistance	R _{DS(on)}	V _{GS} =10 V, I _D =23.5 A	-			
		T _j = 25°C	-	0.062	0.070	Ω
		T _j = 150°C	-	0.16	-	
Dynamic characteristics						•
Input capacitance	C _{iss}	$V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V},$	-	4677	-	
Output capacitance	C _{oss}	f = 1 MHz	-	2556	-	pF
Reverse transfer capacitance	C _{rss}		-	30	-	
Turn-on delay time	t _{d(on)}	V _{DD} = 400V, I _D = 23.5A	-	29.5	-	
Rise time	t _r	$R_G = 10\Omega$, $V_{GS}=10V$	-	63	-	ns
Turn-off delay time	t _{d(off)}		-	165.5	-	
Fall time	t _f		-	13	-	
Gate charge characteristics		•	<u> </u>			I
Gate to source charge	Q_{gs}	V _{DD} =480 V, I _D =23.5A,	-	24	-	
Gate to drain charge	Q_{gd}	V _{GS} =0 to 10 V	-	31.24	-	nC
Gate charge total	Qg		-	87	-	
Gate plateau voltage	V _{plateau}		-	5.5	-	V
Reverse diode characteristics		•	<u> </u>			I
Diode forward voltage	V _{SD}	V _{GS} =0 V, I _F =23.5A	-	1.0	-	V
Reverse recovery time	t _{rr}	V _R =50 V, I _F =47A,	-	605	-	ns
Reverse recovery charge	Q _{rr}	dI _F /dt=100 A/µs	-	8.8	-	μC
Peak reverse recovery current	I _{rrm}	1	_	26.6	_	Α

Notes:

^{1.} Limited by maximum junction temperature, maximum duty cycle is 0.75.

^{2.} $I_{AS} = 8A$, $V_{DD} = 60V$, Starting $T_j = 25^{\circ}C$.



Electrical Characteristics Diagrams

Figure 1. On-Region Characteristics

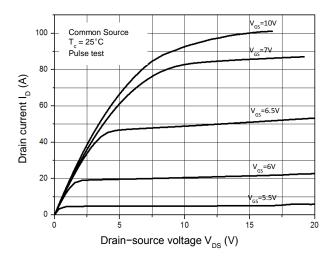


Figure 3. On-Resistance Variation vs. Drain Current

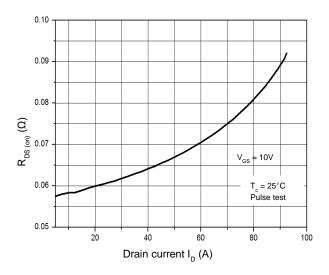


Figure 5. Breakdown Voltage vs. Temperature

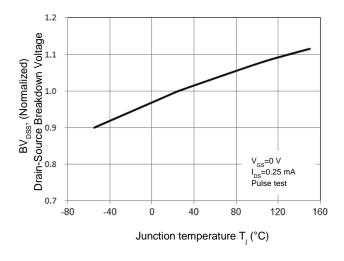


Figure 2. Transfer Characteristics

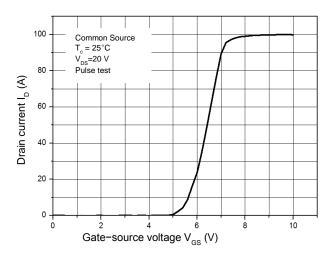


Figure 4. Threshold Voltage vs. Temperature

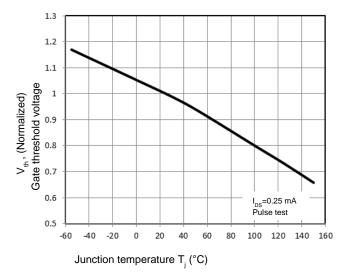


Figure 6. On-Resistance vs. Temperature

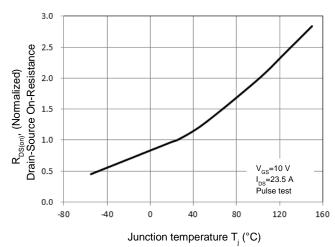




Figure 7. Capacitance Characteristics

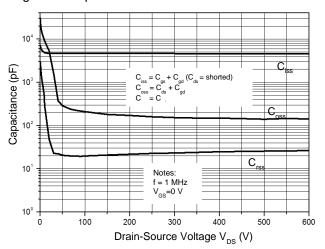


Figure 9. Maximum Safe Operating Area

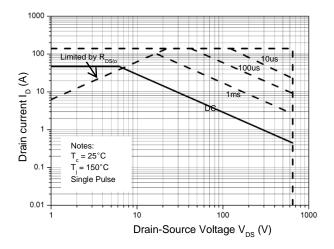


Figure 8. Gate Charge Characterist

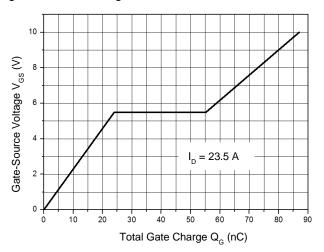


Figure 10. Power Dissipation vs. Temperature

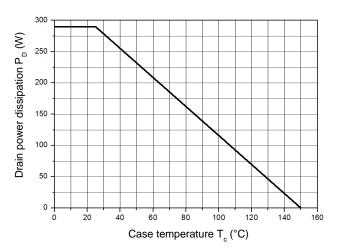
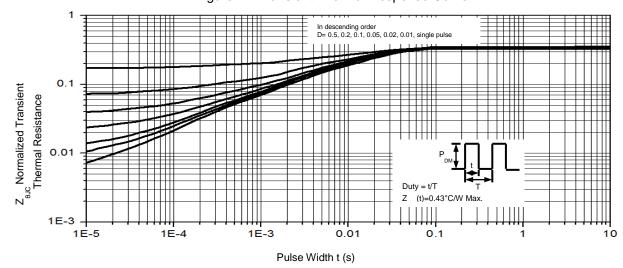
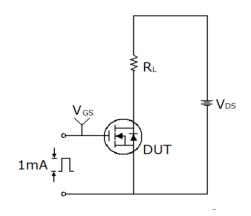


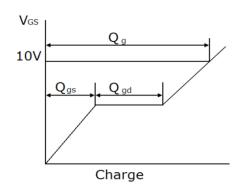
Figure 11. Transient Thermal Response Curve

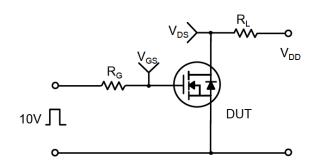


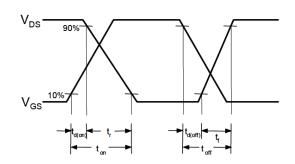


Gate Charge Test Circuit & Waveform

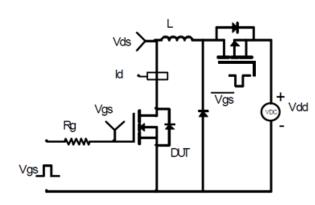


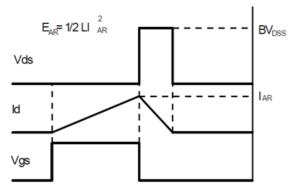






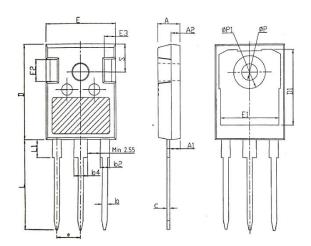
Unclamped Inductive Switching Test Circuit & Waveforms





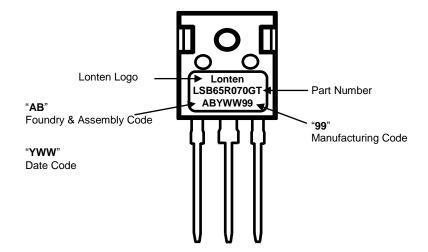


Mechanical Dimensions for TO-247



SYMBOL	mm			
OTIVIDOL	MIN	NOM	MAX	
Α	4.80	5.00	5.20	
A1	2.21	2.41	2.59	
A2	1.85	2.00	2.15	
b	1.11	1.21	1.36	
b2	1.91	2.01	2.21	
b4	2.91	3.01	3.21	
С	0.51	0.61	0.75	
D	20.80	21.00	21.30	
D1	16.25	16.55	16.85	
Е	15.50	15.80	16.10	
E1	13.00	13.30	13.60	
E2	4.80	5.00	5.20	
E3	2.30	2.50	2.70	
е	5.44BSC			
L	19.82	19.92	20.22	
L1	_	_	4.30	
ØP	3.40	3.60	3.80	
ØP1	_	_	7.30	
S	6.15BSC			

TO-247 Part Marking Information





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