

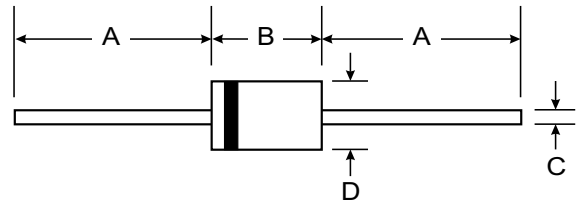
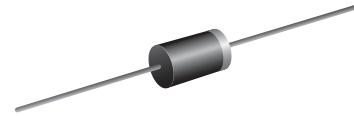
VOLTAGE RANGE: 50 - 1000V
CURRENT: 2.0 A

Features

- Diffused Junction
- Low Forward Voltage Drop
- High Current Capability
- High Reliability
- High Surge Current Capability

Mechanical Data

- Case: DO - 15 , Molded Plastic
- Terminals: Plated Leads Solderable per MIL-STD-202, Method 208
- Polarity: Cathode Band
- Weight: 0.40 grams (approx.)
- Mounting Position: Any
- Marking: Type Number



DO-15		
Dim	Min	Max
A	25.40	—
B	5.50	7.62
C	0.686	0.889
D	2.60	3.60
All Dimensions in mm		

Maximum Ratings and Electrical Characteristics @ $T_A=25^{\circ}\text{C}$ unless otherwise specified

Single Phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Characteristic	Symbol	UF200	UF201	UF202	UF204	UF205	UF206	UF208	UF2010	Unit	
Peak Repetitive Reverse Voltage	V_{RRM}	50	100	200	300	400	600	800	1000	V	
Working Peak Reverse Voltage	V_{RWM}										
DC Blocking Voltage	V_R										
RMS Reverse Voltage	$V_{R(RMS)}$	35	70	140	210	280	420	560	700	V	
Average Rectified Output Current (Note 1)	I_O	2.0								A	
		@ $T_A = 55^{\circ}\text{C}$									
Non-Repetitive Peak Forward Surge Current	I_{FSM}	60								A	
		8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)									
Forward Voltage	V_{FM}	1.0			1.3	1.7				V	
		@ $I_F = 2.0\text{A}$									
Peak Reverse Current	I_{RM}	5.0								μA	
		@ $T_A = 25^{\circ}\text{C}$									
		@ $T_A = 100^{\circ}\text{C}$									
Reverse Recovery Time (Note 2)	t_{rr}	50					75				nS
Typical Junction Capacitance (Note 3)	C_j	60					40				pF
Operating Temperature Range	T_j	-65 to +125								$^{\circ}\text{C}$	
Storage Temperature Range	T_{STG}	-65 to +150								$^{\circ}\text{C}$	

- Note: 1. Leads maintained at ambient temperature at a distance of 9.5mm from the case
 2. Measured with $I_F = 0.5\text{A}$, $I_R = 1.0\text{A}$, $I_{RR} = 0.25\text{A}$. See figure 5.
 3. Measured at 1.0 MHz and applied reverse voltage of 4.0V D.C.

RATING AND CHARACTERISTIC CURVES UF200 THRU UF2010

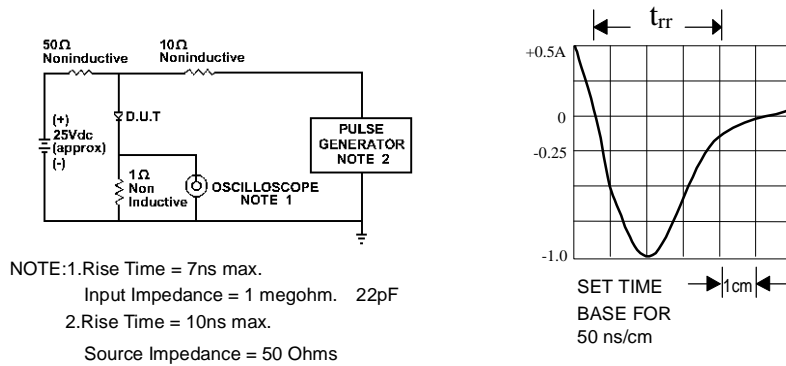


Fig. 1-REVERSE RECOVERY TIME CHARACTERISTIC AND TEST CIRCUIT DIAGRAM

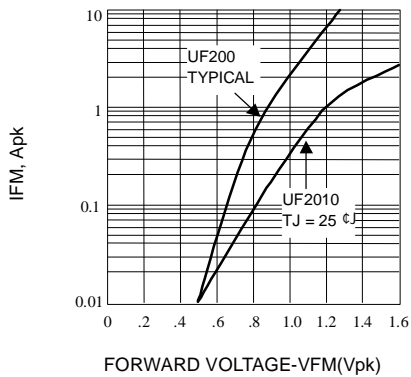


Fig. 2-FORWARD CHARACTERISTICS

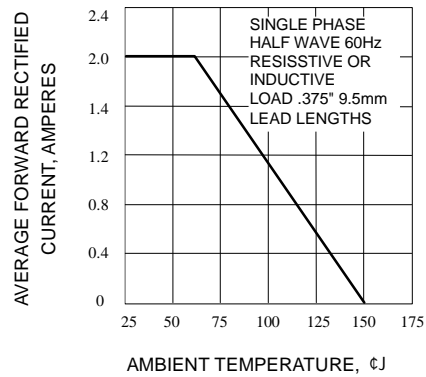


Fig. 3-FORWARD CURRENT DERATING CURVE

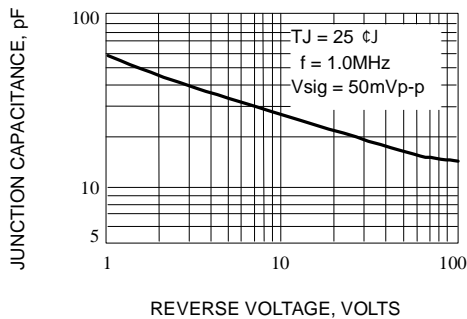


Fig. 4-TYPICAL JUNCTION CAPACITANCE

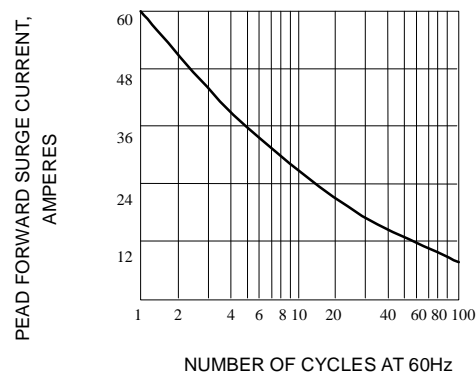


Fig. 5-PEAK FORWARD SURGE CURRENT