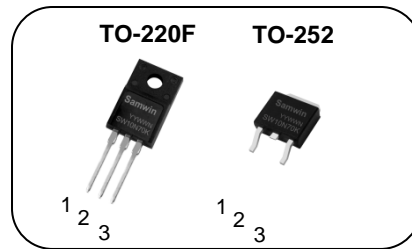


## N-channel Enhanced mode TO-220F/TO-252 MOSFET

### Features

- High ruggedness
- Low  $R_{DS(ON)}$  (Typ 0.36Ω)@ $V_{GS}=10V$
- Low Gate Charge (Typ 29nC)
- Improved dv/dt Capability
- 100% Avalanche Tested
- Application: Charge, LED, TV-Power

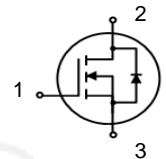


1. Gate 2. Drain 3. Source

$BV_{DSS}$  : 700V

$I_D$  : 10A

$R_{DS(ON)}$  : 0.36Ω



### General Description

This power MOSFET is produced with super junction advanced technology of SAMWIN. This technology enable the power MOSFET to have better characteristics, including fast switching time, low on resistance, low gate charge and especially excellent avalanche characteristics.



### Order Codes

Item	Sales Type	Marking	Package	Packaging
1	SW F 10N70K	SW10N70K	TO-220F	TUBE
2	SW D 10N70K	SW10N70K	TO-252	REEL

### Absolute maximum ratings

Symbol	Parameter	Value		Unit
		TO-220F	TO-252	
$V_{DSS}$	Drain to source voltage	700		V
$I_D$	Continuous drain current (@ $T_C=25^\circ C$ )	10*		A
	Continuous drain current (@ $T_C=100^\circ C$ )	6.3*		A
$I_{DM}$	Drain current pulsed (note 1)	40		A
$V_{GS}$	Gate to source voltage	$\pm 30$		V
$E_{AS}$	Single pulsed avalanche energy (note 2)	120		mJ
$E_{AR}$	Repetitive avalanche energy (note 1)	15		mJ
dv/dt	MOSFET dv/dt ruggedness (@ $V_{DS}=0\sim 400V$ )	30		V/ns
dv/dt	Peak diode recovery dv/dt (note 3)	20		V/ns
$P_D$	Total power dissipation (@ $T_C=25^\circ C$ )	25.5	240.4	W
	Derating factor above 25°C	0.2	1.9	W/°C
$T_{STG}, T_J$	Operating junction temperature & storage temperature	-55 ~ + 150		°C
$T_L$	Maximum lead temperature for soldering purpose, 1/8 from case for 5 seconds.	300		°C

\*. Drain current is limited by junction temperature.

### Thermal characteristics

Symbol	Parameter	Value		Unit
		TO-220F	TO-252	
$R_{thjc}$	Thermal resistance, Junction to case	4.9	0.5	°C/W
$R_{thja}$	Thermal resistance, Junction to ambient	48.7		°C/W

Electrical characteristic (  $T_C = 25^\circ\text{C}$  unless otherwise specified )

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
<b>Off characteristics</b>						
$BV_{DSS}$	Drain to source breakdown voltage	$V_{GS}=0V, I_D=250\mu A$	700			V
$\Delta BV_{DSS} / \Delta T_J$	Breakdown voltage temperature coefficient	$I_D=250\mu A$ , referenced to $25^\circ\text{C}$		0.65		$V/^\circ\text{C}$
$I_{DSS}$	Drain to source leakage current	$V_{DS}=700V, V_{GS}=0V$			1	$\mu A$
		$V_{DS}=560V, T_C=125^\circ\text{C}$			50	$\mu A$
$I_{GSS}$	Gate to source leakage current, forward	$V_{GS}=30V, V_{DS}=0V$			100	nA
	Gate to source leakage current, reverse	$V_{GS}=-30V, V_{DS}=0V$			-100	nA
<b>On characteristics</b>						
$V_{GS(TH)}$	Gate threshold voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	2		5	V
$R_{DS(ON)}$	Drain to source on state resistance	$V_{GS}=10V, I_D=5A$		0.36	0.42	$\Omega$
$G_{fs}$	Forward transconductance	$V_{DS}=30V, I_D=5A$		7		S
<b>Dynamic characteristics</b>						
$C_{iss}$	Input capacitance	$V_{GS}=0V, V_{DS}=200V, f=1\text{MHz}$		972		pF
$C_{oss}$	Output capacitance			47		
$C_{riss}$	Reverse transfer capacitance			2.5		
$t_{d(on)}$	Turn on delay time	$V_{DS}=350V, I_D=10A, V_{GS}=10V, R_G=25\Omega$ (note 4,5)		16		ns
$t_r$	Rising time			34		
$t_{d(off)}$	Turn off delay time			55		
$t_f$	Fall time			27		
$Q_g$	Total gate charge	$V_{DS}=560V, V_{GS}=10V, I_D=10A$ (note 4,5)		29		nC
$Q_{gs}$	Gate-source charge			7		
$Q_{gd}$	Gate-drain charge			14		

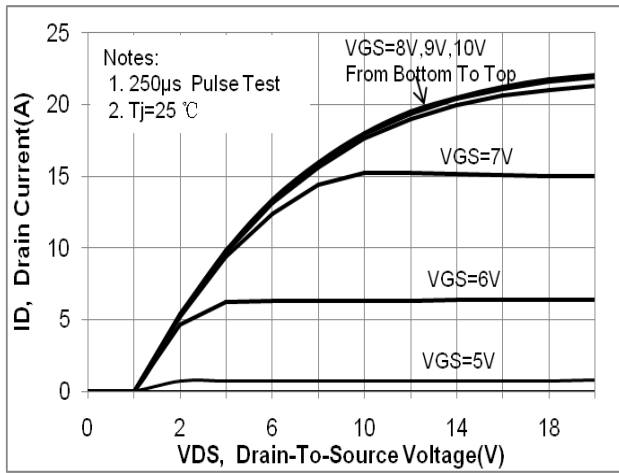
### Source to drain diode ratings characteristics

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
$I_S$	Continuous source current	Integral reverse p-n Junction diode in the MOSFET			10	A
$I_{SM}$	Pulsed source current				40	A
$V_{SD}$	Diode forward voltage drop.	$I_S=10A, V_{GS}=0V$			1.4	V
$t_{rr}$	Reverse recovery time	$I_S=10A, V_{GS}=0V,$		266		ns
$Q_{rr}$	Reverse recovery charge	$di_f/dt=100A/\mu s$		3.6		$\mu C$

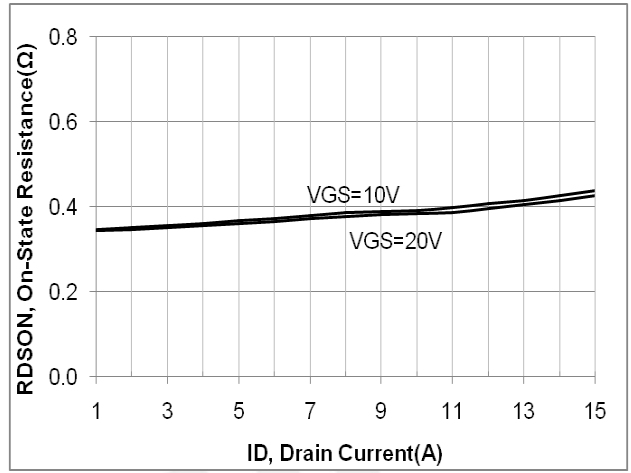
※. Notes

1. Repeattive rating : pulse width limited by junction temperature.
2.  $L = 60\text{mH}, I_{AS} = 2A, V_{DD} = 50V, R_G=25\Omega$ , Starting  $T_J = 25^\circ\text{C}$
3.  $I_{SD} \leq 10A, di/dt = 100A/\mu s, V_{DD} \leq BV_{DSS}$ , Starting  $T_J = 25^\circ\text{C}$
4. Pulse Test : Pulse Width  $\leq 300\mu s$ , duty cycle  $\leq 2\%$
5. Essentially independent of operating temperature.

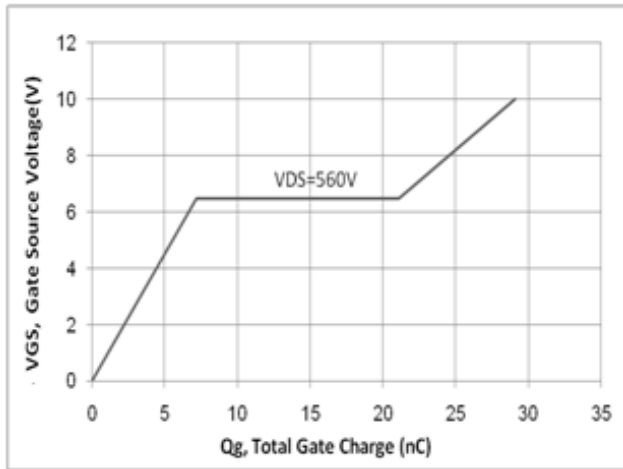
**Fig. 1. On-state characteristics**



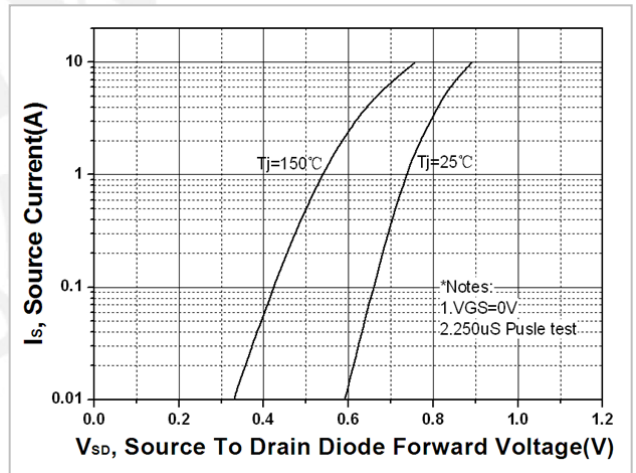
**Fig. 2. On-resistance variation vs. drain current and gate voltage**



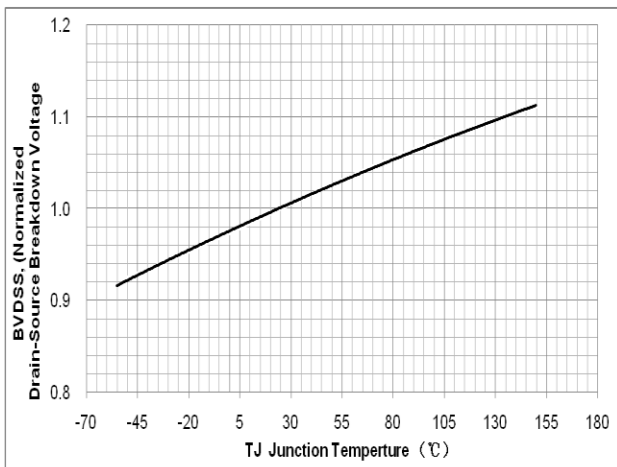
**Fig. 3. Gate charge characteristics**



**Fig. 4. On-state current vs. diode forward voltage**



**Fig 5. Breakdown voltage variation vs. junction temperature**



**Fig. 6. On-resistance variation vs. junction temperature**

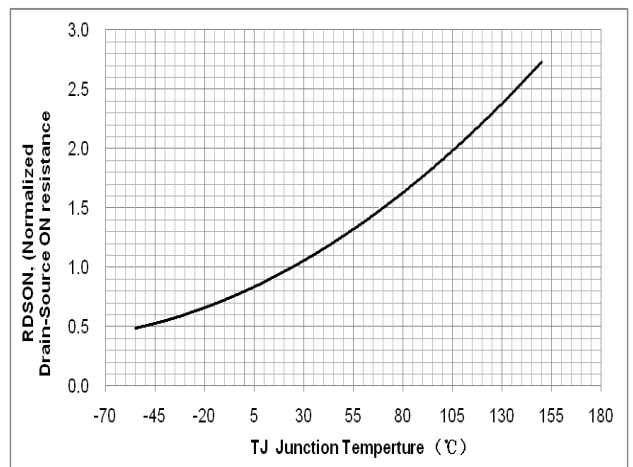


Fig. 7. Maximum safe operating area(TO-220F)

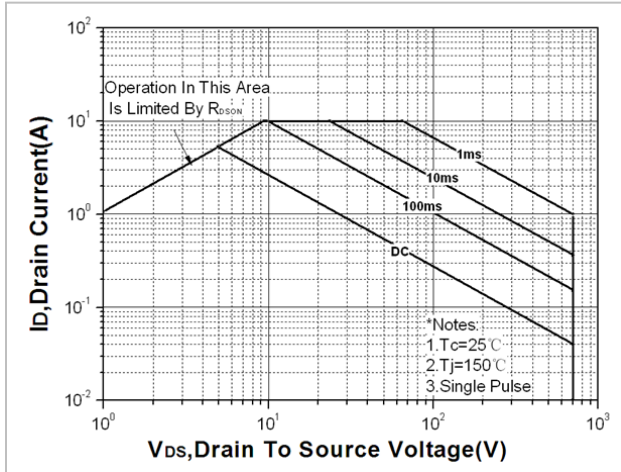


Fig. 8. Maximum safe operating area(TO-252)

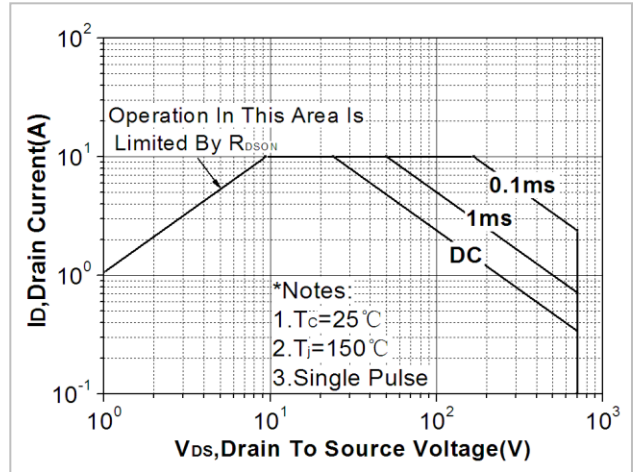


Fig. 9. Capacitance Characteristics

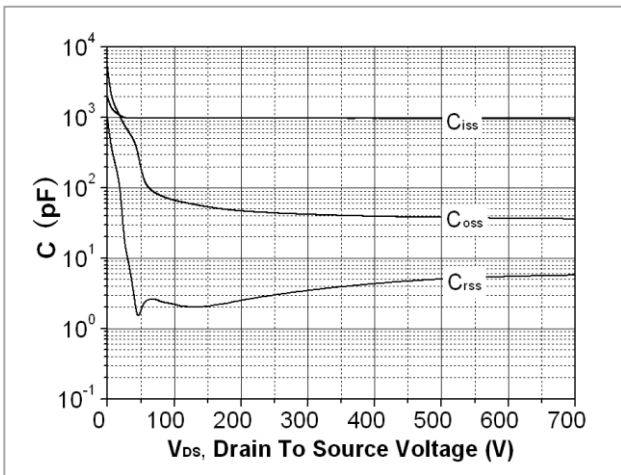


Fig. 10. Transient thermal response curve(TO-220F)

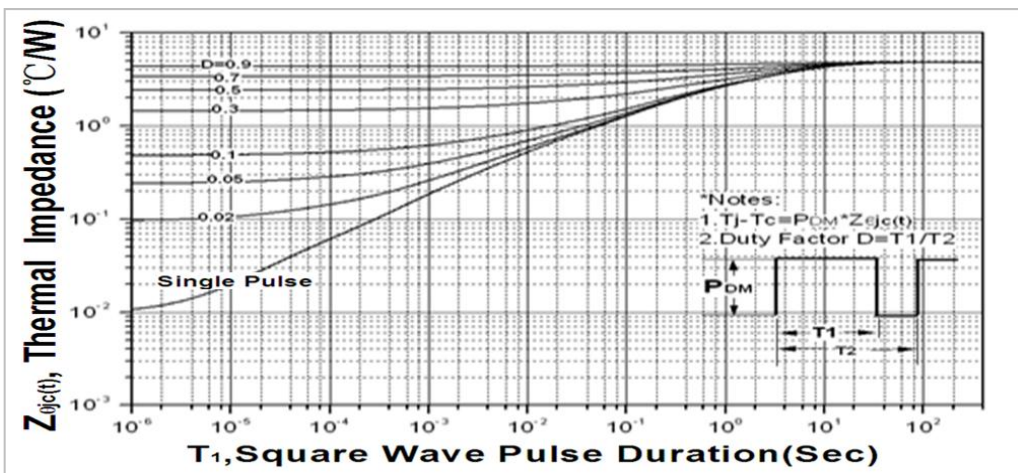


Fig. 11. Transient thermal response curve(TO-252)

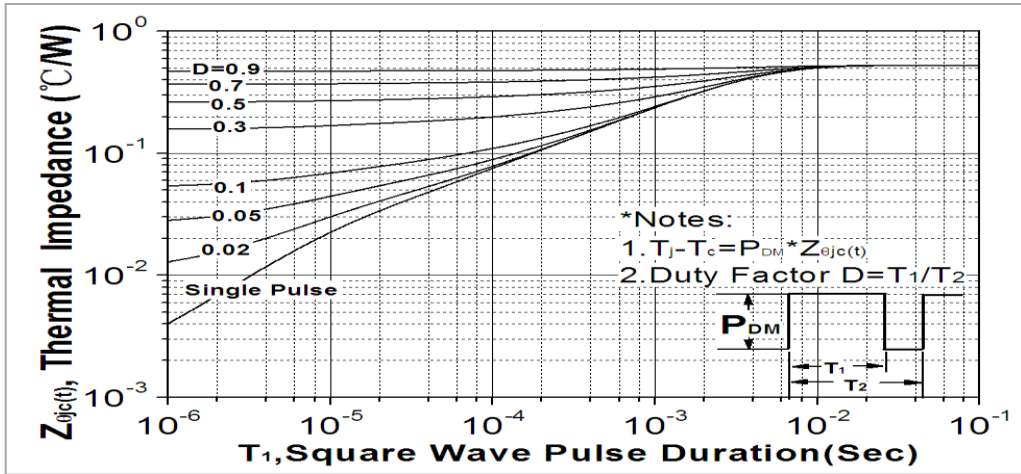


Fig. 12. Gate charge test circuit & waveform

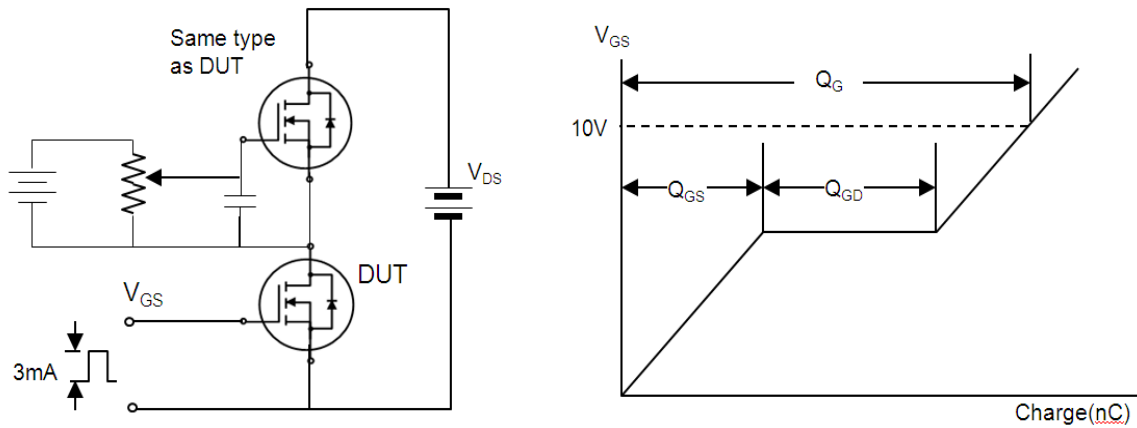


Fig. 13. Switching time test circuit & waveform

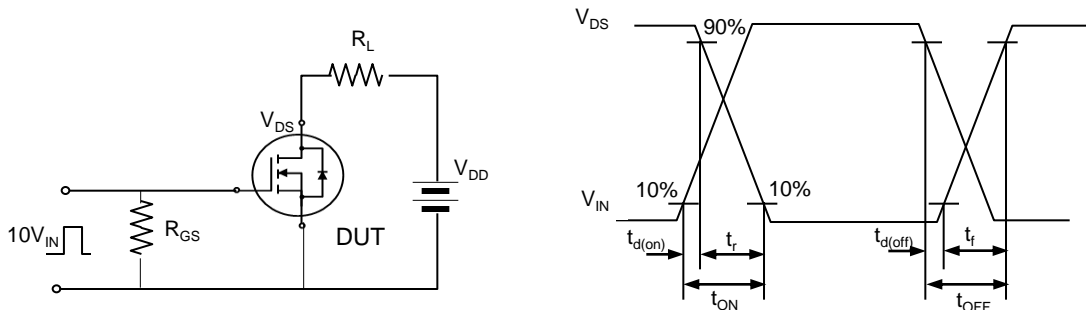


Fig. 14. Unclamped Inductive switching test circuit & waveform

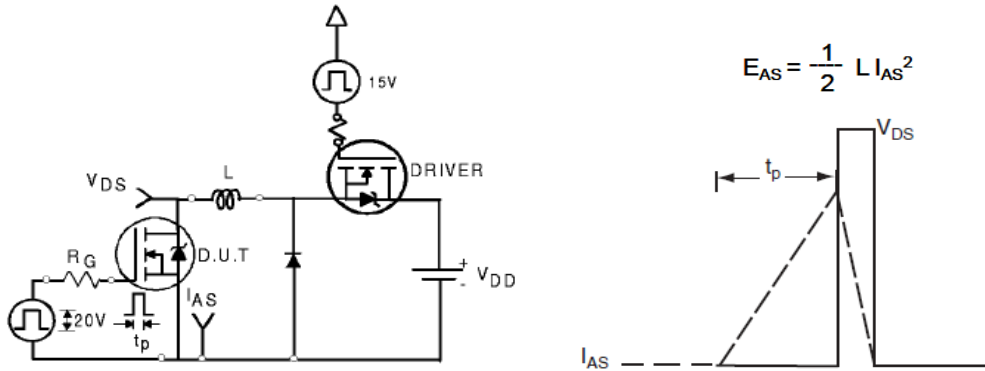
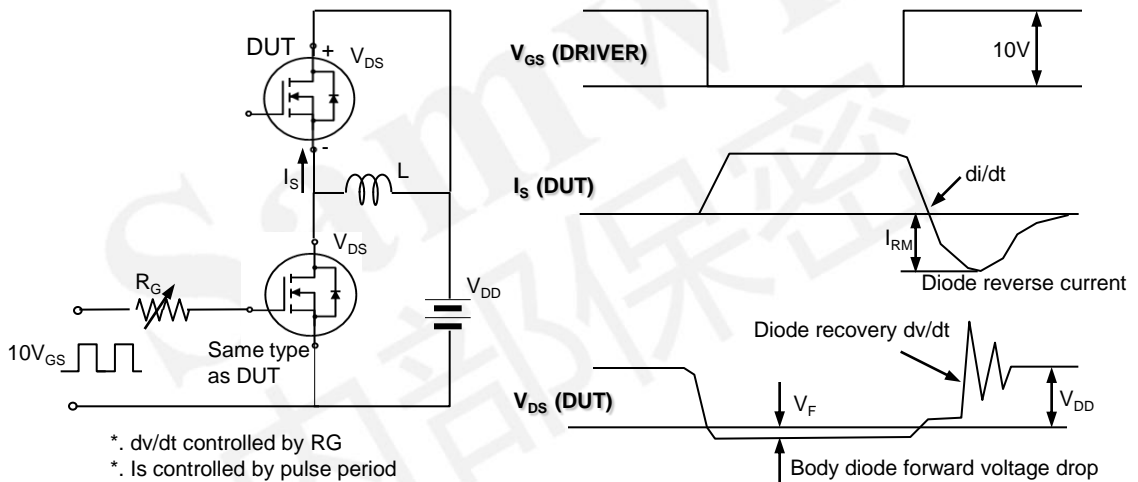


Fig. 15. Peak diode recovery dv/dt test circuit & waveform



## DISCLAIMER

\* All the data & curve in this document was tested in XI' AN SEMIPOWER TESTING & APPLICATION CENTE R.

\* This product has passed the PCT,TC,HTRB,HTGB,HAST,PC and Solderdunk reliability testing.

\* Qualification standards can also be found on the Web site (<http://www.semipower.com.cn>)

\* Suggestions for improvement are appreciated, Please send your suggestions to [samwin@winsemi.com](mailto:samwin@winsemi.com)