



SESD3ZxxC

Transient Voltage Suppressors for ESD Protection

Revision:B

General Description

The SESD3ZxxC is designed to protect voltage sensitive components from ESD and transient voltage events. Excellent clamping capability, low leakage, and fast response time, make these parts ideal for ESD protection on designs where board space is at a premium.

Applications

- Cellular phones
- Portable devices
- Digital cameras
- Power supplies

Features

- Small Body Outline Dimensions
- Low Body Height
- Peak Power up to 350 Watts @ 8 x 20 µs Pulse
- Low Leakage current
- Response Time is Typically < 1 ns
- ESD Rating of Class 3 (> 16 kV) per Human Body Model

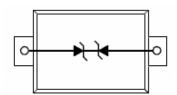
Complies with the following standards IEC61000-4-2

Level 4 15 kV (air discharge) 8 kV(contact discharge)

MIL STD 883E - Method 3015-7 Class 3 25 kV HBM (Human Body Model)

Functional diagram



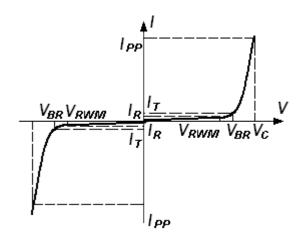


SOD-323

Absolute Ratings (T _{amb} =25°C)						
Symbol	Parameter	Value	Units			
P_{PK}	Peak Pulse Power (t _p = 8/20μs)	350	W			
T∟	Maximum lead temperature for soldering during 10s	260	ç			
T_{stg}	Storage Temperature Range	-55 to +155	°C			
Top	Operating Temperature Range	-40 to +125	°C			
TJ	Maximum junction temperature	150	°C			

Electrical Parameter

F				
Symbol	Parameter			
I _{PP}	Maximum Reverse Peak Pulse Current			
Vc	Clamping Voltage @ IPP			
V _{RWM}	Working Peak Reverse Voltage			
I _R	Maximum Reverse Leakage Current @ VRWM			
lτ	Test Current			
V _{BR}	Breakdown Voltage @ I⊤			

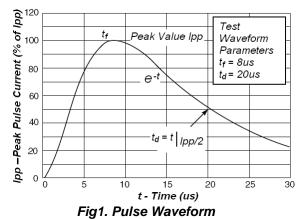


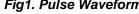
Electrical Characteristics Ratings at 25°C ambient temperature unless otherwise specified.

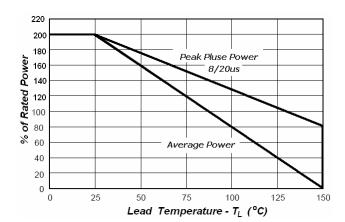
	V_{BR}					С	
Part Numbers	Min.	Тур.	Max.	I _T	V_{RWM}	I _R	Typ. (Note1)
	V	٧	V	mA	V	μΑ	pF
SESD3Z3V3C	5.3	5.5	5.8	1	3.3	3	50
SESD3Z5C	5.8	6.7	7.8	1	5.0	1	30
SESD3Z12C	13.3	14.5	15.7	1	12.0	1	16
SESD3Z15C	16.7	18.0	19.0	1	15.0	1	12
SESD3Z24C	25.1	27	30.0	1	24.0	1	8

Capacitance is measured at f=1MHz, $V_R=0V, T_A=25$ °C.

Typical Characteristics







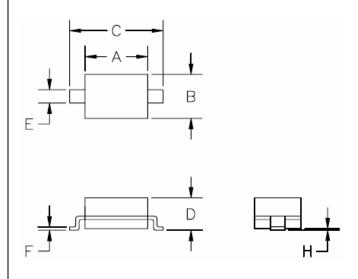
Application Note

Electrostatic discharge (ESD) is a major cause of failure in electronic systems. Transient Voltage Suppressors (TVS) are an ideal choice for ESD protection. They are capable of clamping the incoming transient to a low enough level such that damage to the protected semiconductor is prevented.

Surface mount TVS offers the best choice for minimal lead inductance. They serve as parallel protection elements, connected between the signal lines to ground. As the transient rises above the operating voltage of the device, the TVS becomes a low impedance path diverting the transient current to ground. The SESD3ZxxC is the ideal board evel protection of ESD sensitive semiconductor components.

The tiny SOD-323 package allows design flexibility in the design of high density boards where the space saving is at a premium. This enables to shorten the routing and contributes to hardening against ESD.

SOD-323 Mechanical Data



Dimensions							
Dim	Inc	hes	Mil				
	Min	Max	Min	Max			
Α	0.060	0.071	1.5	1.8			
В	0.045	0.054	1.2	1.4			
С	0.060	0.107	2.3	2.7			
D	-	0.043	-	1.1			
Е	0.012	0.016	0.3	0.4			
F	0.004	0.010	0.10	0.25			
Н	-	0.004	-	0.10			

CONTROLLILNG DIMENSION: MILLIMETERS

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