## NCE P-Channel Enhancement Mode Power MOSFET

### **Description**

The NCE01P30K uses advanced trench technology and design to provide excellent  $R_{DS(ON)}$  with low gate charge. It can be used in a wide variety of applications. It is ESD protested.

#### **General Features**

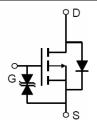
- V<sub>DS</sub> =-100V,I<sub>D</sub> =-30A
  - $R_{DS(ON)}$  <58m $\Omega$  @  $V_{GS}$ =-10V (Typ:44m $\Omega$ )
  - $R_{DS(ON)}$  <65m $\Omega$  @  $V_{GS}$ =-4.5V (Typ:48m $\Omega$ )
- Super high dense cell design
- Advanced trench process technology
- Reliable and rugged
- High density cell design for ultra low On-Resistance

### **Application**

Portable equipment and battery powered systems

100% UIS TESTED!

100% ΔVds TESTED!



Schematic diagram



Marking and pin assignment



TO-252-2L top view

## **Package Marking and Ordering Information**

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCE01P30K	NCE01P30K	TO-252-2L	-	-	-

## Absolute Maximum Ratings (T<sub>C</sub>=25 ℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V <sub>DS</sub>	-100	V
Gate-Source Voltage	V <sub>G</sub> S	±20	V
Drain Current-Continuous	I <sub>D</sub>	-30	А
Drain Current-Continuous(T <sub>C</sub> =100℃)	I <sub>D</sub> (100℃)	-21	Α
Pulsed Drain Current	I <sub>DM</sub>	-150	Α
Maximum Power Dissipation	P <sub>D</sub>	120	W
Derating factor		0.8	W/℃
Operating Junction and Storage Temperature Range	$T_{J},T_{STG}$	-55 To 175	$^{\circ}$

### **Thermal Characteristic**

Thermal Resistance, Junction-to-Case (Note 2)	R <sub>eJc</sub>	1.25	°C/W
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# NCE01P30K

## Electrical Characteristics (T<sub>C</sub>=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics	<u>.</u>			•		
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =-250μA	-100	-	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-100V,V <sub>GS</sub> =0V	-	_	1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V	-	_	±10	μA
On Characteristics (Note 3)	•		•	•		•
Gate Threshold Voltage	$V_{GS(th)}$	V <sub>DS</sub> =V <sub>GS</sub> ,I <sub>D</sub> =-250μA	-1.5	-1.9	-2.5	V
	Б	V <sub>GS</sub> =-10V, I <sub>D</sub> =-15A	-	44	58	mΩ
Drain-Source On-State Resistance	$R_{DS(ON)}$	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-15A	-	48	65	mΩ
Forward Transconductance	<b>g</b> Fs	V <sub>DS</sub> =-50V,I <sub>D</sub> =-10A	5	-	_	S
Dynamic Characteristics (Note4)				•		
Input Capacitance	C <sub>lss</sub>	)/ 50)/)/ 0)/	-	3810	_	PF
Output Capacitance	C <sub>oss</sub>	$V_{DS}$ =-50V, $V_{GS}$ =0V, F=1.0MHz	-	93	_	PF
Reverse Transfer Capacitance	C <sub>rss</sub>	F=1.UIVIDZ	-	91	-	PF
Switching Characteristics (Note 4)				•		
Turn-on Delay Time	t <sub>d(on)</sub>		-	17	-	nS
Turn-on Rise Time	t <sub>r</sub>	V <sub>DD</sub> =-50V,I <sub>D</sub> =-15A	-	80	-	nS
Turn-Off Delay Time	$t_{d(off)}$	$V_{GS}$ =-10V, $R_{GEN}$ =9.1 $\Omega$	-	45	_	nS
Turn-Off Fall Time	t <sub>f</sub>		-	65	_	nS
Total Gate Charge	Qg	V - 50VI - 45A	-	136	_	nC
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ =-50V, $I_{D}$ =-15A, $V_{GS}$ =-10V	-	22	_	nC
Gate-Drain Charge	Q <sub>gd</sub>	V <sub>GS</sub> =-10V	-	26	-	nC
Drain-Source Diode Characteristics	<u>.</u>			•		
Diode Forward Voltage (Note 3)	$V_{SD}$	V <sub>GS</sub> =0V,I <sub>S</sub> =-10A	-	-	-1.2	V
Diode Forward Current (Note 2)	Is	-	-	-	-30	Α
Reverse Recovery Time	t <sub>rr</sub>	TJ = 25°C, IF =-15A	-	90	_	nS
Reverse Recovery Charge	Qrr	di/dt = 100A/µs <sup>(Note3)</sup>	-	70	-	nC
Forward Turn-On Time	t <sub>on</sub>	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)				

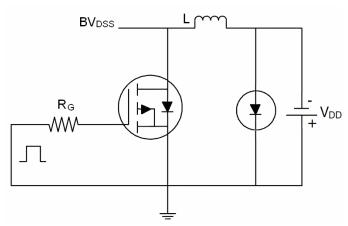
## Notes:

- $\textbf{1.} \ \textbf{Repetitive Rating: Pulse width limited by maximum junction temperature.}$
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- 3. Pulse Test: Pulse Width  $\leq$  300 $\mu$ s, Duty Cycle  $\leq$  2%.
- 4. Guaranteed by design, not subject to production

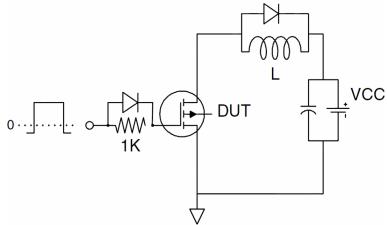


## **Test Circuit**

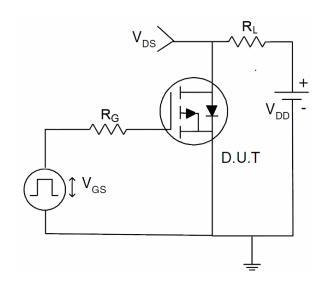
# 1) E<sub>AS</sub> Test Circuit



## 2) Gate Charge Test Circuit



## 3) Switch Time Test Circuit

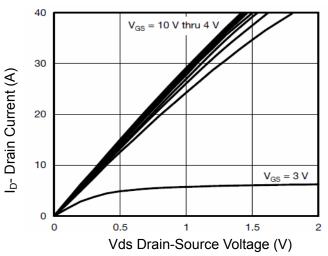


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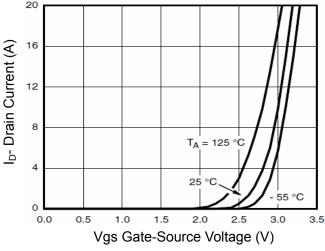


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## **Typical Electrical and Thermal Characteristics (Curves)**



**Figure 1 Output Characteristics** 



**Figure 2 Transfer Characteristics** 

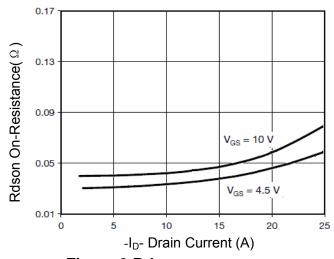


Figure 3 Rdson- Drain Current

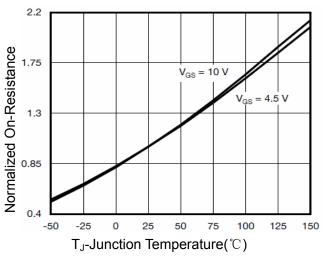


Figure 4 Rdson-JunctionTemperature

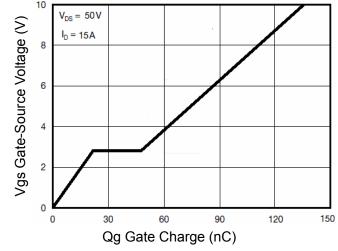


Figure 5 Gate Charge

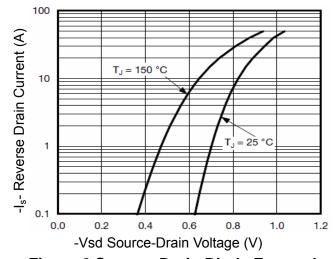
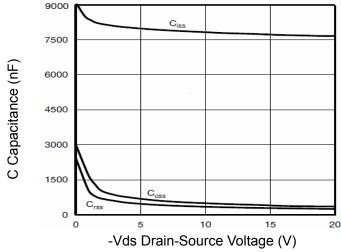


Figure 6 Source- Drain Diode Forward



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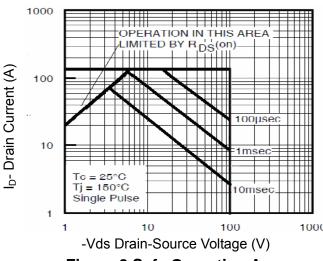


30 D- Drain Current (A) 20 10 100 150  $T_C$  Case Temperature( $^{\circ}C$ )

40

Figure 7 Capacitance vs Vds

**Figure 9 Drain Current vs Case Temperature** 



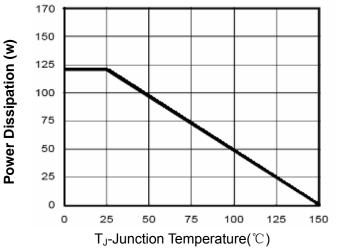
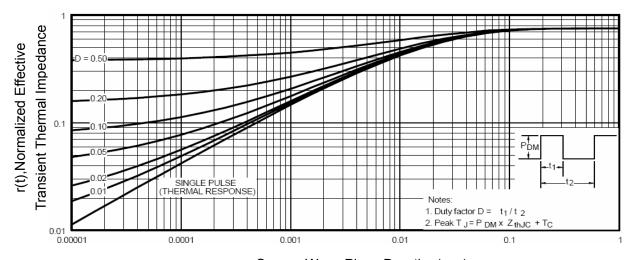


Figure 8 Safe Operation Area

Figure 10 Power De-rating



Square Wave Pluse Duration(sec)

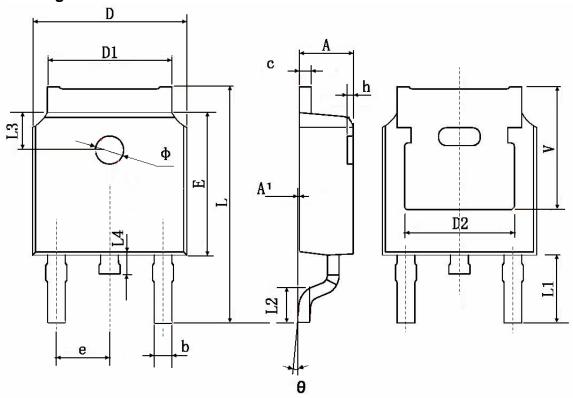
**Figure 11 Normalized Maximum Transient Thermal Impedance** 

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# NCE01P30K

# **TO-252 Package Information**



Symbol	Dimensions	In Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
Α	2.200	2.400	0.087	0.094	
A1	0.000	0.127	0.000	0.005	
b	0.660	0.860	0.026	0.034	
С	0.460	0.580	0.018	0.023	
D	6.500	6.700	0.256	0.264	
D1	5.100	5.460	0.201	0.215	
D2	4.83	OTYP.	0.190	TYP.	
E	6.000	6.200	0.236	0.244	
е	2.186	2.386	0.086	0.094	
L	9.800	10.400	0.386	0.409	
L1	2.90	0 TYP. 0.114 TY		ſP.	
L2	1.400	1.700	0.055	0.067	
L3	1.600 TYP.		0.063	TYP.	
L4	0.600	1.000	0.024	0.039	
Ф	1.100	1.300	0.043	0.051	
θ	0°	8°	0°	8°	
h	0.000	0.300	0.000	0.012	
V	5.35	0 TYP.	0.211 TYP.		



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# NCE01P30K

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